	Departmer	nt of Electrical En Midterm Exam Date: 25/04/2020	gineering	
		<b>Course Details</b>		
Course Instruct	<b>Fitle:</b> Electronic Devices and         or:	l Circuits	Module: Total Marks:	30
	<u>S</u>	tudent Details		
Name:			Student ID:	
Student	Signature:			
Q1.	For the circuit given in figure a) What type of circuit is b) What is the total peak c) Find the peak voltage d) What is the peak current e) What minimum PIV r F F $V_{in}$ $V$ -50V	re 1, answer and solv s this? (1) secondary voltage? (1 across each half of the ent through each diodes ating must the diodes	<i>ve</i> following problem 1) e secondary. (1) e? (2) have? (2) $D_1$ $V_{out}$ $V_{out}$	s. $\frac{\text{Marks } 07}{\text{CLO } 02}$

1N4001

Marks 05 CLO 02

Figure 1

Determine the ripple factor for the filtered bridge rectifier with a load as indicated

in Figure 2

Q2.

0



<ul> <li>d) What component in a clamping circuit effectively acts as a battery? (1)</li> <li>e) When a 60 Hz sinusoidal voltage is applied to the input of a half-wave matifier what is the output frequency? (1)</li> </ul>
f) If the load resistance connected to a filtered power supply is decreased,
what happens to the ripple voltage? (1)
<ul><li>g) Discuss how diode limiters and diode clampers differ in terms of their function. (3)</li></ul>