

Department of Electrical Engineering

Assignment

Date: 20/08/2020

Course Details

Course Title: Microprocessor Theory and Interfacing

Module: 4rth

Instructor: \_\_\_\_\_

Total Marks: 30

Student Details

Name: \_\_\_\_\_

Student ID: \_\_\_\_\_

Q1	(a)	Explain Subroutine with proper examples.	Marks 05
			CLO 1
	(b)	Briefly explain types of segments. Back your answer with suitable examples.	Marks 05
			CLO 1
Q2	(a)	Draw the Microcomputer Bus Architecture and briefly explain each part.	Marks 10
			CLO 1
	(b)	Write the function of Bus Interface Unit (BIU) and Execution Unit (EU). Back your answer with suitable examples.	
Q3	(a)	Explain Instruction pipelining with suitable example. Also explain instruction cycle.	Marks 05
			CLO 2
	(b)	Explain address mapping with suitable example.	Marks 05
			CLO 2

☺Good Luck☺