

Assignment

Week #06

Signal & System

① Draw the block diagram of the following DTS systems.

$$(i) \quad y[n] = 0.31x[n] + 0.72x[n-1] + 0.99y[n-1]$$

$$(ii) \quad y[n] = 0.91x[n] + 0.5x[n-1] - 0.2y[n-1]$$

② Explain Memory Systems and memory less Systems in Signal & Systems

also describe ^{the logic} whether unit delay

and modulator is memory or

memory less system.