|  |  |
| --- | --- |
| Digital Logic Design (Theory) | EEE211 |

|  |
| --- |
| Combinational Logic, AND-OR Logic, Exclusive OR-Logic,Exclusiv, Exclusive NOR-Logic. |
| Implementing Combinational Logic, From Boolean expression to Logic circuit, From a truth table to a  Logic circuit. |
| The universal Property of NAND and NOR Gates , NAND gate as Universal Logic element, NOR as  Universal Logic element. |
| Designing Combinational Logic, Functions of Combinational Logic, Half Adders, Full  Adders,HalfSubtractor, Full Subtractor |
| Decoders, BCD to 7-segment 2\*4 and 3\*8 decoder, Encoders. |
| Multiplexer, Cascading Multiplexer, Demultiplexer |

Cover the above mentioned topics for Final Exam. Follow the book **Digital\_Fundamentals\_11ed\_by\_Thomas\_L.Floyd**