

Course : Electronic device and circuit

: Summer semester

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Signature : 

Q2:- Determine the ripple factor for filtered bridge with a load as indicated in figure - 2

Sol:- the transformer turns ratio  $n=0.5$   
the peak primary voltage

$$V_p(\text{pri}) = 1.414 V_{\text{rms}} \\ = 1.414 (120) \\ = 170V$$

the peak secondary voltage is

$$V_p(\text{sec}) = n V_p(\text{pri}) \\ = 0.5 (170) = 85V$$

Unfiltered peak full-wave rectified voltage is

$$V_p(\text{rect}) = V_p(\text{sec}) - 1.4 \\ = 85 - 1.4$$

$$V_p(\text{rect}) = 83.6V$$

Frequency of a full-wave rectified voltage is 120 Hz. The approximate peak to peak ripple voltage at the output is

$$V_{r(pp)} = \left( \frac{1}{fRLC} \right) V_p (\text{rect})$$

$$= \left( \frac{1}{(120 \text{ Hz})(3300 \Omega)(100 \times 10^{-6} \text{ F})} \right) \cdot 83.6 \text{ V}$$

$$V_{r(pp)} = 2.111 \text{ V}$$

Approximate dc value of the output voltage is determined as follows:

$$V_{DC} = \left( 1 - \frac{1}{2fRLC} \right) V_p (\text{rect})$$

$$V_{DC} = \left( 1 - \frac{1}{(240)(3300)(100 \times 10^{-6})} \right) (83.6)$$

$$V_{DC} = \left( 1 - \frac{1}{79.2} \right) 83.6$$

$$V_{DC} = (1 - 0.0126) 83.6$$

$$= (0.9874) 83.6$$

$$V_{DC} = 82.5$$

Resulting ripple factor is

$$V_{r(pp)} = \frac{2.111}{82.5}$$

$$= 0.025$$

Q 5<sup>th</sup> :-

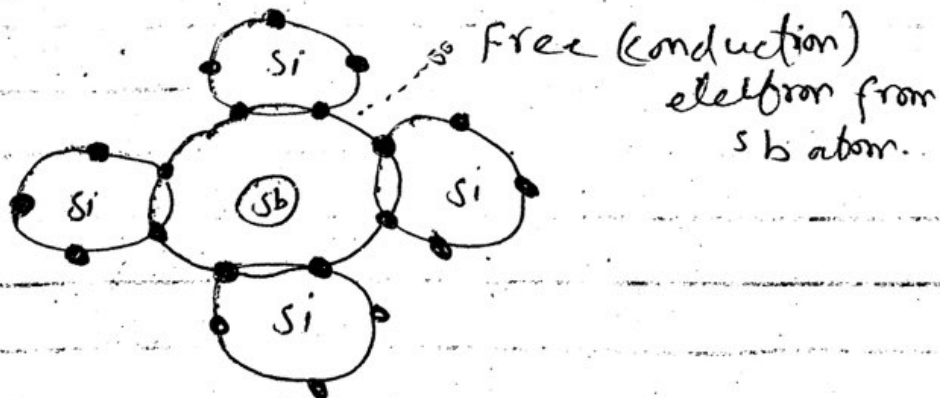
Part (B).

How are n-type and p-type Semiconductors formed?

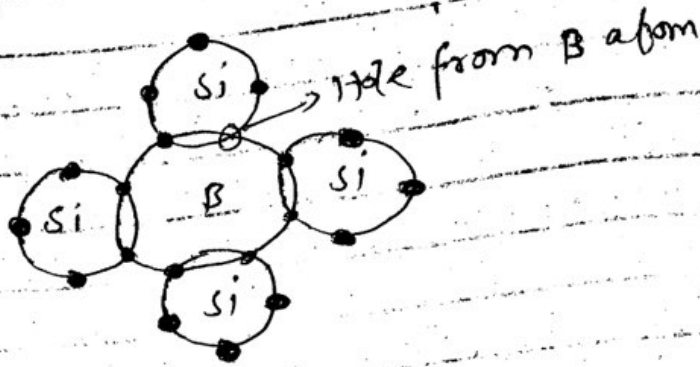
N-Type and P-Type Semiconductors:-

The Doping of Semiconductors pentavalent impurities Impurity atoms with 5 valence electrons produce n-type semiconductors by contributing extra electrons. Trivalent impurities Impurity atoms with 3 valence electrons produce p-type semiconductors by producing a "hole" or electron deficiency.

N-type Semiconductors:-



## P type Semiconductor:-



### Part (a):-

#### Power supply Filter:-

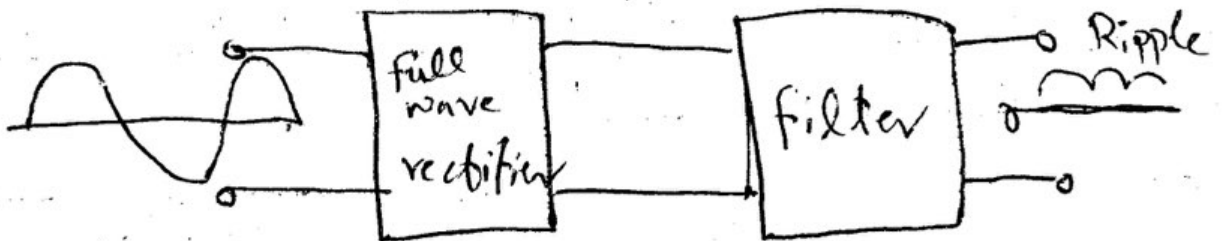
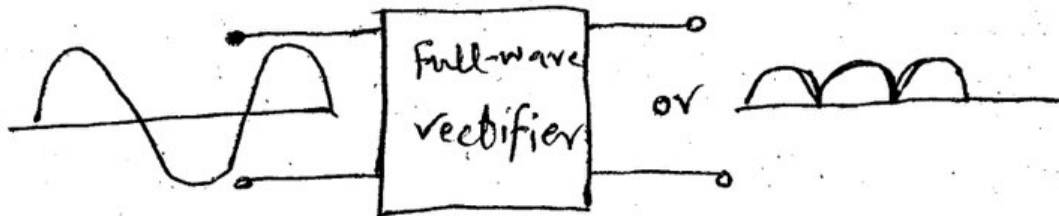
\* A power supply filter ideally eliminates the fluctuations in the output voltage of a half-wave or full-wave rectifier and produces a constant-level dc voltage.

\* The 60 Hz pulsating dc output of a half-wave rectifier or the 120 Hz pulsating output of a full-wave rectifier must be filtered to reduce the large voltage variations.

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\* the output of a filter is nearly smooth dc output voltage.

\* the small amount of fluctuation in the filter output voltage is called ripple.



Part C :-diode Limiter :-

The diode clipper, also known as a diode limiter is a wave shaping circuit that takes an input waveform and clips or cuts off its top half, bottom half or both halves together. This clipping of the input signal produces an output waveform that resembles a flattened version of the input.

\* Positive Limiter and negative Limiter :-

The Series-Negative Limiter limits the negative portion of the input pulse. The difference between a Series-negative limiter and a Series-positive limiter is that the diode is

reversed in the negative Limiter. In the parallel positive Limiter the positive portion of the input signal is limited when the diode conducts.

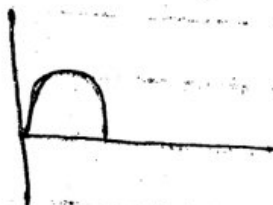
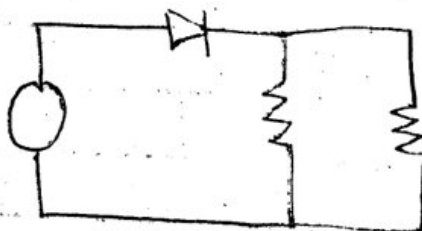
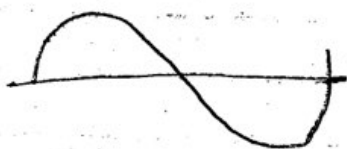
Part (d) :-

As  $V_{in}$  becomes negative, the capacitor acts as a battery of the same voltage of  $V_{in}$ . The voltage source and the capacitor counteract each other, resulting in a net voltage of zero as seen by the load.



Part (e) :-

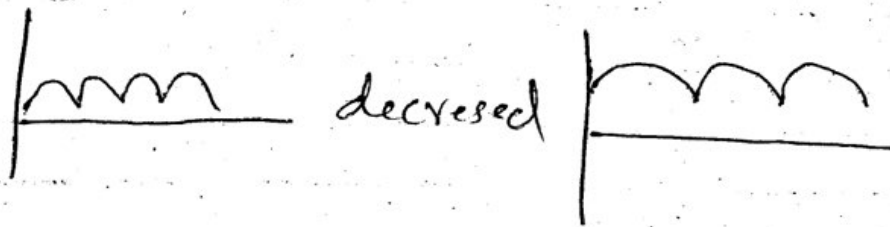
The output frequency of  
half wave rectifier  
is equal as  
input as this means  
one complete cycle  
output also complete  
one cycle



$$f_{in} = f_{out}$$

Part (f) :-

if the load resistance to filtered power supply decreases the charging and discharging time of capacitor also decreases. So it takes longer time to charge and discharge.

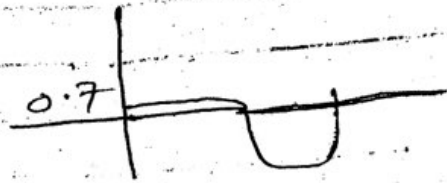
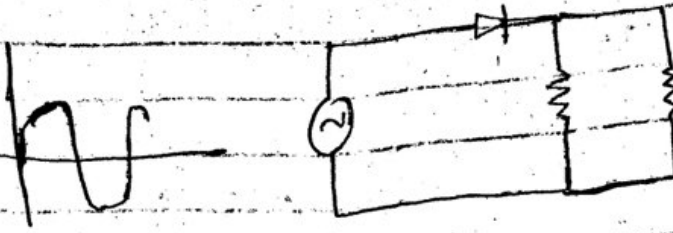


Part (g) :-

Difference between diode limiter & clamper :-

Diode Limiter :-

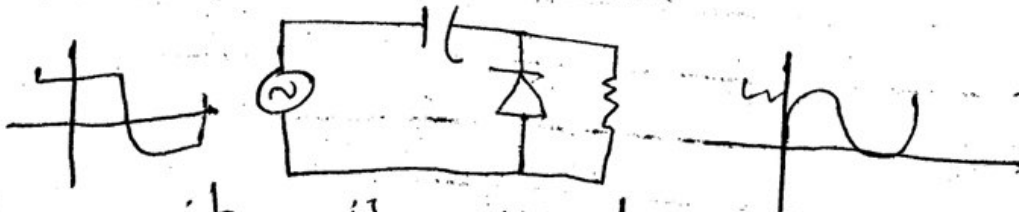
It is a circuit which is used to clip a sinusoidal wave form at a desired level.



it is used as a voltage regulator to keep the voltage at a desired level

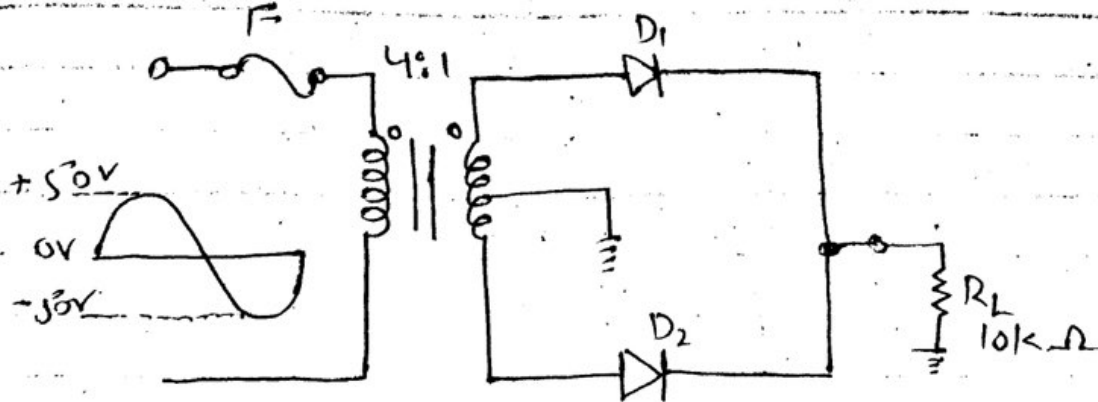
### Diode clamper:

Diode clamper is used to add a DC offset to a +ive or -ive half cycle of a sinusoidal waveform. it clamps the whole signal at a desired level.



it is used as a voltage doubler.

Q19-



Part (a)

What type of circuit is this?

Ans:- The given figure circuit is center tapped full-wave rectifier transformer circuit.

Part (B)

What is the total peak secondary voltage?

Sol:- The transformer turns ratio,  $n = 0.25$   
The total peak secondary voltage is.

$$V_p(\text{sec}) = nV_p(\text{pri}) = 0.25(50\text{V}) = \boxed{12.5\text{V}}$$

Part (c):

Find the peak voltage across each half of the secondary

Ans: - there is 6.25V peak across each half of the secondary with respect to ground.  $6.25 - 0.7$  which diode drop.

Part (d) what is the peak current through each diode?

Ans: - the peak current through each diode is

$$I_F = \frac{V_p(\text{sec}) - 0.7}{R_L}$$

$$= \frac{5.55\text{V}}{10\text{k}\Omega}$$

$$= 0.000555$$

$$= 0.555\text{mA}$$

Part e.g-

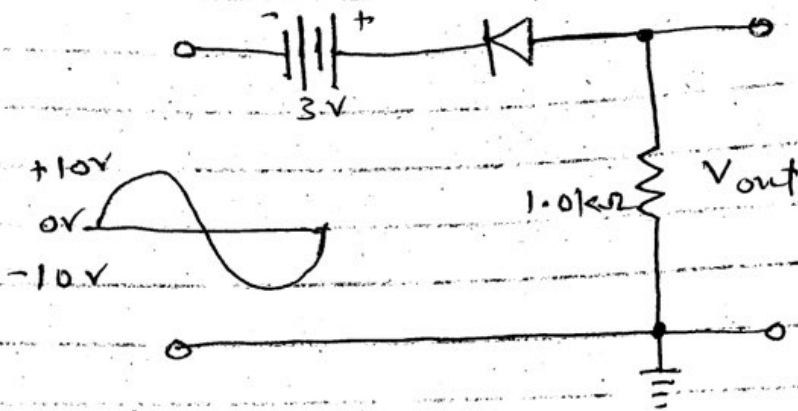
What minimum PIV rating must the diode have?

Ans:- Each diode must have a minimum PIV rating of

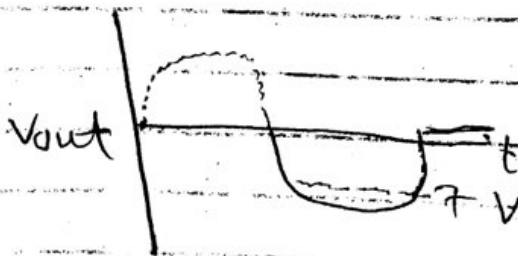
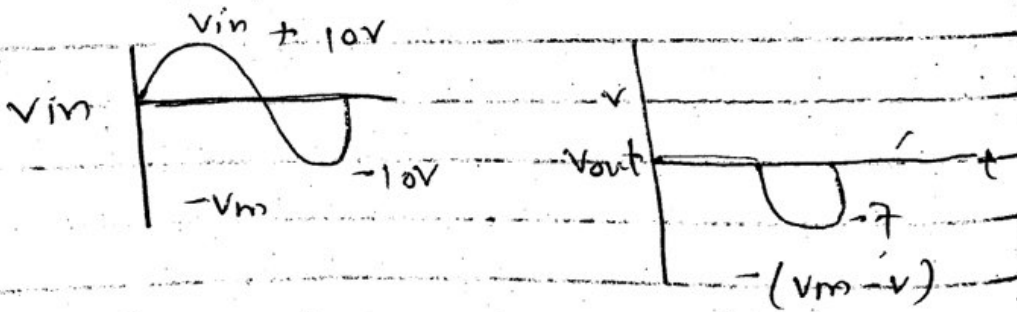
$$PIV = 2V_p(\text{out}) + 0.7 = 2(5.55V) + 0.7$$

$$= 11.8V$$

Q3: Determine the output voltage waveform for the circuit given in fig-3



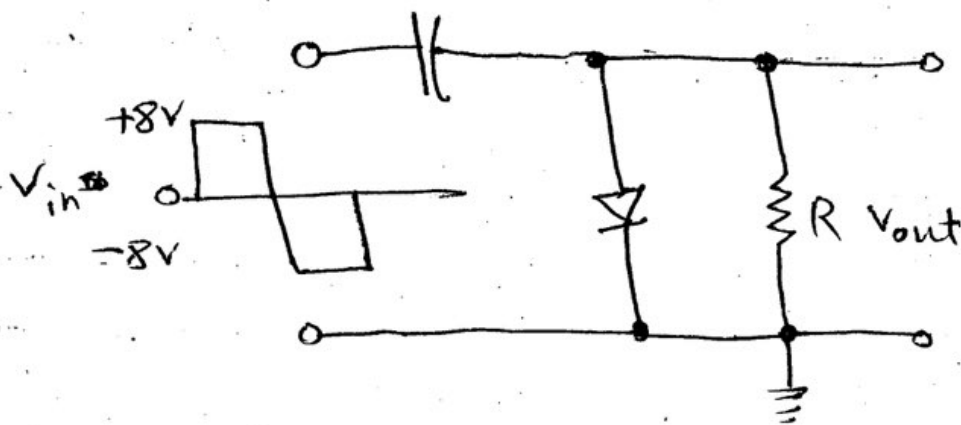
output waveform of clipper circuit.





Q48-

Determine the output voltage waveform for the circuit given in figure-4. Assume the RC time constant is much greater than the period of the input.



Solution:-

if we take  
 $(\tau_{\text{discharge}} > 50 T/2)$  thus

we obtain condition for the clamping operation.

$$T_{\text{discharge}} \geq 5T$$



$$\text{So, } V_o = V_i - V + V_D(\text{or})$$

∴ this is period of input signal.  $V_i$

