

Department of Electrical Engineering
Sessional Assignment
Date: 05/05/2020

Course Details

Course Title: Signals & Systems **Module:** 04
Instructor: _____ **Total Marks:** 20

Student Details

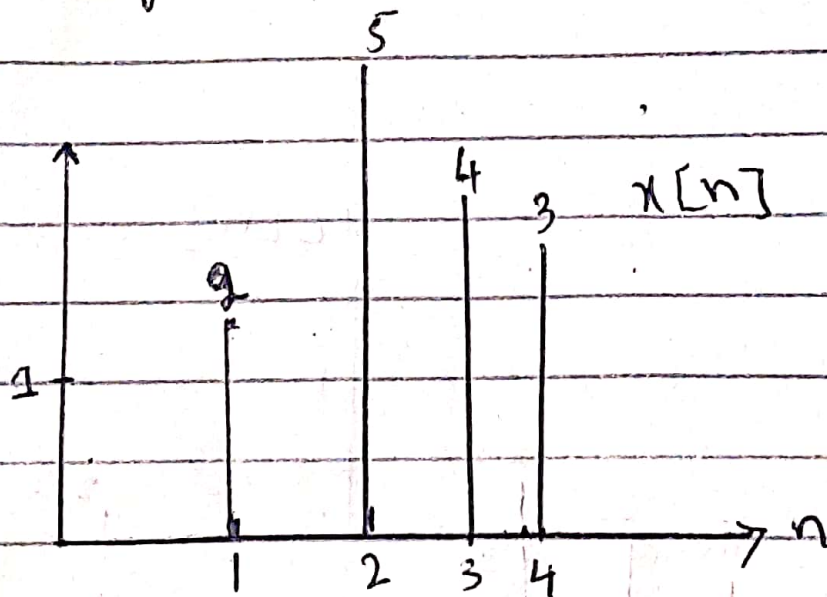
Name: _____ **Student ID:** _____

Q1.	<p>Evaluate the even and odd components for the given function.</p> <div style="text-align: center;"></div>	Marks 05 CLO 1
Q2.	<p>Calculate the inverse Laplace transform of the given equation.</p>	Marks 07 CLO 3
Q3.	<p>i. Discuss the procedure of converting an analog signal into a digital one. ii. Suppose an analog signal has a highest frequency of 60Hz. Outline the steps that will ensure that no aliasing occurs.</p>	Marks 02+02 CLO 2
Q4.	<p>Show that: $x[n] * [h_1[n] * h_2[n]] = [x[n] * h_1[n]] * h_2[n]$</p>	Marks 04 CLO 2

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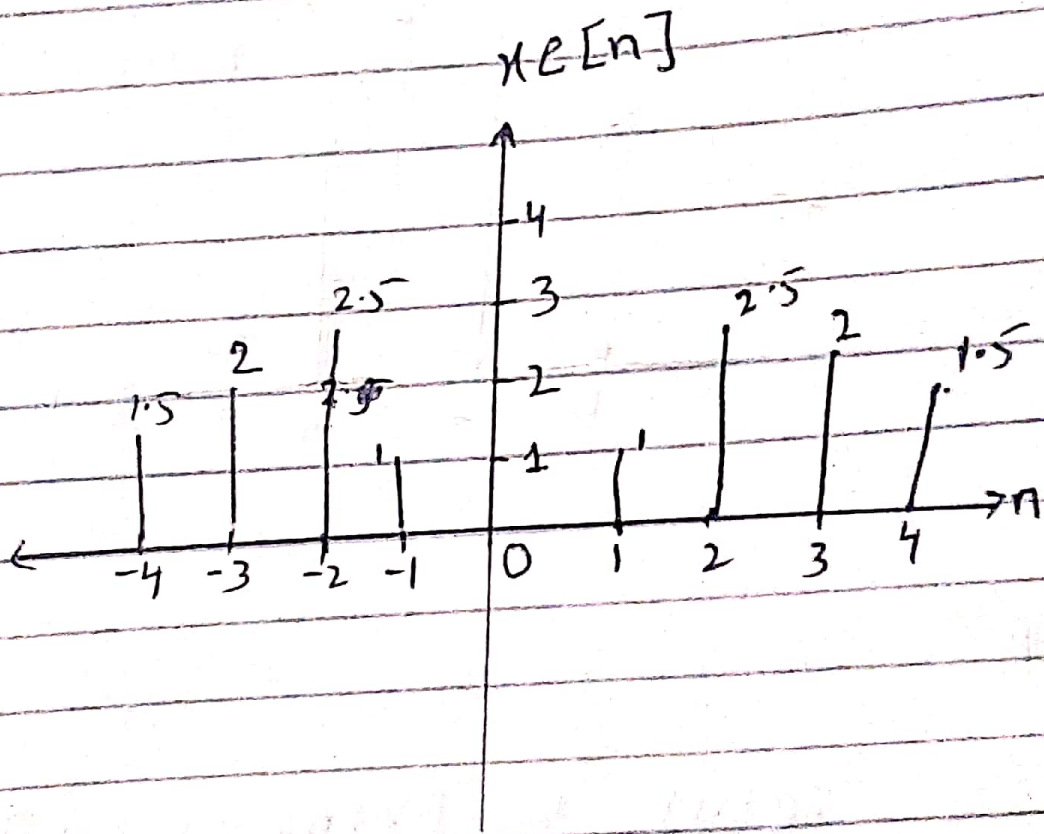
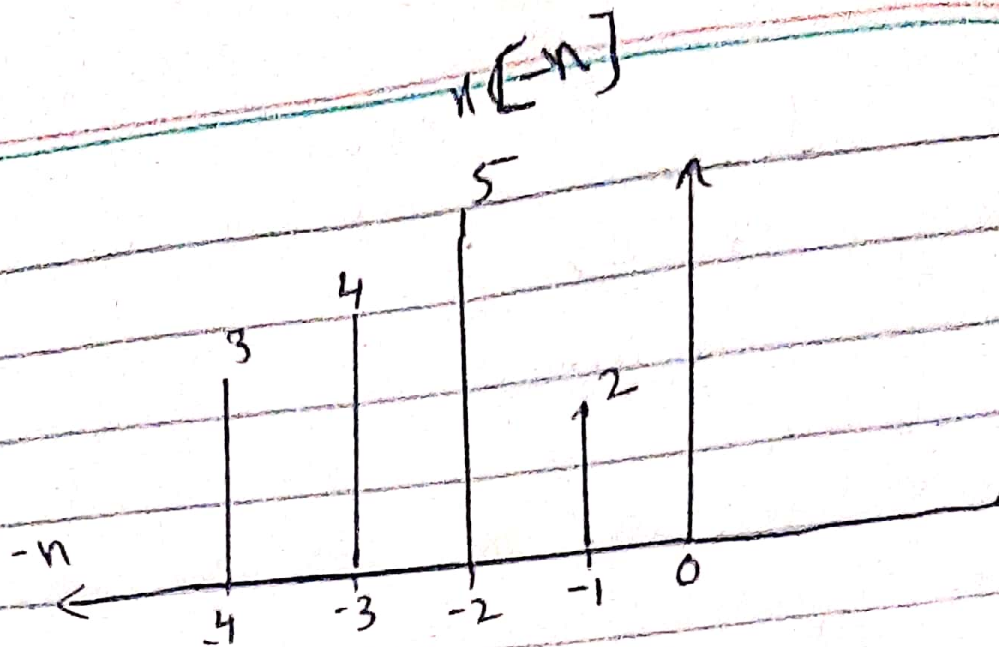
Q1: Evaluate the even and odd ~~na~~ components for the given function.



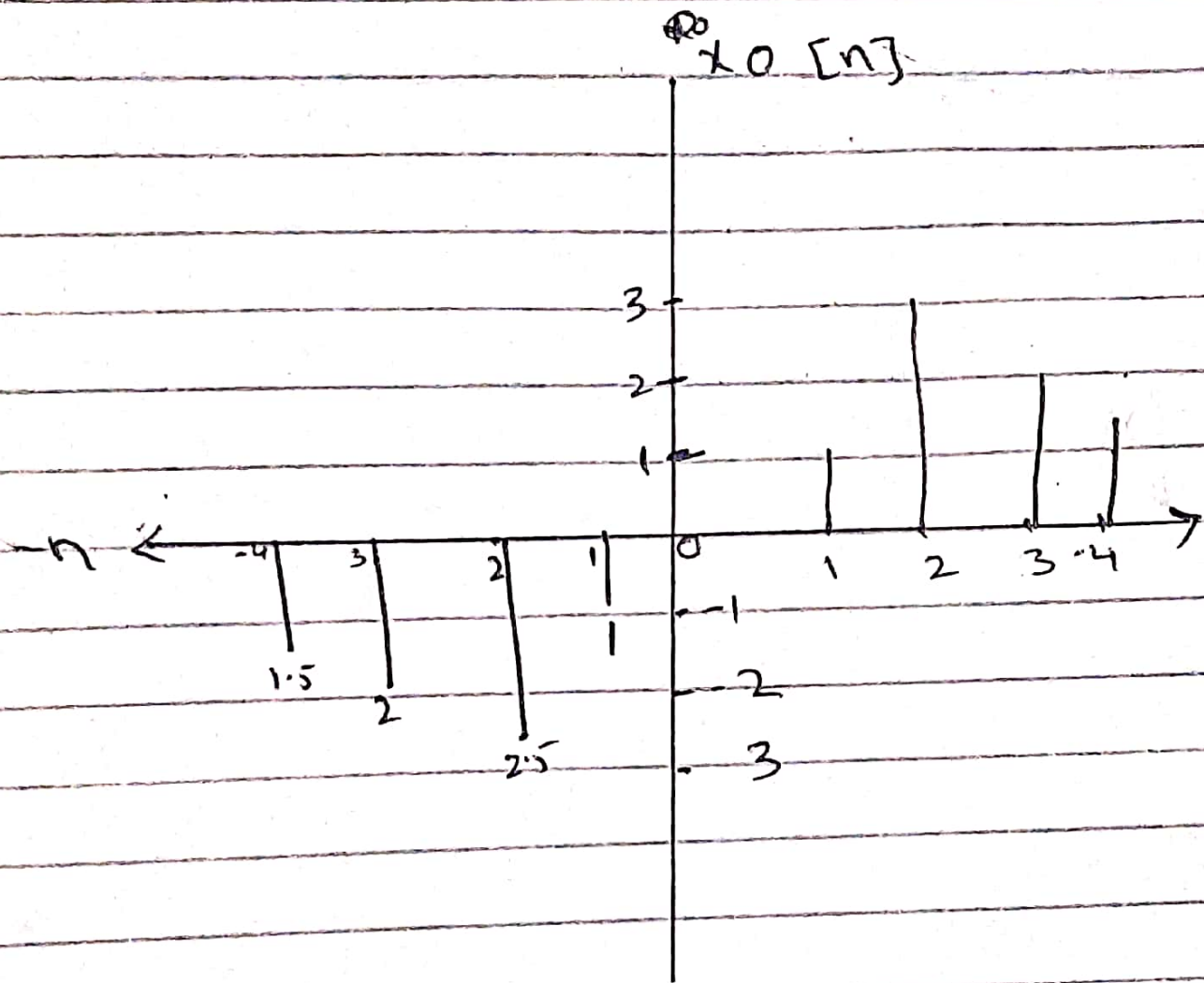
Ans:

$$x_e[n] = \frac{1}{2} [x[n] + x[-n]]$$

$$x_o[n] = \frac{1}{2} [x[n] - x[-n]]$$



even component of the signal.



odd component of the signal.

Q2. Calculate the inverse Laplace transform of a given equation.

$$y(s) = \frac{s+4}{s^2+4s-12}$$

Sol:-

$$y(s) = \frac{s+4}{s^2+4s-12}$$

$$y = \mathcal{L}^{-1} \left[\frac{s+4}{s^2+4s-12} \right]$$

By partial fraction

$$\mathcal{L}^{-1} \left[\frac{-\frac{2}{8}}{(s+6)} \right] + \mathcal{L}^{-1} \left[\frac{\frac{6}{8}}{(s-2)} \right]$$

$$= \left[\frac{1}{4} e^{-6t} + \frac{3}{4} e^{2t} \right]$$

3(I) Discuss the procedure of converting an ~~also~~ analog signal into a digital one.

15. Analog signal is converted to digital signal using a two step process. The device used to do this is called as ADC (Analog to Digital ~~to~~ Converter).

Step 1.

Sampling converts a continuous time continuous amplitude (real valued) signal to discrete time continuous amplitude (~~still~~) (still real valued) signal.

Remember only time axis is ~~discretized~~ discretized and not the amplitude axis.

Step 2.

Quantization converts the discrete time continuous amplitude (~~still~~) signal to discrete time and discrete valued (from a set of finite values, so that it can be represented by finite bits and can be stored on a computer).

Q3 (II).

Suppose an analog signal has highest frequency of f_m . Outline the steps that will ensure that no aliasing occurs.

Ans. Aliasing

The sampling rate for an analog signal must be at least two times as high as the highest frequency in the analog signal in order to avoid aliasing.

Conversely for a fixed sampling rate, the highest frequency in the analog signal can be no higher than a half of the sampling rate:

Aliasing is generally avoided by applying low pass filters or Anti-Aliasing filters (AAF).

to the input signal before sampling, when converting a signal from higher to a lower sampling rate.

04
show that:

$$a) \quad x[n] * [h_1[n] * h_2[n]] = [x[n] * h_1[n]] * h_2[n]$$

Sol--

Proof: Let

$$a[n] = h_1[n] * h_2[n] = \sum_{k=-\infty}^{\infty} h_1[k] h_2[n-k]$$

$$b[n] = x[n] * h_1[n] = \sum_{l=-\infty}^{\infty} x[l] h_1[n-l]$$

$$x[n] * a[n] = \sum_{l=-\infty}^{\infty} x[l] a[n-l]$$

$$= \sum_{l=-\infty}^{\infty} x[l] \left(\sum_{k=-\infty}^{\infty} h_1[k] h_2[n-l-k] \right)$$

$$b[n] * h_2[n] = \sum_{k=-\infty}^{\infty} b[k] h_2[n-k]$$

$$= \sum_{k=-\infty}^{\infty} \left(\sum_{l=-\infty}^{\infty} x[l] h_1[k-l] \right) h_2[n-k]$$

$$= \sum_{l=-\infty}^{\infty} x[l] \left(\sum_{k=-\infty}^{\infty} h_1[k-l] h_2[n-k] \right)$$

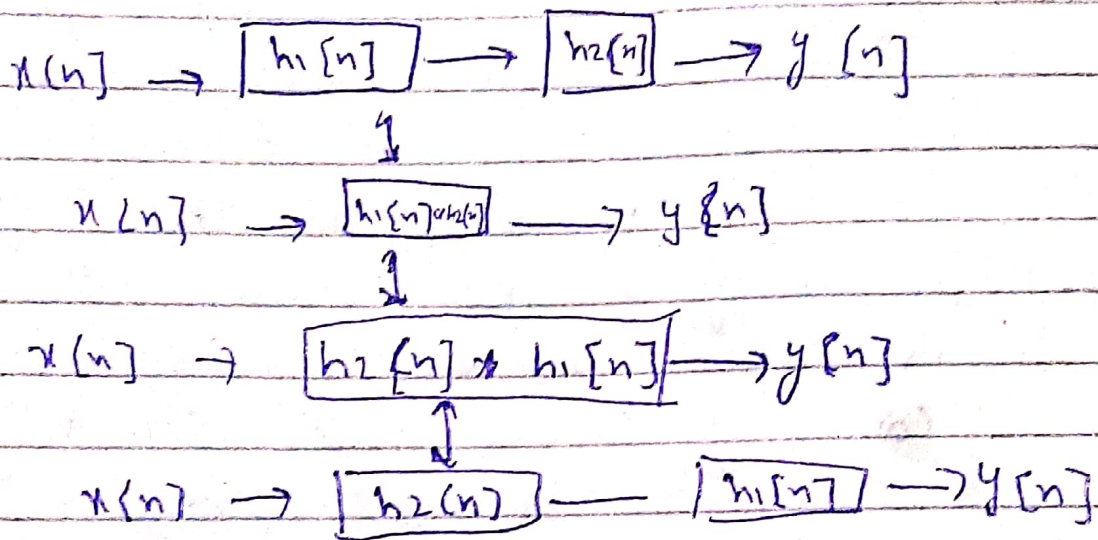
Let, $k' = k-l$. we have $k = l + k'$ and hence

$$b[n] * h_2[n] = \sum_{l=-\infty}^{\infty} x[l] \left(\sum_{k'=-\infty}^{\infty} h_1[k'] h_2[n-l-k'] \right)$$

Combining properties 2 & 4 we have.

$$\begin{aligned}
 & (x[n] * h_1[n]) * h_2[n] * h_2[n] * h_1[n] \\
 & = x[n] * (h_1[n] * h_2[n]) = x[n] * (h_2[n] * h_1[n]) \\
 & * h_1[n] = (x[n] * h_2[n]) * h_1[n]
 \end{aligned}$$

Interpretation:



Observe that

$$\begin{aligned}
 x[n] &= -\delta[n+2] + \delta[n+1] + \delta[n] + \delta[n-2] \\
 h[n] &= \delta[n] + \delta[n-1].
 \end{aligned}$$

Thus,

$$\begin{aligned}
 x[n] * h[n] &= (-\delta[n+2] + \delta[n+1] + \delta[n] + \delta[n-2] + \delta[n-3]) * (\delta[n] + \delta[n-1]) \\
 &= -\delta[n+2] + \delta[n+1] + \delta[n] + \delta[n-2] + (-\delta[n+1] + \delta[n] + \delta[n-1] + \delta[n-3]) \\
 &= -\delta[n+2] + 2\delta[n] + \delta[n-1] + \delta[n-2] + \delta[n-3].
 \end{aligned}$$