Department of Electrical Engineering Assignment Date: 27/06/2020 <u>Course Details</u>					
Course Title: Instructor:	Power Electronics Engr. Shayan Tariq Jan	Module: Total Marks:	<u>8th</u> 50		
Student Details					
Name:	FAWAD AHMAD	Student ID:	13204		

Note: Plagiarism of more than 20% will result in negative marking. Similar answers of students will result in cancellation of the answer for all parties.

Q1.	Rectifiers are common circuits used in most electronic devices. There are multiple types of rectifiers used now a days. Explain in detail what are the similarities and differences between:	CLO 2
	1. $1 - \varphi$ Uncontrolled Half Wave Rectifier and Full Wave Bridge Rectifier 2. $1 - \varphi$ Uncontrolled Rectifier and Controlled Rectifiers (Bridge Rectifier).	Marks 10
Q2.	A AC voltage of $Vm = (Last 2 \text{ digits of ID}) V$ has to be delivered to a Resistive DC load of $R = (First 2 \text{ digits of ID})$ ohms.	CLO 2
	The load and source are connected through 2 types of $1 - \varphi$ Uncontrolled rectifiers (Half Wave and Full Wave Bridge) and data is collected. Find the following for both rectifiers:	Marks 10
	1. Vdc 2. Idc 3. Vrms	
	4. Irms5. Which rectifier do you think is better and why.	
Q3.	The Buck chopper is a type of DC-DC converter. Explain in detail the principals and working of Buck converter when the switch is open and closed.	CLO 3
	The buck converter is connected to a DC source voltage of Vin = 50V. The duty cycle is $D = (Last 2 \text{ digits of ID}) \%$, load of $R = (First 2 \text{ digits of ID})$ ohms and switching frequency of 20kHz. What will be the	Marks 10
	1. Vout	
	2. Iout	
	3. lin	
	4. Inductor (L)	

Q4	The Boost chopper is a type of DC-DC converter. Explain in detail the principals and working of Boost converter when the switch is open and closed.	
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	2. Iout	
	3. lin 4. Inductor (I.)	
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Q3	principals and working of Buck converter when the switch is open and closed.	CLO 5
	The Buck-Boost converter is connected to a DC source voltage of Vin = 50V. The Output voltage Vout = (Last 2 digits of ID) %, load of R = (First 2 digits of ID) ohms and switching frequency of 20kHz. What will be the	Marks 10
	1. Duty Cycle (D)	
	2. Iout	
	3. Vin	
	4. Inductor (L)	

ANSWER:

φ Half – Wave uncontrolled Rectifier:

It uses a $3-\phi$ transformer with primary in delta and secondary in star connection.

D1, D2 and D3 have common connected cathode to common load R and all diodes are

oriented in different phases and therefore called as Common - Cathode Circuit.



• The rectifier element connected to the line at the highest +ve instantaneous voltage can only conduct and pulsates between V max and 0.5 V max.

- It is called $3 \varphi 3$ pulse rectifier as the o/p is repeated thrice in every cycle of V s.
- The ripple frequency (f r) of the o/p voltage is

$$f_r = nf_s;$$
 $n = no. of diodes, f_s = AC supply freq.$

• The ON diode connects its most +ve source terminal to the other two diode cathodes

keeping the other diodes OFF.

• The sudden switchover from one diode to another is called "commutation".

- Each diode conducts for 120 ° intervals.
- Delta connection provides path for triplen (odd multiples of the 3rd harmonic) harmonic

currents stabilizing the voltage on star secondary.

$$V_{0}(Av_{g}) = \frac{n}{\pi} Y_{m} Sin\left(\frac{\pi}{n}\right) = 0.8\partial 7 * V_{m} = 0.477 * V_{L} \cdot h = 3$$

$$I_{0}(Av_{g}) = \frac{n}{\pi} I_{Sin}\left(\frac{\pi}{n}\right) = 0.8\partial 7 * I_{m} \cdot I_{0}(Av_{g}) = I_{0}(Av_{g})_{n}$$

$$Rms \quad Value \quad of \quad Lovel \quad Current$$

$$I_{0}(Rms) = I_{m} \left[\frac{1}{\partial \pi}\left(\frac{\pi}{n} + \frac{1}{2} - Sin\frac{\partial \pi}{n}\right)^{l_{2}}\right] = 1$$

Ripple Factor =
$$\frac{\sqrt{2}}{n^2 - 1} = \frac{\sqrt{2}}{3^2 - 1} = 0.177$$

Form Factor = $\sqrt{n} = \sqrt{3} = 1.732$



φ Full Wave uncontrolled Bridge Rectifier:

• On the positive half cycle of transformer secondary supply voltage, diodes D1 and D2

conduct, supplying this voltage to the load.

- On the negative half cycle of supply voltage, diodes D3 and D4 conduct supplying this voltage to the load.
- It can be seen from the waveforms that the peak inverse voltage of the diodes is only V m
- The average output voltage is the same as that for the centre tapped transformer full-

wave rectifier.

Peak Repetitive Diode Current I = Vm AV. output Vallage V o = 2Vm Rms output Value V Yms = V2Vi AV. Diode Cussent I $D = \frac{1}{2\pi} I Sin w t d(wt) = \frac{Im}{\pi}$ Rms output Valtage Vams = JaVs Rms Diode Current IDrms = $\int \frac{1}{2\pi} \frac{\pi}{\delta} Im^2 sin^2 totd (wt)$ $= \frac{I_m}{2}$



PART (B):

Phase Controlled Rectifiers:

• Unlike diode, an SCR does not become conducting immediately after its voltage has

become positive.

- It requires triggering by means of pulse at the gate.
- So it is possible to make the thyristor conduct at any point on the half wave which applies positive voltage to its anode.
- Thus the output voltage is controlled.

Phase Controlled Rectifiers – Applications

• Steel rolling mills, paper mills, textile mills where controlling of DC motor speed is

necessary.

- Electric traction.
- High voltage DC transmissions.
- Electromagnet power supplies.

<u>φ Uncontrolled Rectifier:</u>

 $-\phi$ Rectifier offers the following advantages:

- 1. Higher o/p voltage for a given i/p voltage.
- 2. Lower amplitude ripples i.e. output voltage is smoother.
- 3. Higher frequency ripples simplifying filtering.
- 4. Higher overall efficiency.

φ Uncontrolled Rectifier: Classification:

They are generally of four types.

- 1. 3ϕ Half wave rectifier.
- 2. 3ϕ Mid point 6 Pulse rectifier.
- 3. 3φ Bridge rectifier.
- 4ϕ 12 Pulse rectifier.

QUESTION 2:

Solution:

Given data Qa:-Vm= 04V R = 13ohm Find: (1) Vde (2) Voms (3) Ide (4) Irms Sal:-First 708 Vde $Vde = V_0 = V_{AV_g} = \frac{1}{2} \pi \int V_m \sin(\omega t) d(\omega(t)) = \frac{V_m}{T}$ \rightarrow $Vdc = \frac{V_m}{\pi} = \frac{04}{3.14} = 1.37V$ For $Idc = I_0 = \frac{V_0}{R} = \frac{V_m}{\pi R}$ $\longrightarrow Idc = \frac{V_m}{\pi R} = \frac{04}{(3.14)(13)} = \boxed{0.097 A}$ \rightarrow For $V_{8ms} = \frac{V_m}{2} = \frac{04}{2} = \frac{2}{2}$ $\frac{I_{8}m_{5}}{2R} = \frac{V_{m}}{2R} = \frac{04_{V}}{2(13)} = \boxed{0.153_{A}}$

1-0 Full une Exidge Rectifier (uncontrolled) \rightarrow Vs = Vd = Vd = $\frac{\partial(04)}{3.14} = \left[\frac{\partial \cdot 54V}{3.14}\right]$ -> Ide - Vin - A - Forther Ide = 04 = 10-30A -> Vyms = Ja Vs = Ja (2.2) -Vams = 3.59V $\rightarrow I_{82005} = \frac{T_{cd}c}{2} = \frac{0.30H}{2} = 0.15H$

ANSWER:

The Buck Converter:

The Buck Converter is used in SMPS circuits where the DC output voltage needs to be lower than the DC input voltage. The DC input can be derived from rectified AC or from any DC supply. It is useful where electrical isolation is not needed between the switching circuit and the output, but where the input is from a rectified AC source, isolation between the AC source and the rectifier could be provided by a mains isolating transformer.

The switching transistor between the input and output of the Buck Converter continually switches on and off at high frequency. To maintain a continuous output, the circuit uses the energy stored in the **inductor** L, during the on periods of the switching transistor, to continue supplying the load during the off periods. The circuit operation depends on what is sometimes also called a Flywheel Circuit. This is because the circuit acts rather like a mechanical flywheel that, given regularly spaced pulses of energy keeps spinning smoothly (outputting energy) at a steady rate.



Transistor Switch 'on' Period:

In Fig. 3.1.2 therefore, when the switching transistor is switched on, it is supplying the load with current. Initially current flow to the load is restricted as energy is also being stored in L1, therefore the current in the load and the charge on C1 builds up gradually during the 'on' period. Notice that throughout the on period, there will be a large positive voltage on D1 cathode and so the diode will be reverse biased and therefore play no part in the action.



Transistor Switch 'off' Period:

When the transistor switches off as shown in Fig 3.1.3 the energy stored in the magnetic field around L1 is released back into the circuit. The voltage across the inductor (the back e.m.f.) is now in reverse polarity to the voltage across L1 during the 'on' period, and sufficient stored energy is available in the collapsing magnetic field to keep current flowing for at least part of the time the transistor switch is open.

The back e.m.<u>f</u> from L1 now causes current to flow around the circuit via the load and D1, which is now forward biased. Once the inductor has returned a large part of its stored energy to the circuit and the load voltage begins to fall, the charge stored in C1 becomes the main source of current, keeping current flowing through the load until the next 'on' period begins.

The overall effect of this is that, instead of a large square wave appearing across the load, there remains only a ripple waveform, i.e. a small amplitude, high frequency triangular wave with a DC level of:

 $V_{OUT} = V_{IN} x$ (On time of switching waveform (ton) / periodic time of switching waveform(

$$V_{out} = V_{IN} \frac{t_{ON}}{T}$$

Fig. 3.1.4 Buck Converter Operation

Therefore if the switching waveform has a mark to space ratio of 1:1, the output V_{OUT} from the buck Converter circuit will be $V_{IN} x(0.5/1)$ or half of V_{IN} . However if the mark to space ratio of the switching waveform is varied, any output voltage between approximately 0V and V_{IN} is possible.

T))







Griven data $V_{in} = 50V$ D = 04% = 0.04 R = 13 ohmf = 201 Hz

Find: Vout us Tout (3) I'm UN L (Inductor)

Sol: We know that (1) Vowt = VD=DYs Vowt = 047. \times So = 0.04 \times So = $\frac{2V}{2V}$ (2) Jout = Vowt = $\frac{2V}{13\pi} = \frac{2V}{13\pi}$ (3) Jin = DJ. = 0.040 \times 0.153 = $\frac{0.6612 \text{ A}}{2.6}$ (4) $L = \frac{Vi - V_0}{2.6} \times DT$

QUESTION 4:

ANSWER:

Boost Converter:

Switched mode supplies can be used for many purposes including DC to DC converters. Often, although a DC supply, such as a battery may be available, its available voltage is not suitable for the system being supplied. For example, the motors used in driving electric automobiles require much higher voltages, in the region of 500V, than could be supplied by a battery alone. Even if banks of batteries were used, the extra weight and space taken up would be too great to be practical. The answer to this problem is to use fewer batteries and to boost the available DC voltage to the required level by using a boost converter. Another problem with batteries, large or small, is that their output voltage varies as the available charge is used up, and at some point the battery voltage becomes too low to power the circuit being supplied. However, if this low output level can be boosted back up to a useful level again, by using a boost converter, the life of the battery can be extended.

The DC input to a boost converter can be from many sources as well as batteries, such as rectified AC from the mains supply, or DC from solar panels, fuel cells, dynamos and DC generators. The boost converter is different to the Buck Converter in that it's output voltage is equal to, or greater than its input voltage. However it is important to remember that, as power (P) = voltage (V) x current (I), if the output voltage is increased, the available output current must decrease.



Fig. 3.2.1 Basic Boost Converter Circuit

Fig. 3.2.1 illustrates the basic circuit of a Boost converter. However, in this example the switching transistor is a power MOSFET, both Bipolar power transistors and MOSFETs are used in power switching, the choice being determined by the current, voltage, switching speed and cost considerations. The rest of the components are the same as those used in the buck converter illustrated in Fig. 3.1.2, except that their positions have been rearranged.

Boost converter Operation:



Fig. 3.2.2 Boost Converter Operation at Switch On

Fig 3.2.2 illustrates the circuit action during the initial high period of the high frequency square wave applied to the MOSFET gate at start up. During this time MOSFET conducts, placing a short circuit from the right hand side of L1 to the negative input supply terminal. Therefore a current flows between the positive and negative supply terminals through L1, which stores energy in its magnetic field. There is virtually no current flowing in the remainder of the circuit as the combination of D1, C1 and the load represent a much higher impedance than the path directly through the heavily conducting MOSFET.



Fig. 3.2.3 Current Path with MOSFET Off

Fig. 3.2.3 shows the current path during the low period of the switching square wave cycle. As the MOSFET is rapidly turned off the sudden drop in current causes L1 to produce a back e.m.f. in the opposite polarity to the voltage across L1 during the on period, to keep current flowing. This results in two voltages, the supply voltage V_{IN} and the back e.m.f.(V_L) across L1 in series with each other.

This higher voltage ($V_{IN} + V_L$), now that there is no current path through the MOSFET, forward biases D1. The resulting current through D1 charges up C1 to $V_{IN} + V_L$ minus the small forward voltage drop across D1, and also supplies the load.



Fig. 3.2.4 Current Path with MOSFET On

Fig.3.2.4 shows the circuit action during MOSFET on periods after the initial start up. Each time the MOSFET conducts, the cathode of D1 is more positive than its anode, due to the charge on C1. D1 is therefore turned off so the output of the circuit is isolated from the input, however the load continues to be supplied with $V_{IN} + V_L$ from the charge on C1. Although the charge C1 drains away through the load during this period, C1 is recharged each time the MOSFET switches off, so maintaining an almost steady output voltage across the load.

The theoretical DC output voltage is determined by the input voltage (V_{IN}) divided by 1 minus the duty cycle (D) of the switching waveform, which will be some figure between 0 and 1 (corresponding to 0 to 100%) and therefore can be determined using the following formula:

PART (B):

Qu Griven deuta VIN = SOV Voit D = 04% = 0.04 R= 1352 f= 20 KHZ

$$\frac{Find}{10} \text{ (i) Vout (2) Tout (3) Tim (3) Inductor (1)}$$

$$Sal:- Vout = \frac{V_s}{1-D} = \frac{50}{1-0.04} = \frac{52.08V}{1-0.04}$$

$$(2) Tout = \frac{V_{out}}{R} = \frac{52.08}{13} = \frac{4.006A}{13}$$

(3)
$$I_{in} = I_{out} = 4.006 = 4.17 \text{ A}$$

 $1 - D = 1 - 0.04 = 4.17 \text{ A}$



ANSWER:

Buck Boost Converter

It is a type of DC to DC converter_and it has a magnitude of output voltage. It may be more or less than equal to the input voltage magnitude. The buck boost converter is equal to the fly back circuit and single inductor is used in the place of the transformer. There are two types of converters in the buck boost converter that are buck converter and the other one is boost converter. These converters can produce the range of output voltage than the input voltage. The following diagram shows the basic buck boost converter.



Buck Boost Converter

Working principle of Buck-Boost Converter:

The working operation of the DC to DC converter is the inductor in the input resistance has the unexpected variation in the input current. If the switch is ON then the inductor feed the energy from the input and it stores the energy of magnetic energy. If the switch is closed it discharges the energy. The output circuit of the capacitor is assumed as high sufficient than the time constant of an RC circuit is high on the output stage. The huge time constant is compared with the switching period and make sure that the steady state is a constant output voltage Vo(t) = Vo(constant) and present at the load terminal.

There are two different types of working principles in the buck boost converter.

- Buck converter.
- Boost converter.

Buck Converter Working

The following diagram shows the working operation of the buck converter. In the buck converter first transistor is turned ON and second transistor is switched OFF due to high square wave frequency. If the gate terminal of the first transistor is more than the current pass through the magnetic field, charging C, and it supplies the load. The D1 is the Schottky diode and it is turned OFF due to the positive voltage to the cathode.



Buck Converter Working

The inductor L is the initial source of current. If the first transistor is OFF by using the control unit then the current flow in the buck operation. The magnetic field of the inductor is collapsed and the back e.m.f is generated collapsing field turn around the polarity of the voltage across the inductor. The current flows in the diode D2, the load and the D1 diode will be turned ON.

The discharge of the inductor L decreases with the help of the current. During the first transistor is in one state the charge of the accumulator in the capacitor. The current flows through the load and during the off period keeping Vout reasonably. Hence it keeps the minimum ripple amplitude and Vout closes to the value of Vs

Boost Converter Working

In this converter the first transistor is switched ON continually and for the second transistor the square wave of high frequency is applied to the gate terminal. The second transistor is in conducting when the on state and the input current flow from the inductor L through the second transistor. The negative terminal charging up the magnetic field around the inductor. The D2 diode cannot conduct because the anode is on the potential ground by highly conducting the second transistor.

By charging the capacitor C the load is applied to the entire circuit in the ON State and it can construct earlier oscillator cycles. During the ON period the capacitor C can discharge regularly and the amount of high ripple frequency on the output voltage. The approximate potential difference is given by the equation below.

VS + VL

During the OFF period of second transistor the inductor L is charged and the capacitor C is discharged. The inductor L can produce the back e.m.f and the values are depending up on the rate of change of current of the second transistor switch. The amount of inductance the coil can occupy. Hence the back e.m.f can produce any different voltage through a wide range and determined by the design of the circuit. Hence the polarity of voltage across the inductor L has reversed now.

The input voltage gives the output voltage and atleast equal to or higher than the input voltage. The diode D2 is in forward biased and the current applied to the load current and it recharges the capacitors to VS + VL and it is ready for the second transistor.

Modes Of Buck Boost Converters

There are two different types of modes in the buck boost converter. The following are the two different types of buck boost converters.

- Continuous conduction mode.
- Discontinuous conduction mode.

Continuous Conduction Mode

In the continuous conduction mode the current from end to end of inductor never goes to zero. Hence the inductor partially discharges earlier than the switching cycle.

Discontinuous Conduction Mode

In this mode the current through the inductor goes to zero. Hence the inductor will totally discharge at the end of switching cycles.

PART B:

Given data Q5 Vin = Sov Vout = 04% = 0.004 R = 135 fs = DOKHZ Find: Duty Cpele (D) (2) Iout (3) In (4) Inductor (L) $Sal: - I - D = -V_{in}$ Vo - Vin $-D = -\frac{Vin - 1}{V_0 + Vin}$ $D = \frac{Vin + 1}{V_0 + Vin}$ $D = \frac{50v+1}{200u+50} = 1.019$ Y $I_{out} = \frac{V_o}{R} = \frac{0.004}{13} = \boxed{3.07A}$ 3) $I_{ih} = \frac{I_0 D}{t_{-10}} = \frac{3.07 * 1.019}{1 - 1.019} = 3.10 H$ (4) $L = \frac{Vin \times D}{f \times \Delta t} = \frac{SOV \times 1.019}{\partial O \times Hz \times} =$