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Subject

DST Communication
and Networks

Program

BS(SE)

Semester

4th Section (A)

Assignment

~~Sessional~~ Sessional

Date

3-6-2020

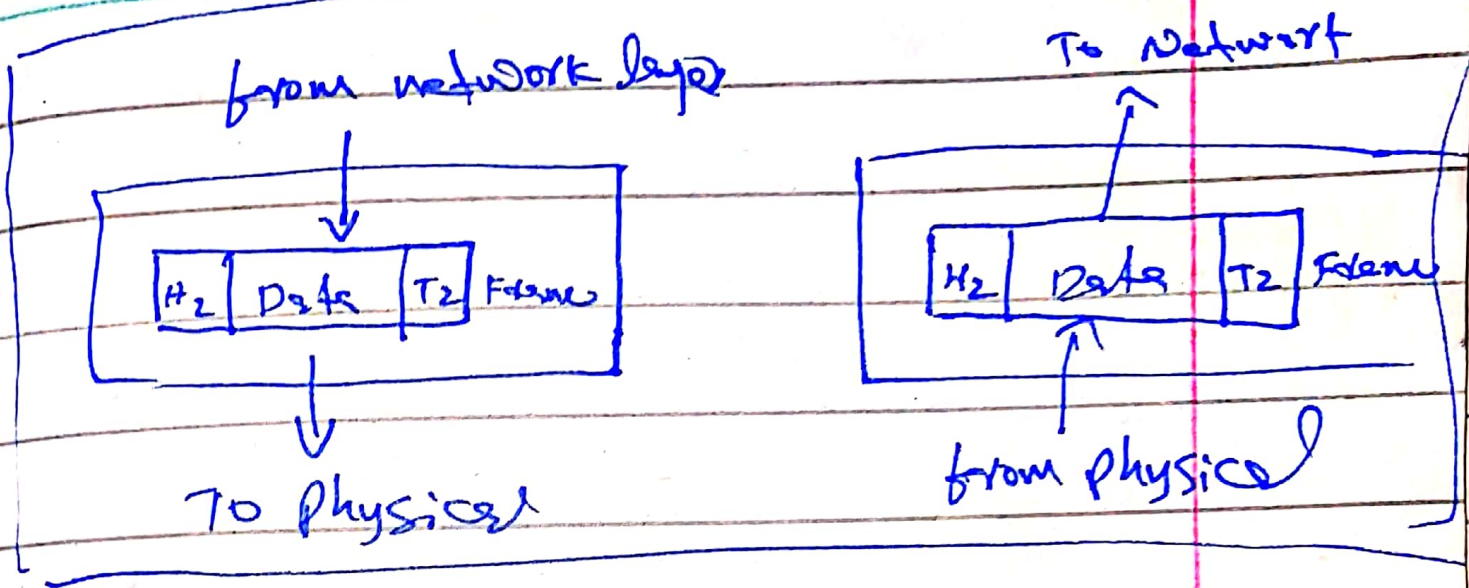
Q12:

Ans:

OSI model consist of 7 layer. The data link layer is the ~~third~~^{second} layer of OSI model.

The data link layer take service from networking layer to physical layer.

The data link layer is responsible for moving frames from one node (node) to the next.



Data link layer detect and correct any error in transmitted data.

It partitions a raw bit stream of physical layer into frames and performs error detection and correction for each frame independently. It is also perform flow control of frames b/w two nodes.

Q 22.

b) flow control.

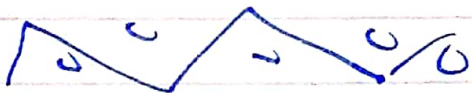
Specifies the amount of data can be transmitted by sender before receiving permission to continue from receiver.

Error Control:

* Always Receiver.

To find damaged or lost frame during transmission.

* To coordinate re-transmission of those frames by sender.



c)

HDLC

vs

PPP

Standard HDLC and
Cisco HDLC

Standard PPP

No Authentication

Authentication with
PAP and CHAP

No Protocol field

Has protocol field
multiplexing

No multiplexing

No-link quality
check

check link quality

B)

Go-Back-N-AR

- whenever error/lost of frame, it retransmits the previous N frames.
- it requires more bandwidth if high error rate.
- less complicated
- sorting is not required

Selective repeat AR protocol.

- whenever error occurs if statements only that frame that have error/lost.
- less bandwidth ~~is~~ required.
- it complex.



E)

Ans

Circuit Switching

Physical path b/w source and destination

All packets use same path

Reserve the entire bandwidth in advance

Bandwidth wastage

No store and forward transmission

Packet Switching

No physical path

Packets travel independently

Does not reserve

No Bandwidth wastage

Supports store and forward transmission.

Q 3 :

Ans :Noisyless channels :

An ideal channels in w/c no frames are lost, duplicated, or corrupted, there are two protocols for this type of channel.

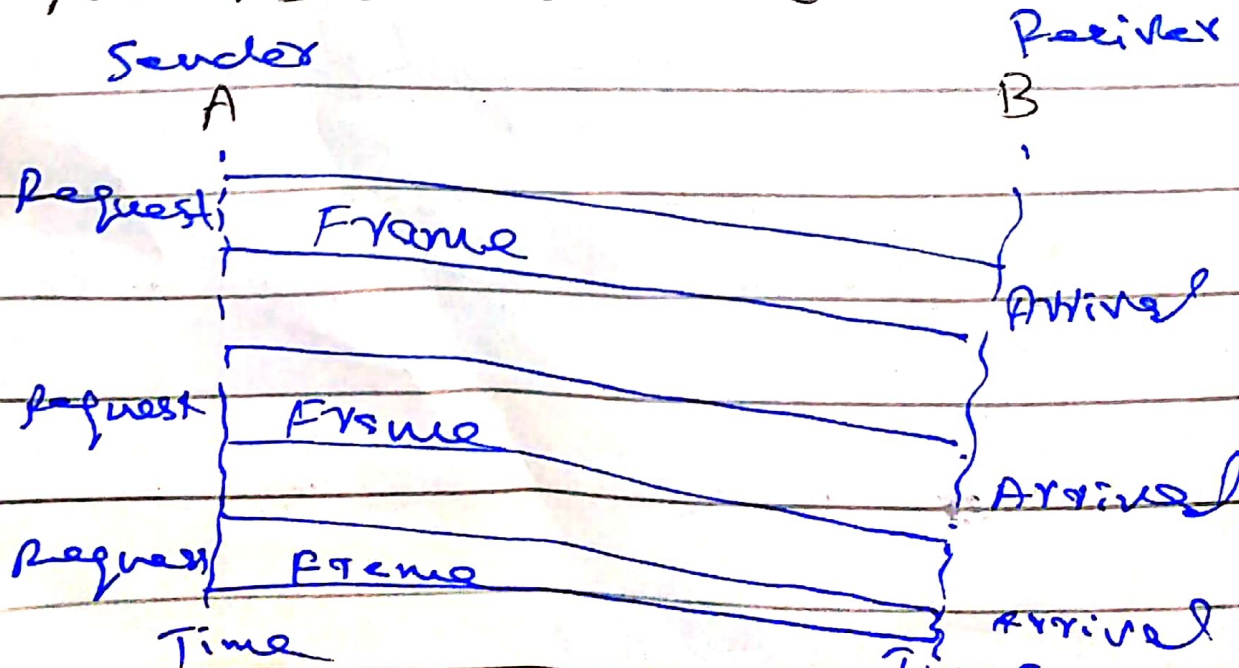
Simplest protocols

stop-and-wait protocol.

2) Noisy channels:

Although the stop-and-wait protocol gives us an idea of how to add flow control to its predecessor noiseless channels are nonexistent. We discuss three protocols in this section that use error control.

Noiseless and Noise channel



Q42.

Ans: Piggybacking.

in a wireless communications context is the unauthorized access of a wireless LAN... the usual purpose of piggybacking is simply to gain network access rather than any malicious intent, but it can show down data transfer for legitimate users of the network.

HDLC

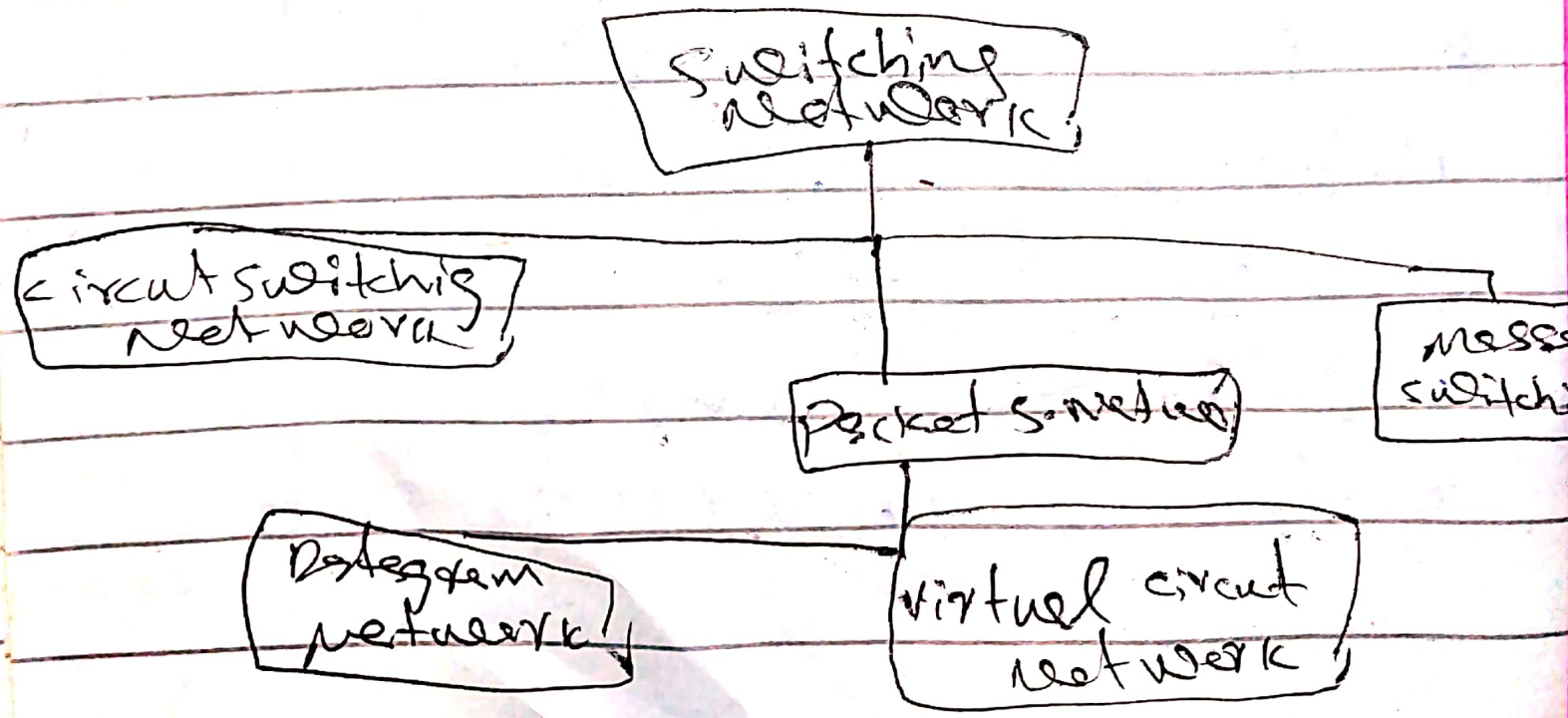
High-level Data
link Control.

- * widely used and influential standard (1979)
- * Default protocol for Serial link on Cisco routers.
- * PPP is based on a variant of HDLC.
- * HDLC in public Networks that uses X.25 protocol.
- * ISDN D channel.
- * LLC in LAN (IEEE 802.3)

Switching Network:-

A Switching Network consist of a set of switch connected by physical links.

A connection b/w two stations is a dedicated path made of one or more links.



Q5:

Ans:

Blocking refers to when an input terminal cannot be connected to an output terminal because there is no path available b/w them i.e. all possible intermediate switches are occupied.

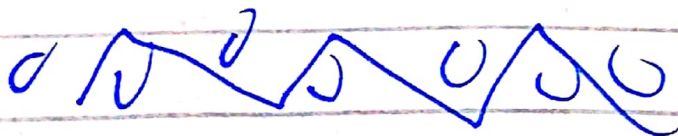
Q62

Ans

Two neighboring nodes (A and B) use a sliding window protocol with a 3-bit sequence number.

As the ARQ mechanism go-back-N is used with a window size of 7.

Assuming A is transmitted and B is receiving. Both the sliding windows at the sender and receiver have frame 0, 1, 2, 3, 4, 5, 6 at the beginning.



Q72.

Page 13

Ans:

digital to digital techniques of the three different conversions are line coding, block coding and scrambling.

Digital-to-digital encoding is the representation of digital information by a digital signal. When binary 1s and 0s generated by the computer are translated into a sequence of voltage pulses that can be propagated over a wire. This process is known as digital-to-digital encoding.



Q8:

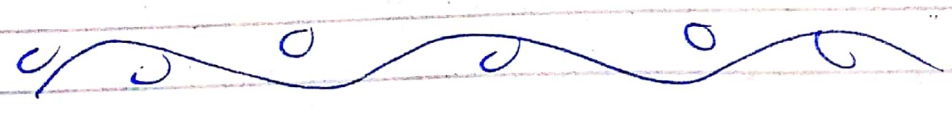
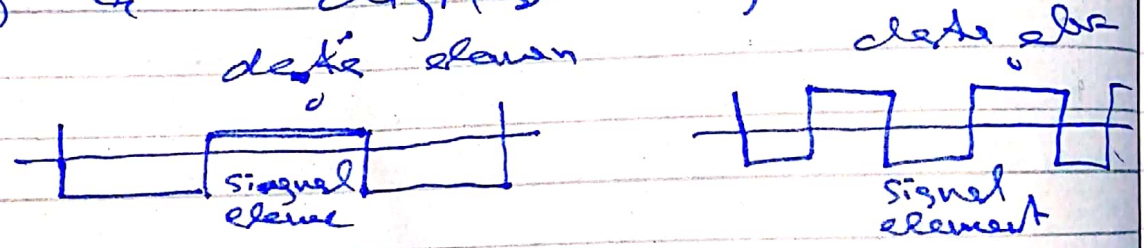
Ans: Signal elements.

Let us distinguish b/w a single element and a data element. In data communications, the goal is to send data element to send.

* A data element is the smallest entity that can represent a piece of information - this is the bit.

↑ in digital data communication a signal element carries data elements.

* A signal element is the shortest unit (time wise) of a digital signal.



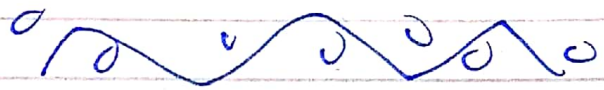
Q92.

Ans Data rate: Number of data ~~rate~~ ^{element} transmitted per second.
 Signal rate: Number of signal elements transmitted per second.

Now the unit of data rate is bit rate.

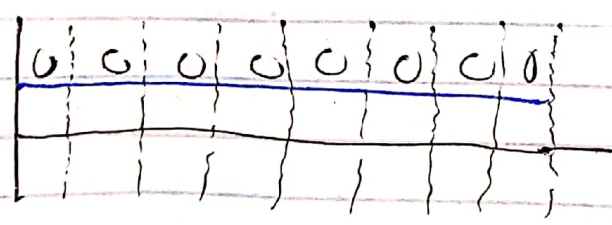
And the unit of signal rate is pulse rate. band rate or simply baud. from the previous example. We can see that.

A carriage in a train can carry more than one person so. if you consider the number of person is more than one per carriage you can see that bit rate is greater the band rate for the signal.

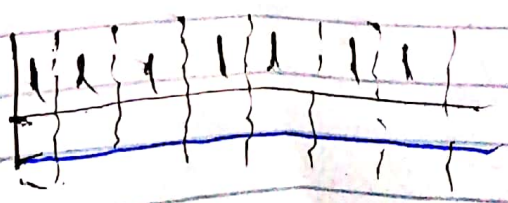


Q (0)

(Ans) (a) 00000000

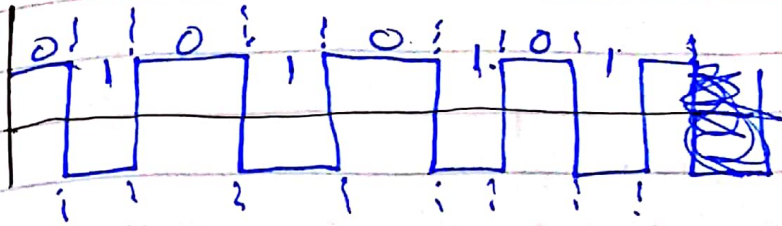


b) 111111

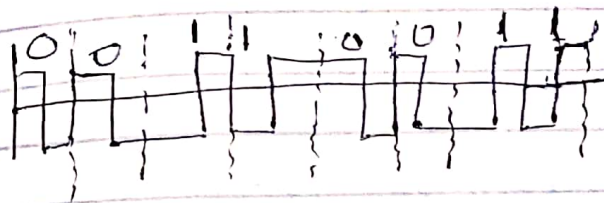


Av
le
lb.
bi.
Eg
Q
in
C
ipV
P.C
C
10

c) 01010101



d) 00110011



e) 1111

Ans: IPv6 Address

An IPv6 address is 128 bits in length and consists of eight 16-bit fields. Each field bounded by a colon. Each field must contain a hexadecimal number. In contrast to the dotted decimal notation of IPv4 addresses.

e.g: 2001:0DB8:AC10:FE01:0000:0000:0000:0000
 ↓ ↓ ↓ ↓ zero cannot be omitted
 2001:0DB8:AC10:FE01

0100000000000000:000011011011000:1010110000
 100000:1111100000000000:1000000000000000

Q122.

Ans).

classless addressing and classful addressing refer to two different way to think about ip address. Both refer to a perspective on the structure of a subnetwork. IP address classless addressing uses a two part view with classful addressing address has in 8-16 or 24-bit network field. based on the class A, B and C addressing rules. the third part, namely the subnet part of the address part from the classful view are combined into a single part often called the subnet or prefix. with the address ending in the host part.

Q132.

Ans: classes A, B and C are used for unicast communication. class D is for multicast communication and class E addresses are reserved for special purpose.

* unicast may be saying used to go into detail communication when a bit of data is mailed derived from one of point to the other point.

* multicast is the term used to describe communication where a piece of information is sent from one or more points to a set of other points.



Q1112.

Ans) IPv4 addressing:

IPv4 address space has been structured into several classes.

An IP address has two components the network address and the host address.

Subnet mask.

A Subnet mask is a 32-bit number that masks an IP address and divided the IP address into network address and host address.

Subnet mask is made by setting network bit to all 1s and setting host bit to all 0s.

Q152 (

Ans 2

The Network address in a block of address is the first address.

The mask can be ANDed with any address in the block to find the network address

A mask is a 32-bit binary number that gives the first address in the block.

When bitwise ANDed with an address in the block.

If one of the address is ~~0~~ 167.199.170.8227 then

⇒

the prefix length is 27 bits as it is and change the remaining bit.

⇒ the last byte is 01010010

→ + change the last 5 bits to 0's, we get 01000000 or 64.

⇒ the network address is 167.199.170.64/27

Q16:

ANS:

NAT:

- Nat is a process used to translate network addresses.
- Nat's primary use is to conserve public IPv4 addresses.
- Nat is usually implemented at border network devices such as a firewall or router.

there are two type of NAT.

- i) Static NAT
- ii) Dynamic NAT

A NAT (Network address translation) or network address translator is the virtualization of IP address. NAT help improve security and decrease the number of IP address an organization needs. NAT gateway sit b/w two networks. the inside network and the outside network.

Q17)

Ans:

One address addresses one byte. Using 16 bits, you can write 2^{16} addresses (from address 0 to $2^{16}-1$), and address 0 to $2^{16}-1$ bytes. 2^{16} bytes is 65536. If memory were organized in bits, this would be 65536 (kilobits). If memory is organized in 16-bit or 20-bit or 32-bit words, then addressable space would be 65536 of these words.



Q18)

Ans: To be divisible by 1024, the rightmost byte of an address should be zero (0) and the second rightmost byte must be divisible by 4 (2 bits of 2nd byte needed) only the address 17.17.32.0 meets this condition.

Q 19)

Ans)

i) 128, 14, 6, 8

$$\begin{array}{r} 2 \overline{) 128} \\ 2 \overline{) 64} - 1 \\ 2 \overline{) 32} - 0 \\ 2 \overline{) 16} - 0 \\ 2 \overline{) 8} - 0 \\ 2 \overline{) 4} - 0 \\ 2 \overline{) 2} - 0 \\ \hline 1 - 0 \end{array}$$

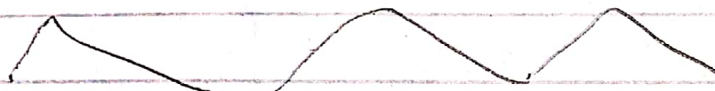
$$\begin{array}{r} 2 \overline{) 14} \\ 2 \overline{) 7} - 0 \\ 2 \overline{) 3} - 1 \\ \hline 1 - 1 \end{array}$$

$$\begin{array}{r} 2 \overline{) 6} \\ 2 \overline{) 3} - 0 \\ \hline 1 - 1 \end{array}$$

$$\begin{array}{r} 2 \overline{) 8} \\ 2 \overline{) 4} - 0 \\ 2 \overline{) 2} - 0 \\ \hline 1 - 0 \end{array}$$

128, 14, 6, 8

100000010000111000011000010001000



ii) 208, 34, 54, 12

$$\begin{array}{r} 2 \overline{) 208} \\ 2 \overline{) 104} - 0 \\ 2 \overline{) 52} - 0 \\ 2 \overline{) 26} - 0 \\ 2 \overline{) 13} - 0 \\ 2 \overline{) 6} - 1 \\ 2 \overline{) 3} - 0 \\ \hline 1 - 1 \end{array}$$

$$\begin{array}{r} 2 \overline{) 34} \\ 2 \overline{) 17} - 0 \\ 2 \overline{) 8} - 1 \\ 2 \overline{) 4} - 0 \\ 2 \overline{) 2} - 0 \\ \hline 1 - 0 \end{array}$$

$$\begin{array}{r} 2 \overline{) 54} \\ 2 \overline{) 27} - 0 \\ 2 \overline{) 13} - 0 \\ \hline 1 - 1 \end{array}$$

$$\begin{array}{r} 2 \overline{) 54} \\ 2 \overline{) 27} - 0 \\ 2 \overline{) 13} - 1 \\ 2 \overline{) 6} - 0 \\ 2 \overline{) 3} - 0 \\ \hline 1 - 1 \end{array}$$

208, 34, 54, 12

1101000000010001000100010001000

Q 202

Ans:-

a) 0111111 1110000 0110011 0111101

0111111 - 127

1110000 - 240

0110011 - 103

0111101 - 125

127.240.103.125

Noted - decimal notation of

0111111111000001100110111101

is 127.240.103.125

b) 1010111 11000000 11110000001101

1010111 - 175

11000000 - 192

11110000 - ~~248~~ 248

0001101 - 29

Noted - decimal notation

1010111110000011110000001101

is

175.192.248.29

Q 212

Ans

in a block of addresses
we know the IP address of
the host is 25.34.12.56/16
one host. first address

25.34.0.1

network address: 25.34.0.0

last address: 25.34.255.255

limited address: 25.34.255.255
in the block.

