

Date:...../...../20.....

M T W T F S

NAME :- SULMAN KHAN

ID :- 14518

SEMESTER :- 4 (A)

COURSE :- COMPUTER NETWORKING.

TEACHER :- MANSOOR OADIR.

Q. No. 2) Ans

Performing \Rightarrow operation

IP :- 14518

101 . 10 . 11 . 19

255 . 0 . 0 . 0

101 . 0 . 0 . 0

IP address = 101 . 0 . 0 . 0

LIMITED Broadcast Address

As limited broadcast does not changing from the router to another and send message to build limited people of an organisation. So it is always either

" All one's or all zero's".

limited broadcast address = 255 . 255 . 255 . 255

or

limited broadcast address = 0 . 0 . 0 . 0

Q.No:- 2 Answer.

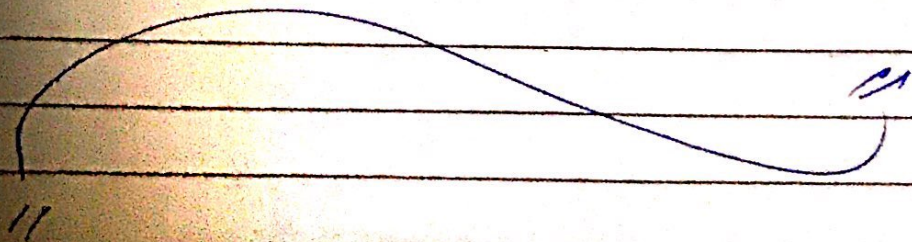
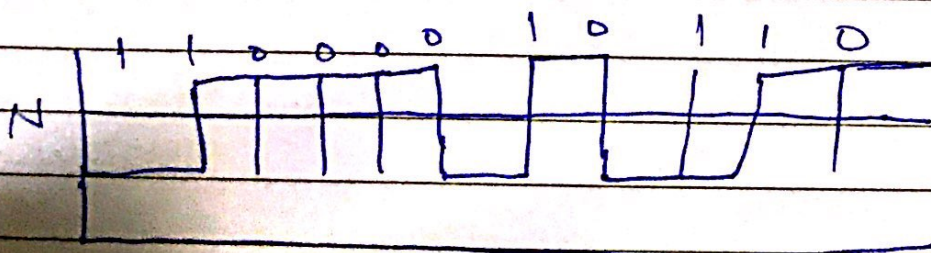
My Roll No = 14518

Now to convert 14518 into binary

14518 = 11000010110

2	14518	0
2	7259	1
2	3629	1
2	1814	0
2	907	1
2	453	1
2	226	0
2	113	0
2	56	0
2	28	0
2	14	0
2	7	1
	1	

NRZ-L Scheme Graph



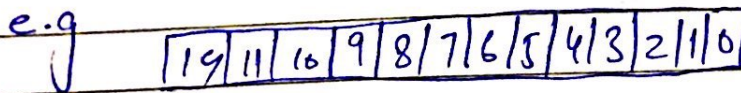
Q No. 3 Answer.

$IO = 14518$

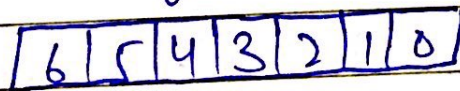
$IO \text{ LASE} = 8/2 = 4$

window size = 4

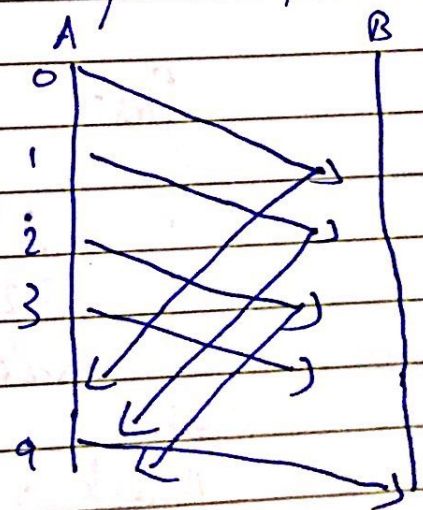
Now if no frams are sent from to B
the window has not appeared on the
frames



After A sends frame 0, 1, 2, 3, 4 and
the acknowledgment from B for 0, 1, and



As the acknowledgement
is received. So the window
slides forward.



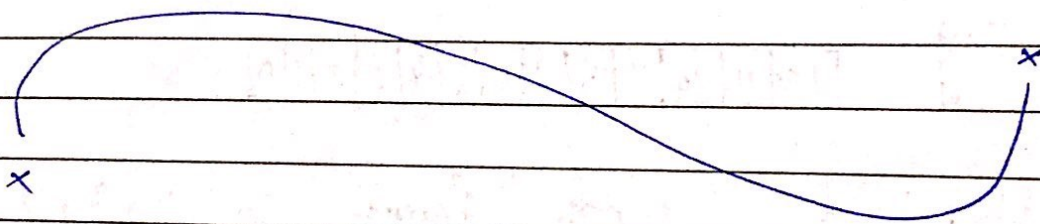
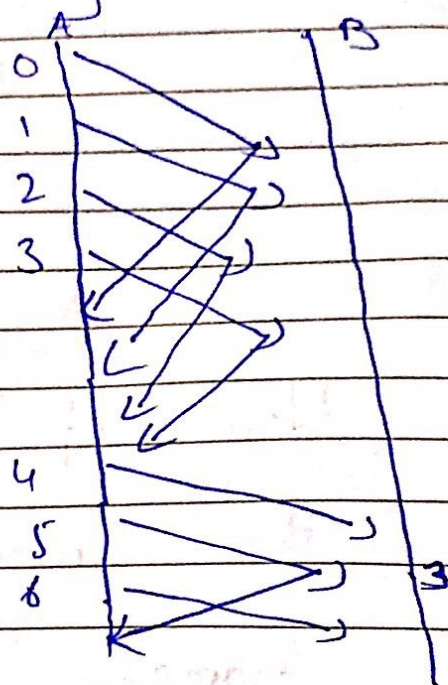
4
14518

M T W T F S

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After A sends frames 5, 6 & B acknowledges & ask received by A.

As the window size is 4
So 4 frames are sent &
after the frame is acknowledged
the window slides forward.



X

4 a)

14518

[64 customer
16 address]

Total no of address = 16

Mask 100 . 21 . 7 . 0 | 32

Network 160 . 21 . 7 . 5 | 24

Last Net : 160 . 21 . 7 . 31 | 15

total : $16 \times 64 = 1088$ available

Darsi
Notes

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MOTOWTOS

a 0114518

Receiver has received all data

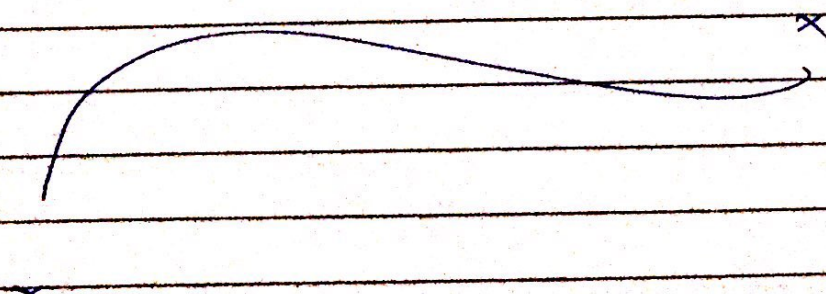
hence 4 bits remains the window.

before A sends frames

sender 0114518

window of PDV that may be

transmitted.



Qu (b)

ID :- 14518

Receiver

014|518

b) Ans

After a sends 0, 1, 4, 5, 1, 8

receives acknowledgement from R for 0, 1, 4, 5

0114|5|18

acknowledgement received for 2 bits

s 4 a)

Sender

0 1 2 3 4 1 4 5 1 8

Receiver

Acknowledge receive for
bit

0 1 2 3 4 | 1 4 5 1 8

