

DIGITAL LOGIC DESIGN.

ASSIGNMENT : 7.

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Q1)

ANSWER:-

A register is an electronic device consists of a series of flip-flops to store data bits and moving the data bits.

The length of the stored binary word depends on the number of flip-flops that made of the register.

Q:3)

ANSWER:

The shift capacity of a register permits to store and move data from one stage to another within into, or out of register.

Q: 2)

ANSWER :-

The storage capacity of a register that can retain one byte of data is 8 bits.

This particular register will be an 8 stage shift register.

Q: 4)

ANSWER :-

Since the fifth register is initially cleared.

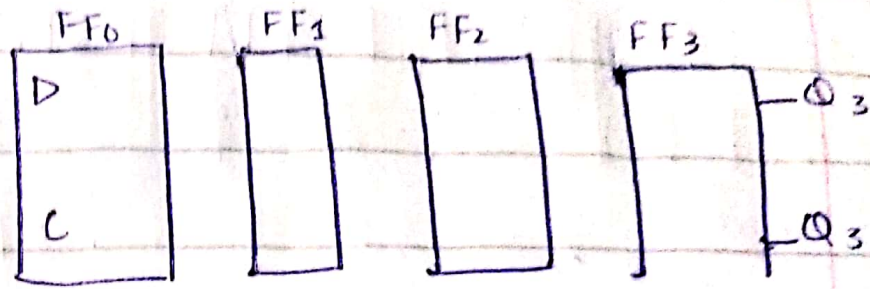
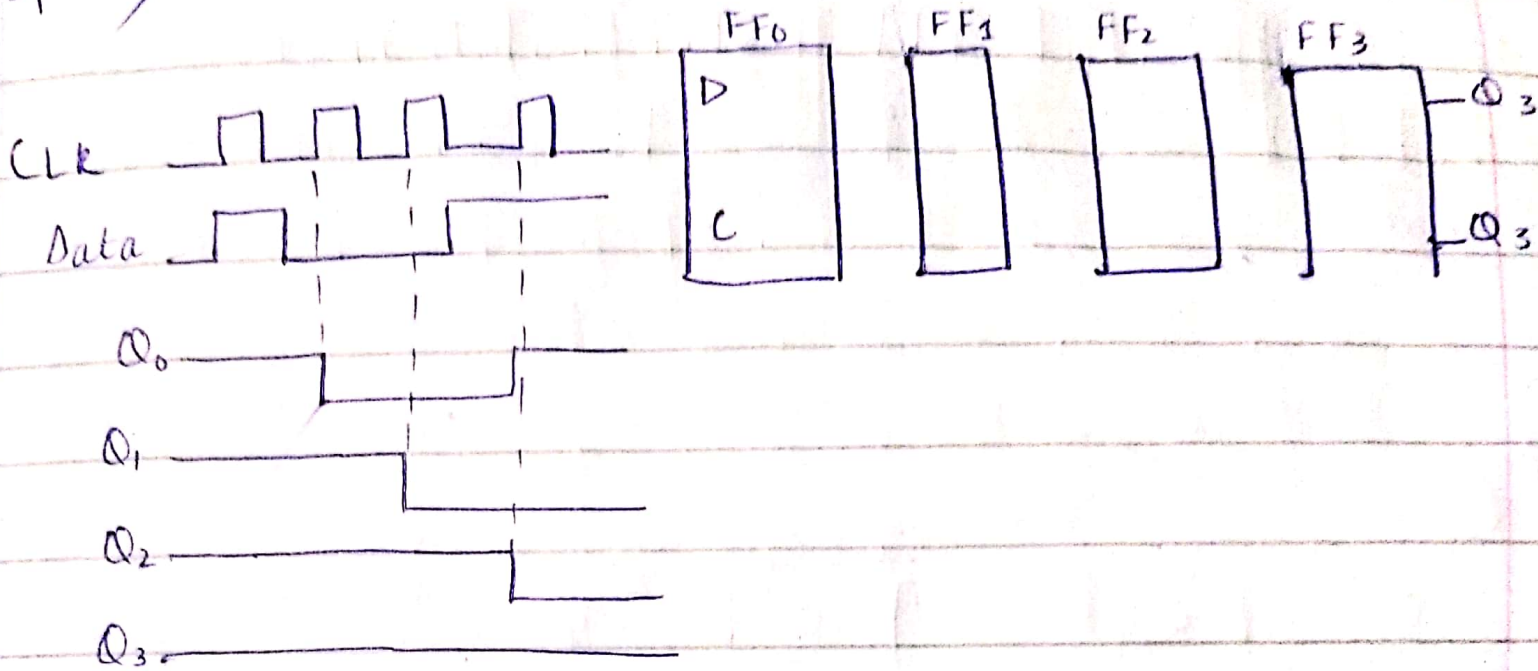
Initially 0 0 0 0

1 0 0 0

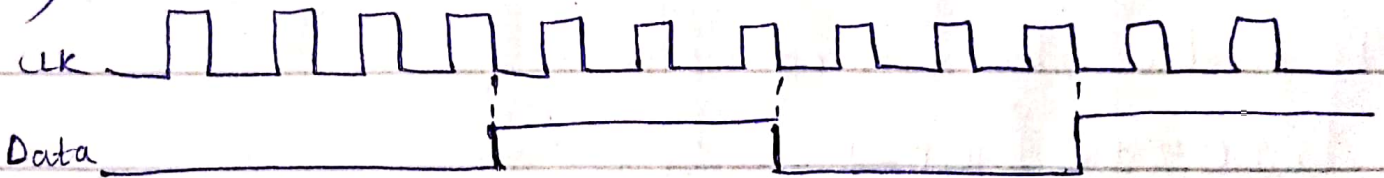
1 1 0 0

0 1 1 0

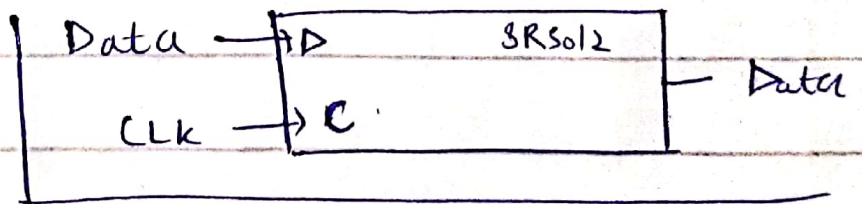
Q: 5)



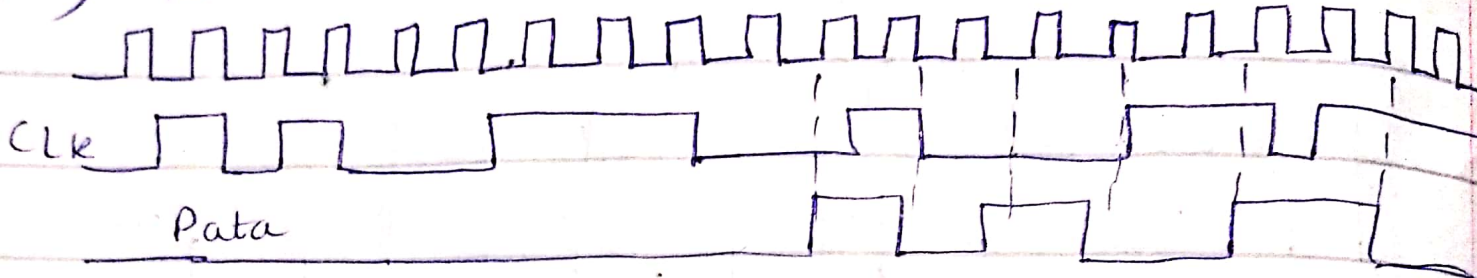
Q: 6)



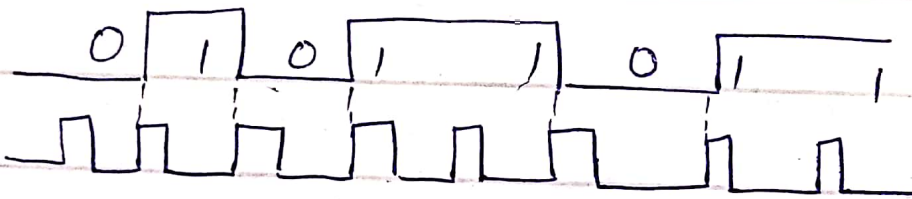
Initially	110001110000
CLK1	011000111000
CLK2	001100011100
CLK3	000110001110
CLK4	100001100011
CLK5	110000110001
CLK6	111000011000
CLK7	011100001100
CLK8	001110000110
CLK9	000111000011
CLK10	100011100001
CLK11	110001110000
CLK12	011000111000



Q: 7) Data in



Q: 8)



The data bit stored are .

11011010(2).

Answer.