

COURSE TITLE :-

ECD

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QUESTION NO :- 01 Part (A)

ANSWER :-

DARLINGTON CONNECTION.

The main feature is that the composite transistor acts as a single unit with a current gain that is the product of the current gains of the individual transistors provides high current gain than a single BJT.

The connection is made using two separate transistors having current gains of β_1 and β_2

so that the current gain

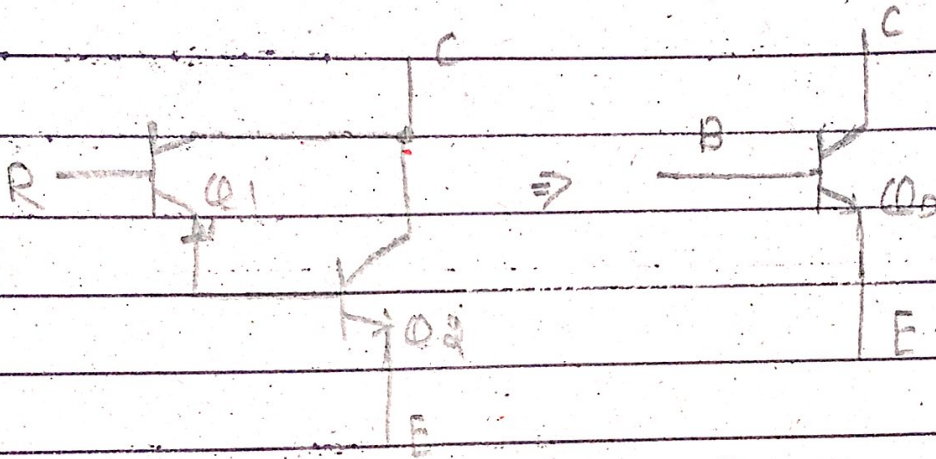
$$\beta_D = \beta_1 \beta_2$$

if $\beta_1 = \beta_2 = \beta$

The darlington connection provides a current gain of

$$D \theta = R^2$$

DIAGRAM:-



Part (b)

Solution:-

$$\text{Lin Reg} = \frac{0.062 \times 100\%}{4.5} = 1.377\%$$

$$\text{Line Reg} = \frac{0.062 \times 100\%}{40} = 0.034\%$$

Q2^(a) - Compute the magnetic field of a long straight wire that has a circular loop with a radius of 0.05m. 2amp is the reading of the current flowing through this closed loop.

Solution :-

Given

$$R = 0.05\text{m}$$

$$I = 2\text{amp}$$

$$\mu_0 = 4\pi \times 10^{-7} \text{ N/A}^2$$

Ampere's law formula is,

$$\oint \vec{B} \cdot d\vec{l} = \mu_0 I$$

In case of long straight wire

$$\oint d\vec{l} = 2\pi R = 2 \times 3.14 \times 0.05 = 0.314$$

$$B \oint d\vec{l} = \mu_0 I$$

$$\vec{B} = \frac{\mu_0 I}{2\pi R}$$

$$\vec{B} = \frac{4\pi \times 10^{-7} \times 2}{0.314} = 8 \times 10^{-6} \text{ T}$$

Part B :-

Within the cylinder $\rho = 2, 0 < z < 1$, the potential is given by $V = 100 + 50\rho + 150\rho \sin\phi$.

(a) Find V, E, D and p_v at $P(1, 60^\circ, 0.5)$ in free space. First, substituting the given point we find $V_p = 279.4V$, then

$$E = -\nabla V = -\frac{\partial V}{\partial \rho} a_\rho - \frac{1}{\rho} \frac{\partial V}{\partial \phi} a_\phi = -[50 + 150 \sin\phi] a_\rho - [150 \cos\phi] a_\phi$$

Evaluate the above at P to find $E_p = -179.9 a_\rho - 75.0 a_\phi \text{ V/m}$

$$M = V \cdot A$$

Now $D = \epsilon_0 E$, so $D_p = -1.59 a p - 664 a \phi \text{ nC/m}^2$

$$P_v = V \cdot D = \left(\frac{1}{p} \right) \frac{d}{d\phi} (p D_p) + \frac{1}{p} \frac{\partial D_\phi}{\partial \phi} = \left[\frac{-1}{p} (50 + 150 \sin \phi) + \frac{1}{p} 150 \sin \phi \right] \epsilon_0 = \frac{50 \epsilon_0 C}{p}$$

At p , this is $P_v p = -443 \text{ pC/m}^3$

(b) :- How much charge lies within the cylinder? Will we integrate P_v over the volume V to obtain.

$$Q = \int_0^1 \int_0^{2\pi} \int_0^a \frac{-50 \epsilon_0 p}{p} dp d\phi dz = -2\pi$$

QUESTION = 03 PART (A)

ANSWER

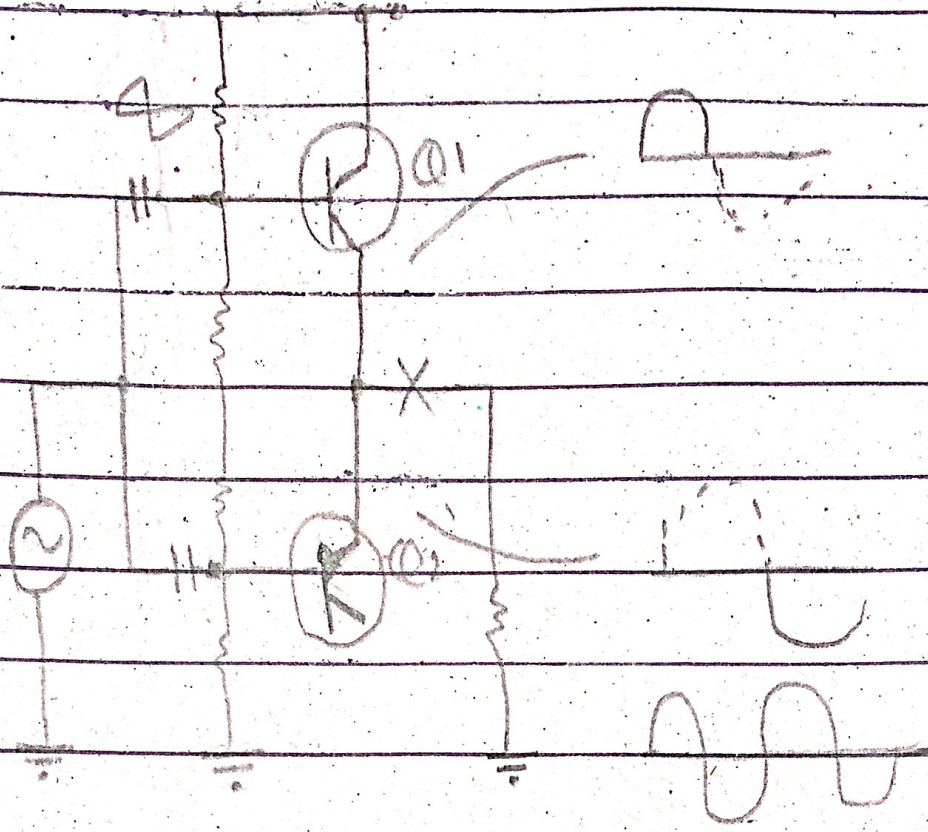
CLASS-B AMPLIFIER

When the amplifier is in its quiescent state both transistors are biased at cutoff.

When the input is positive Q_1 is biased above cutoff - transistor conducts, producing a replica of the positive input at the output.

Q_2 remains in cutoff.

When input is negative, Q_1 is biased in cutoff Q_2 is biased above cutoff and the transistor conducts, producing a replica of the negative input at the output.



Typical class 8. Amplifier

Question No = 04

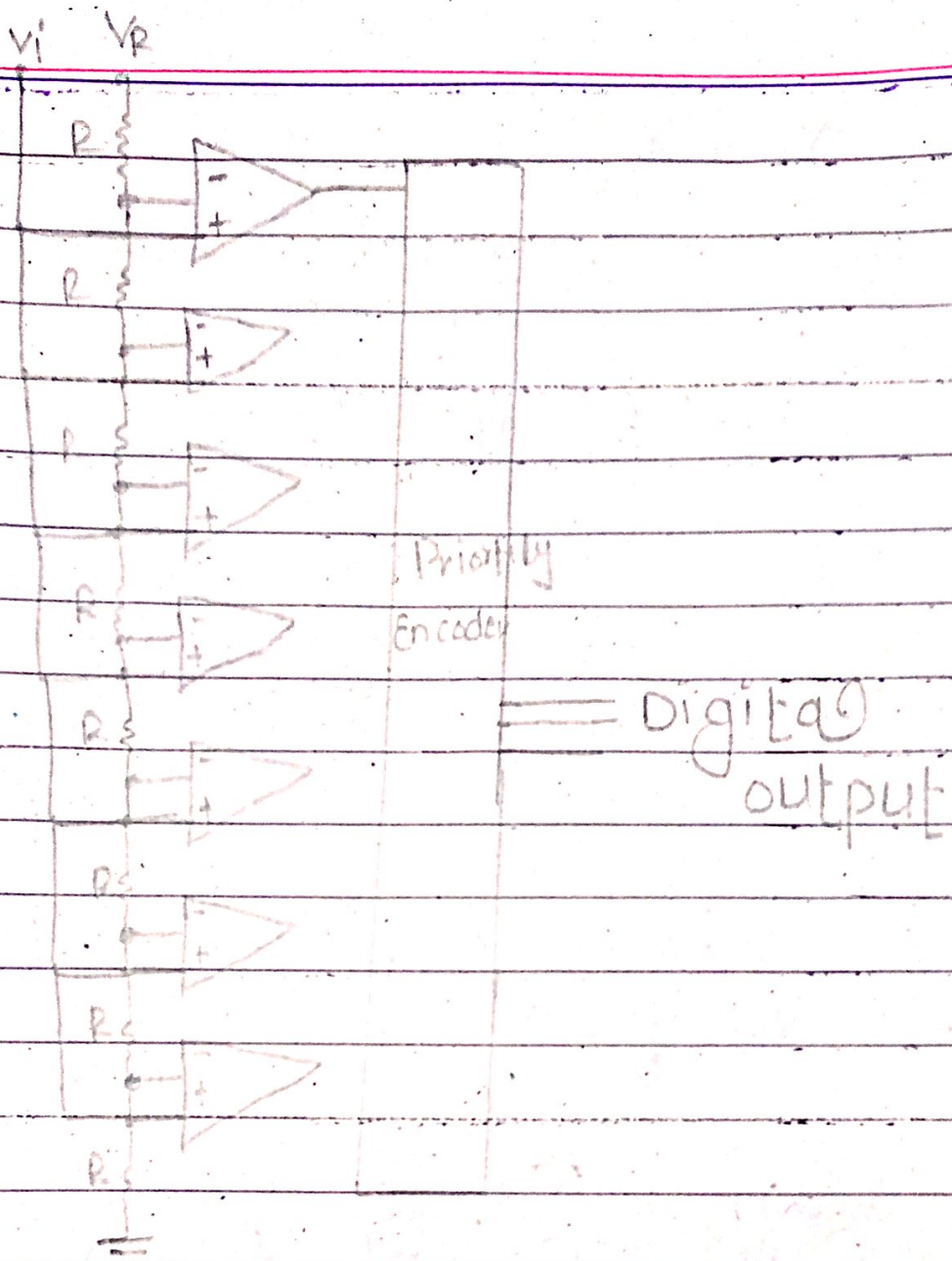
ANSWER :-

FLASH ADC

A flash adc is the type of analog-to-digital converter that uses linear voltage ladder with a comparator at each "rung" of the ladder to compare the input voltage to successive reference voltages.

A flash type adc produces an equivalent digital output for a corresponding analog input in no time. Hence flash type adc is the fastest ADC.

The circuit diagram of a 3-bit flash type ADC is shown in the following figure -



The 3 bit flash type ADC consists of voltage divider network, 7 comparators and a priority encoder.

WORKING OF FLASH ADC :-

The voltage divider network contains 8 equal resistors. A

reference voltage V_R is applied across that entire network with respect to the ground. The voltage drop across each resistor from bottom to top with respect to ground will be integer multiples of $\frac{V_R}{8}$.

The external input voltage V_i is applied to the non-inverting terminal of all comparators. The voltage drop across each resistor from bottom to top with respect to ground is applied to inverting terminal of comparators from bottom to top.

At a time, all the comparators compare the external input voltage with the voltage drops present at the respective other input terminal. That means the comparison operations take place by each comparator parallelly.

The output of the comparator will be 1 as long as V_i is greater than the voltage drop.

All the outputs of the comparators are connected as the inputs of priority encoder.

Therefore the output of priority encoder is nothing but the binary equivalent of the external analog input voltage V_i .

The flash type A/D is used in the applications where the conversion speed of analog input into digital data should be very high.

QUESTION NO = 5 Part (A)

In Low pass filter LPF circuit allows the frequency underneath cut-off frequency for flowing through it while in High pass filter HPF circuit allows the frequencies over cut-off frequency for flowing through it.

In low pass filter it can be built with a resistor which is followed by a capacitor while in high pass filter it can be built with a capacitor which is followed by a resistor.

In low pass filter it is important in eliminating the aliasing effect while in high pass filter it is important whenever the distortion occurs because of low frequency signal like noise is to be detached.

In low pass filter it is lesser than cut-off frequency while in high pass filter it is higher than cut-off frequency.

QUESTION No = 05 Part (b)

ANSWER :-

Due to presence of active components, the active filters are expensive.

However, the low cost of the passive filters is the result of the presence of the passive component in it.

The circuit realization of the active filters is quite complex circuit.

Active filters possess high value of quality factor as compared to passive filters.

Active filter need an external supply of power for circuit operation. But passive circuit donot require external energy source because it drives the energy for operation from

The applied voltage signal.

As inductor is the basic component used in passive filters and it generates problems at low frequencies. Thus passive filters are suitable for RF range operation while active filter provides a better response at low frequency.

The weight of active filter is low while passive have high.

Active components show greater sensitivity towards temperature changes. However passive components are comparatively less sensitive towards the same.