

Name : Muhammad Ali khan

Reg No. 16550

Mid term Assignment: No 2

Subject: Digital logic design

(Lab)

Instructor : Muhammad Amin

Lab:- 2
HALF Add

Table 2.1 Output Observation Table

Inputs		Outputs	
A	B	Sum(s)	Carry C
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1