

ID NO

14306

SEMESTER

5th

SUBJECT

ASSEMBLY LANGUAGE

ASSIGNMENT

NO 6

SUBMITTED TO

SIR AMIN

Page ① ASSIGNMENT  
NO (6)

Q1 write a single instruction 16-bit operands that clears the high 8 bits of AX and does not change the low 8 bits.

Ans AND AX, 00FFh

Q2 write a single instruction 16-bit operands that set the high 8 bits of AX and does not change the low 8 bits.

Ans OR AX, 0FFF00h

Q3 write a single instruction (other than NOT) that reverses all the bits in EAX.

Ans XOR EAX, 0FFFFFFFFh

Q4 write instruction that set the ZF if the 32-bit value in EAX is even and clear the

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ZF if EAX is odd.

Ans Test eax, 1; (low bit set if eax is odd.)

Q5 Write a single instruction that converts an uppercase character in AL to lowercase but does not modify AL if it already contains a lowercase letter.

Ans Or al, 00100000b

Q6 Which jump instruction follows unsigned integer comparison?

Ans ~~JA~~ JA, JNB, JAE, JNB, JB, JNAE, JBE, JNA.

Q7 Which jump instruction follows signed integer comparison?

Ans JG, JNLE, JGE, JNL, JL, JNGE, JLE, JNG

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Q8 Will the following code jump to the label named Target?

```
mov ax, 8109h
```

```
cmp ax, 26h
```

```
jg Target
```

Ans NO will the following code jump to the label named Target or NO. because the jg is used with signed value and (8109h is negative, and 26h is positive).

Q9 Implement the following pseudocode in assembly language:

a. if  $ebx > ecx$

$X = 1$

b. if  $edx \leq ecx$

$X = 1$

else

$X = 2$

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a) if  $ebx > ecx$

$X = 1$

Cmp ebx, ecx

jna next

mov X, 1

next:

b) if  $edx \leq ecx$

$X = 1$

else

$X = 2$

Cmp edx, ecx

jnb else

mov X, 1

jump next

else: mov X, 2

next:

Q30 what will be the value of BX after the following instruction execute?

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mov bx, 0FFFFh  
and bx, 6Bh

Ans BX = 006Bh

Q11 what will be the value of BX  
after the following instruction  
execute?

mov bx, 91BAh  
and bx, 92h

Ans BX = 092h

Q12 what will be the value of  
BX after the following instruction  
execute?

mov bx, 0649Bh  
or bx, 3Ah

Ans BX = 064BBh

Q13 what will be the value of BX  
after the following instruction  
execute?

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```
mov bx, 02906h  
xor bx, 8181h
```

Ans BX = A857h

Q14 what will be the value of EBX after the following instructions execute?

```
mov ebx, 0FAF649Bh  
xor ebx, 3A219604h
```

Ans EBX = BFAFF69Fh

Q15 what will be the value of RBX after the following instruction execute?

```
mov rbx, 0FAF649Bh  
xor rbx, 0FFFFFFFFh
```

Ans RBX = 00000000.50509B64h

Q16 In the following in the following instruction sequence, show the resulting value of

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AL where indicated, in binary:

- mov al, 0110111b
- and al, 00101101b ; a.
- mov al, 6Dh
- and al, 4Ah ; b.
- mov al, 00001111b
- or al, 61h ; c.
- mov al, 94h
- xor al, 37h ; d.

Ans a = 00101101      b = 01001000

c = 01101111      d = 10100011

Q17 In the following instruction sequence,  
Show the resulting value of AL  
where indicated, in hexadecimal:

```
mov al, 7Ah
not al, ; a.
mov al, 3Dh
and al, 74h ; b.
mov al, 9Bh
```



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08 al, 35h ; C.

mov al, 72h

X or al, 0DCh ; d.

Ans Al = 85h, 34h, BFh, AEh

Q18 In the following instruction sequence, show the value of the Carry, Zero, and Sign flags where indicated.

mov al, 00001111b

test al, 00000010b; a. CF =

ZF = SF =

mov al, 00000110b

cmp al, 00000101b; b. CF =

ZF = SF =

mov al, 000000101b

cmp al, 00000111b; c. CF =

ZF = SF =

Ans a. CF = 0 ZF = 0 SF = 0

b. CF = 0 ZF = 0 SF = 0

c. CF = 1 ZF = 0 SF = 1

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Q19 which Conditional jump instruction executes a branch based on the contents of ECX?

Ans JECX

Q20 How are JA and JNB affected by the Zero and Carry flags?

Ans JA/JNB (Jump if above/jump if not below or equal)

Condition for jump:-

$CF = 0$  and  $ZF = 0$

The JA/JNB Conditional jump instruction will cause program execution to transfer to another location in a range from +127 bytes to -128 bytes from the instruction following the jump instruction if  $CF = 0$  and if  $ZF = 0$  (both must be 0). if this condition is not true no jump occurs. when

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Used after CMP. This instruction is referring to the unsigned values of the operands used by the CMP instruction. DEBUG Notes:

Regardless of which mnemonic is used during assembly, DEBUG always disassembles this OP Code as JA.

[Flag affected - none].

Q22 what will be the final value in EDX after this code executes?

```
mov  edx, 1
mov  eax, 7FFFh
Cmp  eax, 8000h
jl   L1
mov  edx, 0
L1:
```

Ans The final value of EdX =

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100000000, 00000000, 01111111

11111111 00000000 00000000

10000000 00000000 = 256 J4.

Q22 What will be the final value in EDX after this code executes?

```
mov edx, 1
```

```
mov eax, 7FFFh
```

```
cmp eax, 8000h
```

```
jb L1
```

```
mov edx, 0
```

L1:

Ans Operations on eax do not directly affect the value of edx but since it has been initialized to 1 and the zeroing depend on the outcome of an operation on eax, it is affected indirectly.

jb is an unsigned operation and does what you said. Note

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that 7FFh is below 8000h  
So the jump will be taken,  
there by skipping the mov edx  
with 0. Thus, the final value  
in edx will be (1).

Q23 What will be the final value in  
EDX after this code executes:

```
mov edx, 1  
mov eax, 7FFF8000h  
cmp eax, 0FFF8000h  
j1 L2  
mov edx, 0  
L2:
```

Ans The final value of EDX =

```
00000000 00000000 01111111  
11111111 1111 1111 1111 1111  
1 00000000 00000000
```

Eax = 016.

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Q24 will the following code jump to the label named Target?

Ans Yes: will the following code jump to the label named Target?

```
mov ax, +30  
cmp ax, -50  
jg Target
```

Q25 will the following code jump to the label named Target?

Ans Yes: will the following code jump to the label named Target?

```
mov ax, -42  
cmp ax, 26  
jg Target
```

Q26 write a single instruction that converts an ASCII digit in AL to its corresponding binary value. If AL already contains

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a binary value (00h to 09h),  
leave it unchanged.

Ans And al, 00001111b

Q27 write instructions that calculate  
the parity of a 32-bit memory  
operand.

Ans Use the formula presented  
earlier:

$B_0 \text{ XOR } B_1 \text{ XOR } B_2 \text{ XOR } B_3$

• data

memval, DWORD?

• Code

mov al, BYTE PTR memval

XOR al, BYTE PTR memval+1

XOR al, BYTE PTR memval+2

XOR al, BYTE PTR memval+3

Q28 write instruction that jump  
to label L1 when the

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Unsigned integer in DX is less than or equal to the integer in CX.

Ans `Cmp dx, cx`  
`jbe L1`

Q29 write instruction that jump to label L2 when the Signed integer in AX is greater than the integer in CX.

Ans `Cmp ax, cx`  
`jg L2`

Q30 write instruction that first clear bits 0 and 1 in AL. Then, if the destination operand is equal to zero, the code should jump to label L3. Otherwise, it should jump to label L4.



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Ans And al, 11111100b  
jz L3  
jump L4

Q31 Implement the following pseudocode in assembly language. Use Short-Circuit evaluation.

a) if (val1 > ecx) AND (ecx > edx)  
X = 1  
else  
X = 2;

```
Cmp val1, ecx  
jna else  
Cmp ecx, edx  
jna else  
mov X, 1  
jmp next  
else: mov X, 2  
next:
```

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b) if (ebx > ecx) OR (ebx > val1)

X = 1

else

X = 2

```
Cmp ebx, ecx
```

```
ja L1
```

```
Cmp ebx, val1
```

```
ja L1
```

```
mov X, 2
```

```
jump next
```

```
L1: mov X, 1
```

```
next:
```

c) if (ebx > ecx AND ebx > edx) OR

(edx > eax)

X = 1

else

X = 2

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```
Cmp ebx,ecx ; ebx > ecx ?  
jna l1      ; no: try Condition after  
OR
```

```
Cmp ebx,edx ; Yes: is ebx >  
edx?
```

```
jna l1.     ; no: try Condition  
after OR
```

```
jmp l2.     ; Yes: Set X to 1  
; -----OR (edx > ebx)-----
```

```
l1: Cmp edx,ebx ; edx > ebx ?
```

```
jna else   ; no: Set X to 2
```

```
l2: mov X,1 ; Yes: Set X to 1
```

```
jump next  ; and quit
```

```
else: mov X,2 ; Set X to 2
```

```
next: ;
```

Q32 Implement the following  
pseudocode in assembly language.  
Use short-circuit evaluation and  
assume that A, B, and N are

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32-bit Signed integers.

```
while N > 0
if N != 3 AND (N < A OR N > B)
N = N - 2
else
N = N - 1
end while
```

Ans INCLUDE 32.inc

.data

N	DWORD	10
A	DWORD	9
B	DWORD	8

.Code

main PROC

```
mov eax, N
mov ebx, A
mov ecx, B
```

TOP

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```
Cmp eax, 0
```

```
jbe Next
```

```
Cmp eax, 3
```

```
jne L1
```

```
jump L4
```

L1:

```
Cmp eax, bx
```

```
jb L3
```

```
ja L2
```

L2:

```
Cmp eax, ecx
```

```
ja L3
```

```
jb L4
```

L3:

```
Sub eax, 2
```

```
jump Top
```

;

;

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L4:

```
Sub eax, 1  
jump top
```

Next

```
INVOKE Exit process, 0  
main end p  
end main
```

Q33 Create a procedure that fills an array of doublewords with  $N$  random integers, making sure the values fall within the range  $j \dots K$ , inclusive. When calling the procedure, pass a pointer to the array that will hold the data, pass  $N$ , and pass the values of  $j$  and  $K$ . Preserve all register value between calls to the procedure. Write a test program that calls the procedure twice, using

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different values for j and K.  
verify your results using a  
debugger.

Ans. Solution:-

```
INCLUDE Irvine32.inc
```

```
N = 10
```

```
.data
```

```
array DWORD N DUP(?)
```

```
j DWORD ?
```

```
K DWORD ?
```

```
.Code
```

```
main PROC
```

```
call clrscr
```

```
mov j, -10
```

```
mov K, 10
```

```
mov ESI, OFFSET array
```

```
mov ECX, N
```

```
call Filling An Array
```

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```
mov j, 100  
mov k, 1000  
mov esi, OFFSET array  
mov ecx, N  
call Filling An Array
```

```
call waiting MSG  
exit  
main ENDP
```

```
Filling An Array PROC  
push ecx  
push esi
```

h:

```
mov eax, j  
mov ebx, k  
dec ebx  
sub ebx, eax; Create range from  
0 to N  
Xchg ebx, eax; random work  
with eax
```



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Call Random Range; generate random  
int within range 0  
to N.

neg ebx ; Change sign of  
ebx

sub eax, ebx ; Sub from eax  
to define range

Call CRLF

Call write Int

mov [esi], eax

add esi, 4

loop h

pop esi

pop ecx

ret

filling An Array ~~END~~ ENDP

END main

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Q34 Create a procedure that returns the sum of all array elements falling within the range  $j \dots K$  (inclusive). Write a test program that calls the procedure twice, passing a pointer to a signed doubleword array, the size of the array, and the values of  $j$  and  $K$ . Return the sum in the EAX register, and preserve all other register values between calls to the procedure.

Program:-

```
INCLUDE Irvine32.inc  
N = 10  
.data  
array SDWORD N DUP  
(-10, -8, -6, -4, -2, -1, 1, 3, 5, 7)
```

J.DWORD?  
K.DWORD?

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• Code

main PROC

Call CLDSCX

mov j, 0

mov k, 10

mov ESI, OFFSET array

• mov ECX, N

Call Summing Array Elements  
In Range

Call write Int

Call CRLF

mov j, -10

mov k, 0

mov ESI, OFFSET array

mov ECX, N

Call Summing Array Element  
in Range

Call CRLF

Call waitMsg

exit

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```
main ENDP
Summing Array Elements In Range Proc
push ecx
push esi
mov eax, 0
h:
mov ebx, [esi]
cmp ebx, j
jge true 1
jp next
true 1:
    cmp ebx, k
    jle true 2
    jmp next
true 2:
    add eax, ebx
next:
    add esi, 4
loop h
pop esi
pop ecx
```

Yet  
Summing Array Element in Range EINDP

End main.

Q35 Data transmission systems and file subsystems often use a form of error detection that relies on calculating the parity (even or odd) of blocks of data. Your task is to create a procedure that returns true in the EFLAGS register if the bytes in an array contain even parity, or false if the parity is odd. In other words, if you count all the bits in the entire array, their count will be even or odd. Preserve all other register values .....

Create two arrays containing at least 10 bytes.

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Ans Programming:-

i . 10

```
INCLUDE Irvine32.inc
```

.data

```
byte 1 BYTE 1111110b,
```

```
1101110b, 1000110b, 11101100b,
```

```
11001100b, 11001010b, 11001010b,
```

```
11001010b.
```

```
byte 2 BYTE 1111110b,
```

```
1101110b, 1000110b, 11101100b,
```

```
11001100b, 110010b, 11001010b,
```

```
11001010b.
```

.code

```
main PROC
```

```
mov esi, OFFSET byte 1
```

```
mov ecx, SIZEOF byte 1
```

```
call PFCheck
```

```
call writeInt
```

```
exit
```

```
main ENDP
```

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## PF Check PROC

; eax PF = 1 True PF = 0 FAISE

; esi, ecx

push esi

push ecx

sub ecx, 1

mov al, 0

xor al, 0

mov al, [esi]

L1:

inc esi

xor al, [esi]

mov bl, [esi]

loop L1

jp LPF 1

mov eax, 0

jump LEND

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LPF 1:

mov eax, 1

LEND:

pop esi

ret

PF check ENDP

END main.

\*Completed\*