

Date: _____

Name :- Daniyal

ID :- 17011

Program :- BS (CS)

Subject :- Digital logic
design

Instructor :-

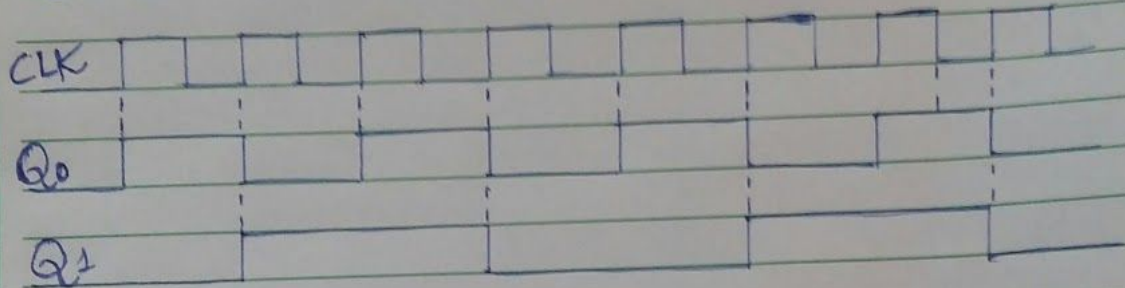
M. Amin

Date _____

1

"Question no 1"

Answer:-



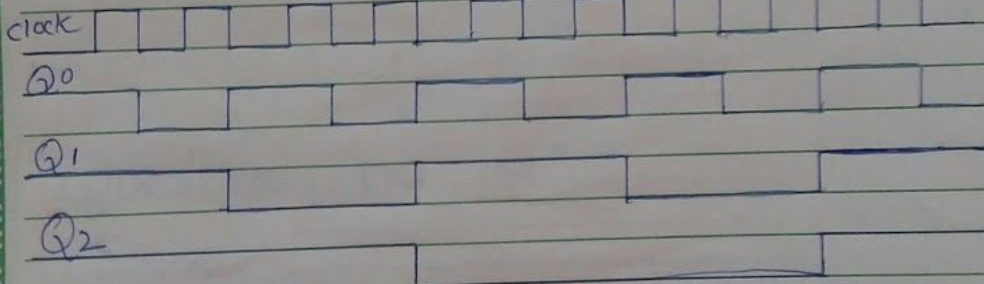
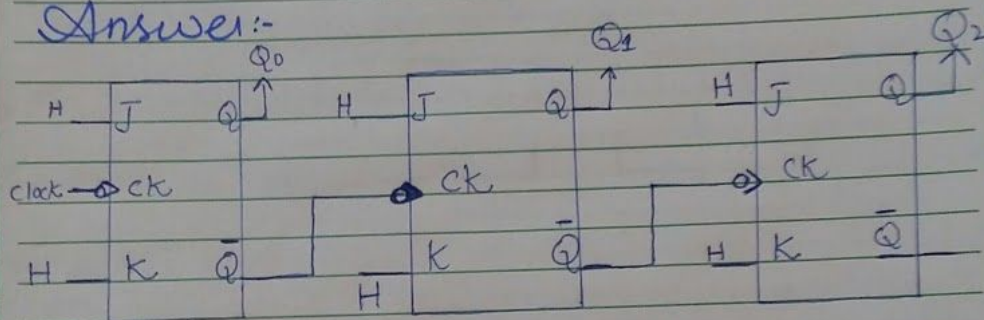
Date _____

(2)

Assign no 9

"Question no 2"

Answer:-



C											
U	0	Binary	111	110	101	100	11	10	1	0	111
n		decimal	7	6	5	4	3	2	1	0	7
T											

An asynchronous down-counter with timing diagram.

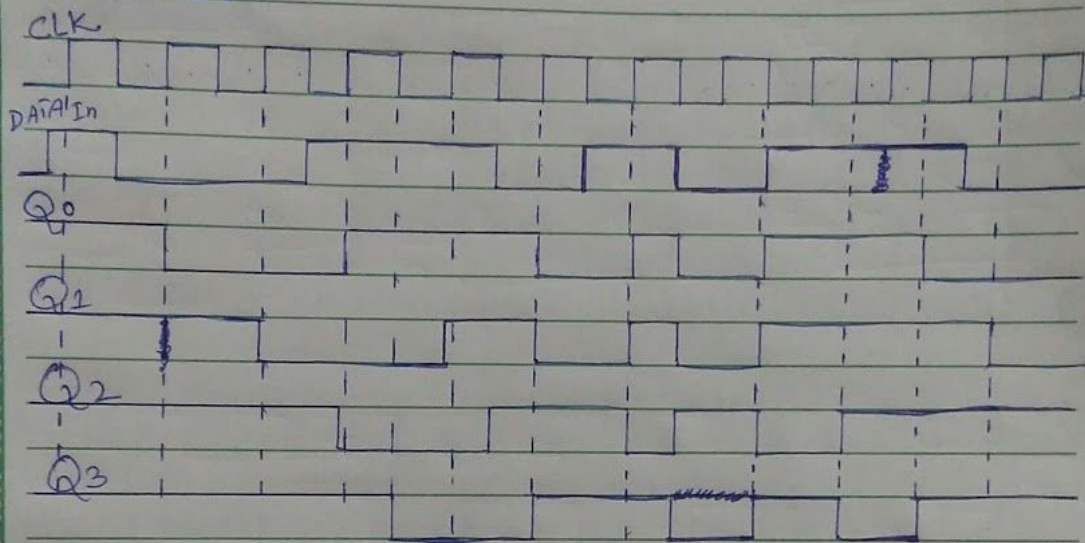
SMtdos

Date _____

(3)

"Question no 3"

Answer:-



"Question no 4"

Answer:-

Initially	101001111000	CLK 9	00111000101
CLK 1	01010011100	CLK 10	000111000010
CLK 2	001010011110	CLK 11	100011100001
CLK 3	000101001111	CLK 12	110001110000
CLK 4	000010100111		
CLK 5	100001010011		
CLK 6	110000101001		
CLK 7	111000010100		
CLK 8	011100001010		

Date _____

(4)

"Question no 5"

Answer:-

