

Course Title :-

Electronic Devices
and Circuits.

Module :-

Summer

Instructor :-

Six, Shahjari

ID No :-

15 2 43

①

Question No 5

(A) :- Answer :-

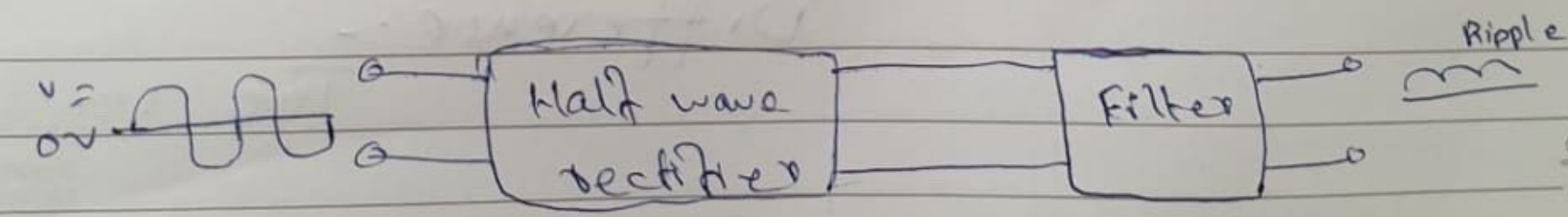
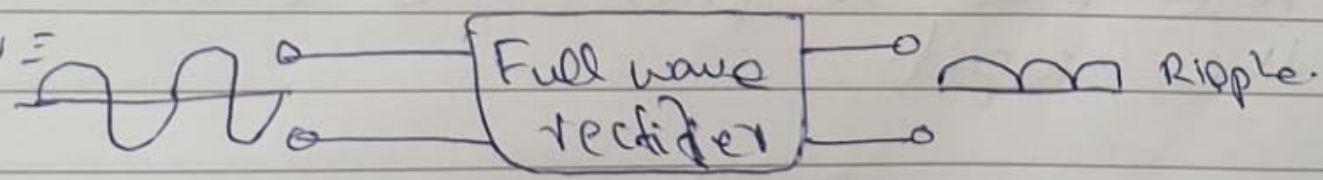
Power supply Filters:-

A power supply filter ideally eliminates the fluctuation in the output voltage of a half wave or full-wave rectifier and produce a constant-level dc voltage.

① The 60Hz pulsating dc output of a half-wave rectifier or the 120Hz pulsating output of a full-wave rectifier must be filtered to reduce the large voltage variations.

② The output of a filter is nearly smooth dc output voltage.

③ The small amount of fluctuation in the filter output voltage is called ripple factor.



Question No 5:-

(2)

(B) Answer:-

The Doping of Semiconductors Pentavalent impurities, Impurity atoms with 5 valence electrons produce n-type semiconductors by contributing extra electrons. Trivalent impurities Impurity atoms 3 valence electrons produce p-type semiconductors by producing a "hole" or electrons deficiency.

Question No 5

(C) Answer:-

The Diode Clipper, also known as a Diode Limiter, is a wave shaping circuit that take an input wave form and clips or cuts off its top half, bottom half or both halves together. This clipping of the input signal produce an output wave form that resembles a flattened version of the input.

Difference:-

The series

(3)

Negative Limiter Limits the negative portion of the input pulse. The difference between the series negative limiter and a positive limiter is that the diode is reversed in the negative limiter. In the parallel positive limiter the positive portion of the input signal is limited when the diode conducts.

Question 5

(D) Answer:-

As V_{in} becomes negative, the capacitor acts as a battery of the same voltage of $-V_{in}$. The voltage source and the capacitor counteract each other, resulting in a net voltage of zero as seen by the load.

(4)

Question No 5 f...

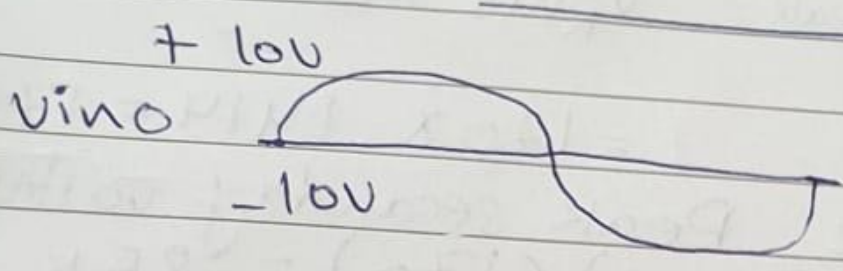
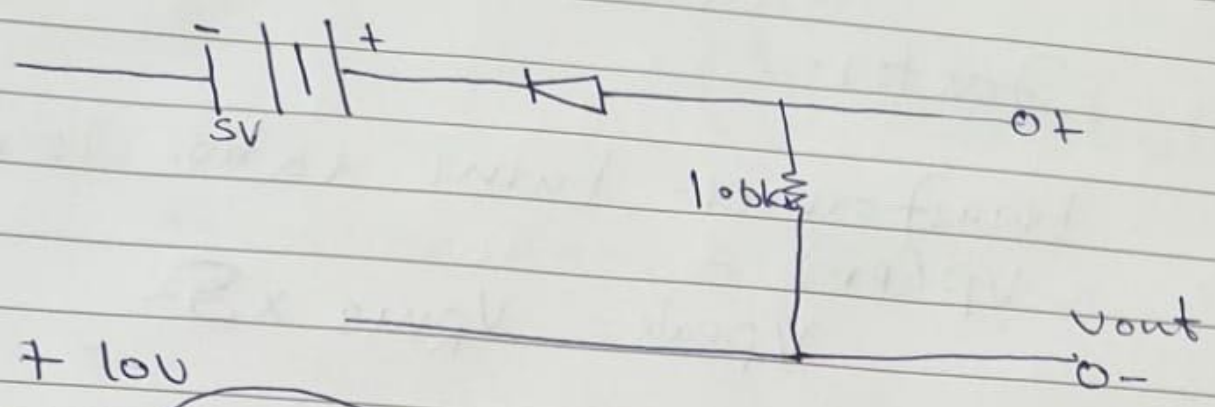
The major difference between clipper and clamper is that clipper is a limiting circuit which limits the output voltage while clamper is a circuit which shift the DC level of output voltage. The clipper and clamper circuit are exactly opposite to each other regarding their working principle.

Question 5

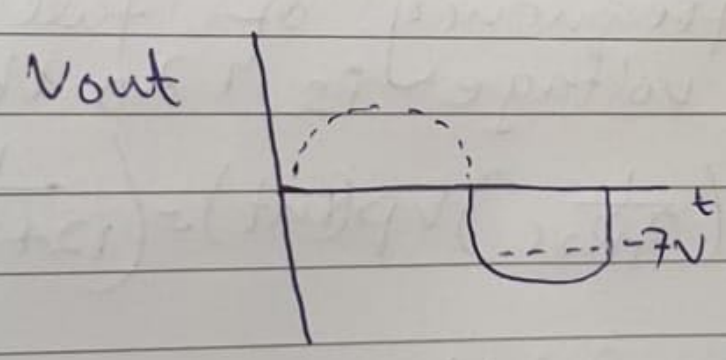
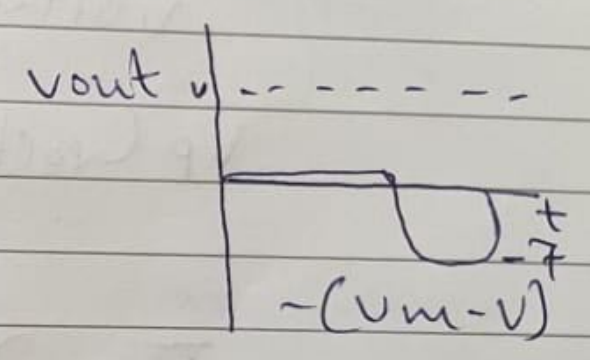
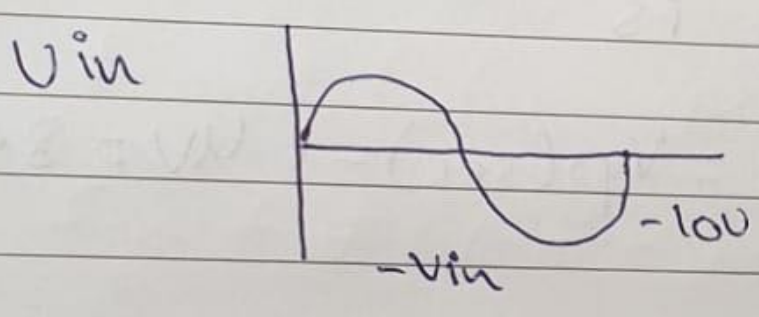
(E) Answer:-

When a 60Hz Sinusoidal voltage is applied to the input of a half wave rectifier the output frequency remain same that is 60Hz

Question No 3:-
Sol:-



output waveform
of clipper circuit



Question 2:-

(6)

Sol:-

Determine the Ripple Factor.

Transformer turns ratio is $n=0.5$

$$V_p(\text{pri}) =$$

$$V_{\text{peak}} = V_{\text{RMS}} \times \sqrt{2}$$

$$= 120 \times 1.414 = 170\text{V}$$

The Peak secondary voltage

$$V_p(\text{sec}) = (0.5)(170) = 85\text{V}$$

The unfiltered full wave rectified voltage is

$$V_p(\text{rect}) = V_p(\text{sec}) - 1.4\text{V} = 85\text{V} - 1.4$$
$$= 83.6$$

The frequency of full wave rectifier voltage is 120 Hz

$$V_r(\text{PP}) = \left(\frac{1}{fR_2C} \right) V_p(\text{int}) = \left(\frac{1}{(120)(12)(33000)} \right) (100\text{V})$$

$$V_r(\text{PP}) = 2.111$$

The approx p/c value of output

voltage is determined as

follows.

$$V_{DC} = \left(1 - \frac{1}{2fRC} \right) V_{p(\text{rect})} =$$

$$\left(1 - \frac{1}{2(120)(3300)(100\mu\text{t})} \right)$$

82.544

resulting ripple factor

$$r = \frac{V_r(\text{pp})}{V_{DC}} = \frac{2.11}{82.544} = 0.02556.$$

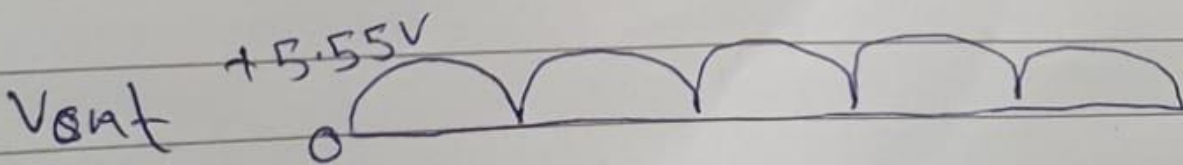
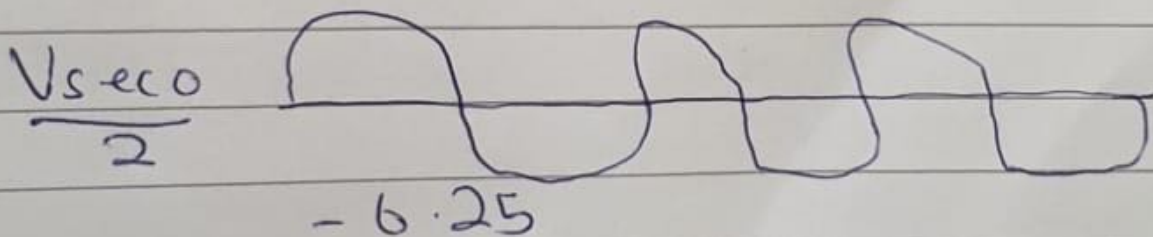
Question No 1:-

(b) Total Peak Secondary voltage

$$V_{p(\text{sec})} = 12.5\text{V}$$

(c) Half of the secondary voltage

$$6.25$$



(8)

There is 6.25V Peak to Peak voltage the output voltage has a peak value of $6.25 - 0.7$ which is diode drop.

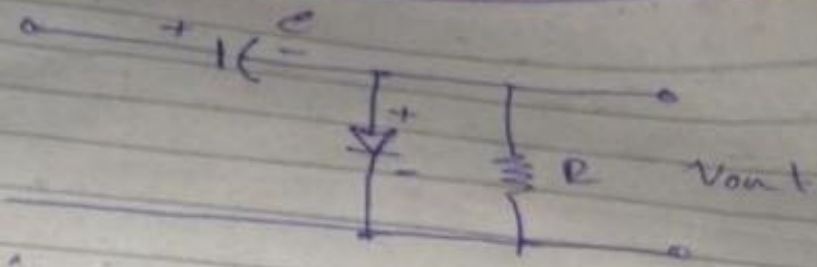
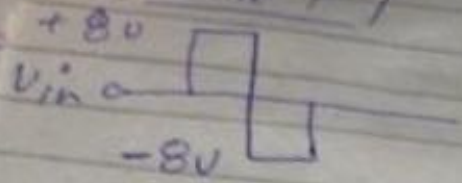
Peak current through each diode

$$I_F = \frac{V_{p(\text{sec})} - 0.7}{2} = \frac{5.55 \text{ V} - 0.00055}{10 \text{ k}\Omega} = 0.555 \text{ mA}$$

2) PIV rating must diode has

$$\begin{aligned} \text{PIV} &= 2V_p(\text{out}) + 0.7 \\ &= 2(5.55 \text{ V}) + 0.7 \\ &= 11.8 \text{ V} \end{aligned}$$

(Q#4)



→ The figure shows that the circuit is a negative clamper with a square wave input voltage. So as a whole the negative clamper will add a negative offset to the input signal.

