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I.D 14713

B.s software engineering

Section B

	Page (3)
	compare to his
	BIT- oriented Protocol:
	any field can
	J JUNG CA IF II FIAI
	Un poscible lief.
	o pirs song. The leave of
	the little
	infortant concideration this
	tighter packing of data
	Ollestion:
	Byte - Stuffing and bit stuffing
	ANSWER:-
	Byte-stuffing: Byte-stuffing is
	the Processe of adding 1 extra byte
-	Whenever there is aflag or escape
	charactor in the text.
	BIT - Stuffing: - Bit - stuffing is
	the process of adding one extra
	whenever five consective is
	e man a o in the data so that
- 1	11 does not mistake the
+	pattern 0111110 for a flag.
-	Pattern Olling
	, www
1	

	page 5	
-	transported in the data	
_	The second second	
	Inks and it could be synchronous	
	or asynchronous.	
•	Wiestron	
	Go-back-N ARQ Protocal and	
4	Selective - Repeat - ARQ - Protocal.	
	ANSWER:	
	The main difference b/w these two	
	Protocols is that after finding	
	the suspect or damage mi cent	
	frames Go-back-n Protocal re-	
	transmits all the frames whereas	
	selective repeat protocal re-transmit	
	only that frame which is damaged	
	Question:-	
	Circut-switched network and a	
	packed - switched network.	
-400	ANSWER:-	
A. The	circut-switched is connection oriented	1
	that means a path is established	
	b/w source and destination before	
		1
	the transmission occurs.	
- 11		

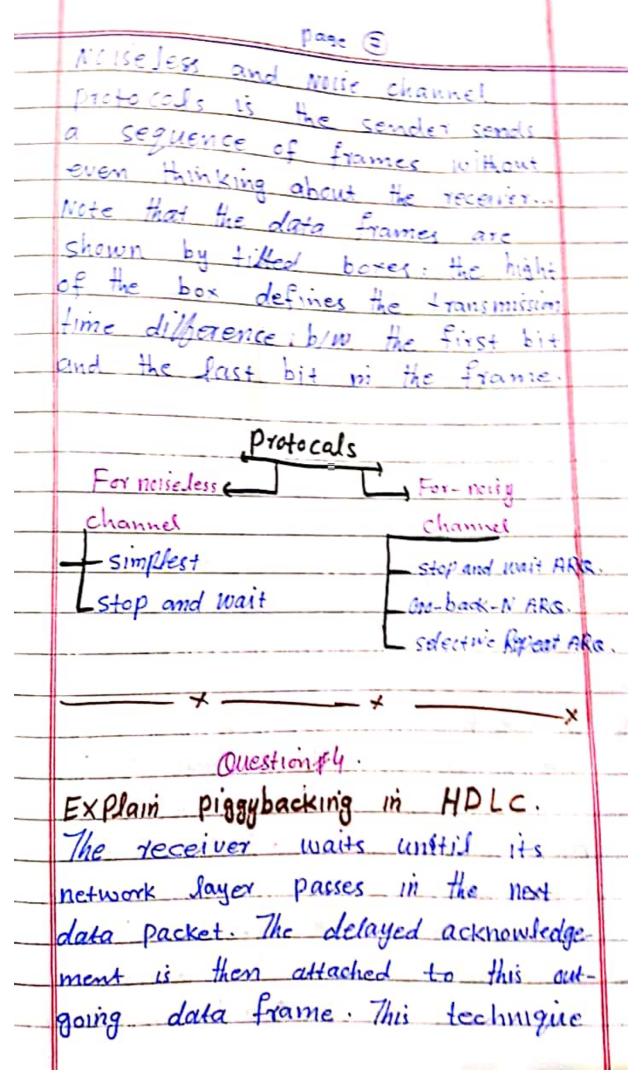
		=-	
	page (4)		
			-
	Flow control	-	_
	Flow control and error control.	-	_
	ANSWER:	-	_
	In data communications, flow contral	-	_
	is the process of managing the	_	_
	rate of data transmission b/w two		
	nodes to prevent a fast sender		
	from overwhelming a slow receiver		
	Them overwheating a side of didingushed	1	Ī
	Flow control should be distinguished	,	_
-	from congestion contral which is use	d	_
	for controlling the flow of data		_
	When congestion has actually occur	ed	
	ERROR contral:		
	The error control observe that	ut	
	the codata delivered to the		
	receiver is error free and relai	ble	H
	Question		

HDL c and PPP. ANSWER : -

0

The major difference b/w HDLS and ppp is that HDLC is the bit-oriented-protocal while ppp is the character-oxiented protocal ... on the other hand, the ppp protocal deals with

m II
Switching is compactingles !!
switching is connectionless that
for each packet While transmission
for each packet while transmission.
(Mest con
Space-division and time-division
Switches: -
ANSWER:
In a 11 space-division switch, the
Path from one device to another
is spatially separate from other
paths. The inputs and the outputs a
- connected using a girid of electric
microswitches. In a time-divis
switch, the inputs are divided
time using TDM. A control un
sends the input to the correc
output device.
//////
Question # 3.
Explain the protocal for noise
11
and noisey channel.
AMEHAD # 3
ANSWER # 3.



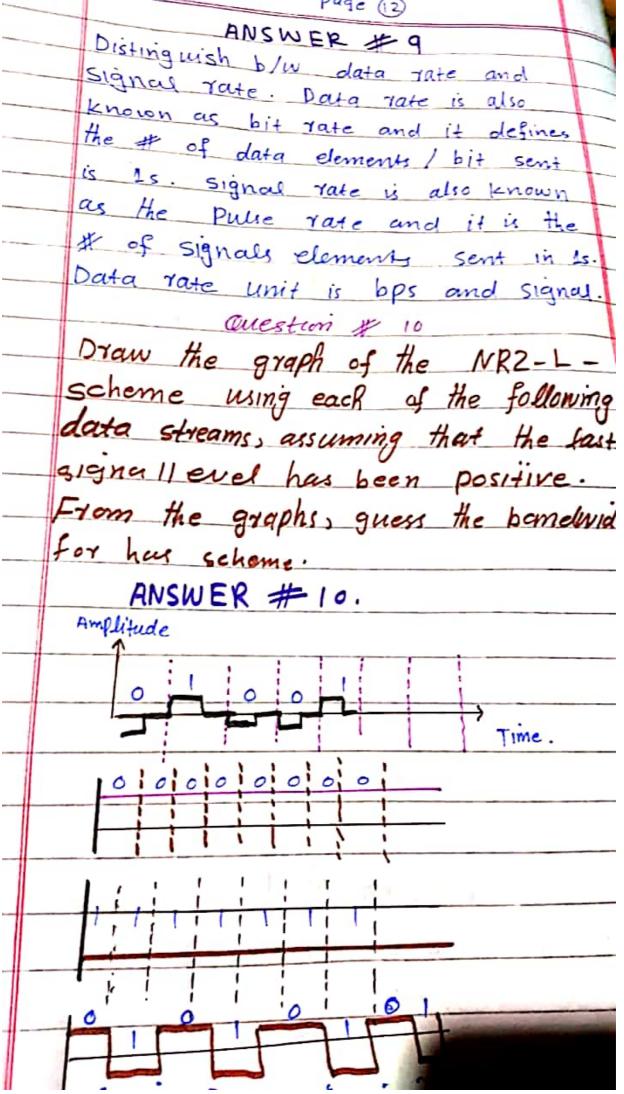
	Page ®	
	of temporary delaying the ack-	
	now leagement so that it can be	
	nocked with next outgoing data	-
	frame is known as piggybacking.	
	Ouestion # 5	
	Explain blocking in a switched:	
	ANSWER: 15	
	In multistage switching blocking	
	lefers to times when one input canno	9
	e connected to an output because	
1	here is no path available b/w then	n
a	Il the possible intermediate switch	e
a	re occupied. one solution to block	ine
	to increase the number of intermed	
SU	vitches based on the close criteria	
	Question # 6	
Tw	o neighboring modes (A and B)	
LAS	e a cliding-window protocal wil	h
a	3-bit sequence number. As the	
AD	a mechanism, go-back- N is	
115	d ill a mindous size of 1	
us	ed with a window size of u	
ASS	uming A is transmiting and	3-
L'S_	receiving, show the window	,
n	itim for the fallowing succe	85102
200		
7		

	of Page @
	Event
	O Defor A
	Teceives acknowledges
	B for a consuled greent from
	A sender Exercise
	WCKNAIN Van O A /
	the ACK is received by A.
	@ Befor A sends any frames.
	Sender: [0 1 2 3 4 5 6]
	window of PDU that may be
	fransmited = 4-bit window.
	Receiver: [0 1 2 3 4 5 6]
	b) After A sender frames 0, 1,2
	and receivers accumousedge ment from
	B for o and 1.
_	A has shrunk its window as it
_	has transmited three PDUs but
_	has transmitted mice pros
	has received ask for 2 PDUS:
	Hence it is keeping copy of one
	DDV.
	Sender: 0 1 2 3 4 5 6
-	JONOUS .
_	
- 11	

	page (10)	1
	Acknowledgment received for	
	DITE.	
	Receiver:	
	0 1 2 3 4 5 6	
	Reeni D	
-	Receiver has received all data	
Ь	hence the window remains in 4-	
	After A sends frames 3, 4 and	
	5 and B admowledges 4-bit	
	and the ACK for is received.	<b>\</b>
11	by A.	-
	Sender:	_
	0 1 2 3 4 5 6 7 0	
		1
	Question # 7	
L	ist three techniques of digital	-
t	o-digital conversion.	_   _
	ANSWER:	_
	) 191tal - to - digital convession	1:-
14	ree techniques of digital-t	0 -
1.:	ital conversion: line coding, t	lock
0119	1/tal conversion.	
Coo	ding and scrambling.	,
111	and was a comment	
	and scrembing n	nay
bloc	ck coding and scrambing n	U
- 1		

F1 - 3	-	
-5		DY may Page (11)
		Line not be needed
	- 11	ting digital data to digital signal
		ing digital I the process of conver
	- 11	011110
	11	Clarity of bita
		Me Sendan I'i
	e	ncoded into a digital data are
		Questin # 2
-	-	Distinguish hours
_	a	Distinguish b/w a signal element and a data element:
		ANSWER # 0
	A	Data de
	the	Data element is the smallest entity
	in	at can represent a piece of
	-117	ofmation (a.bit). A signal
	el	ement is the shortest unit of
	a	digital Signal Data elements
	11	e what we need to send,
	11 .	nal elements are what we
	car	sond data elements are being
	Ca	rried: signal elements are the
	car	diers'
		Question #9
	<b>n</b> ·	stiguish b/w data rate and
	DIS	STIGHTSH DIN WATER
	cia	nal tate:

Pase Co.
What is the Question #(11)
what is the number of bits in an 1pv4
mi an ipv6 addresses?
m an ipv6 addresses?
HM SWEN ALL
The IPV4 address is 30 1:
100 611 - 100
to the second se
the address space in
4294 967296
Question # 12
What are the dilherence how
classful addressing and classless
addressing in ipvu?
Classful addressing acció
Classful addressing assigns an org-
anization a class A, class B or
class a block of addresses. classless
addressing assigns an organization
a block of contigunuous addresses
barend on the soul of the
based on its needs classless addre
mg use a variable number of bit
for the network and host portions
of the address.
Question # 13.
List the classes in classful and defin
The wind with the
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page (14)
the application of each class (unicast,
reactast or reserve).
Classes A, B and c are used for
Communication clave of in fall
Communication and class E
are reserved for special
propose" unicast may be the course
To go into detail convertion
when a bit of data is mailed derived
from one of point to the other point.
Question # 14.
Answer # In.
An IP addresses has two components,
The network address and the host
address A subnet mask is a 32-bit
number that masks an IP address,
munice mass wife address;
and divides the IP address into
network address and host address.
subnet mask is made by seting
network bits to all "1"s and setting
post bits to all "0"s
The default subnet mask for class A
P address is 255.000 which implies
address is 253 000 prince 10/
hat class A addressing can have 126

_	
	networks (-7
	networks (2?-2) and (6777214 hosts
	Question # 15.
	The network #15.
	The network address is the first  address. The network address defines  the network to the network address defines
_	The network to 4
	In classful addressing, the network
_	the Tryst address in the blocks
-	the one that is assigned to the
_	organization. Owen the network address
_	17.0.
	Question # 16
	ANSWER # 16.
	A NAT (Network Address Translation
	or Network Address Translator) is the
	williadio ation of internet protocal (IP)
- 11	NAT helps improve security
- 11	I lacked the Hot IP additions
Ш	Lem Needs. 1171 9000
#	an organization matwork the inside
1	ways sit b/w two network, the inside
I	in and the outstack
1	(UNESTION )
$\parallel$	ANSWER #17.
#	one addresses one byte.
(	one againes
Ш	

Using 16. bits you can write
65536 addresses (from a to bssse,
that is 65536 different addresses).
and address 65536 bytes. 65536 bytes
is 64KB. In computer science b is bit,
B is byte.

Question # 18

## Answer # 18

Addressing within a 1024-word page required 10 bits because 1024 = 210.

Since the logical address space consists of  $8 = 2^3$  pages, the logical address must be 10 + 3 = 13 bits. Simularly, Since there are  $32 = 2^5$  physical pages) physical addresses are 5 + 10 = 15 bits long.

Question # 19

change the following 1p addresses from dotted-decimal notation to binary notation.

- a) 129.14.6.B.
- b) 208.34.54.12.

Answer # 19

9) 129.14.6.8

	Page (17)	
	129 = 1000001	-
-	= 1110	
	6 = 110	_
	8 = 1000	_
	Hence	_
	129.14.6.8	_
	1000001 1110 1/0 1000	_
	b) 208.34.54.12.	_
		_
	208 = 11010000	_
	34 = 100010	_
	Sh = 110110	_
	12 = 1100	_
	Honce,	_
	208.34.54.12	_
	11010000.100010/101101100.	_
	101000010000000000000000000000000000000	_
_	Question # 20	
	Answer # 20	-
0	7) 0111111 11110000 01100111 01111101	
11	) 10101111 11 000000 11111000 00011101	ľ
	10101111 41 88 88 88 1111000 86 11161	Ì
-0	7) 125. 240. 103. 125	
b	) 175.192.248.29.	-
_		1
Ш	Scanned with CamScanner	

	page (18)
	Question # 21.
	ANSWER # 21.
	In a block of addresses we know
	the IP address of the host is 25.34
	12.56/16 one host. first address 189.
	44. 82.1 Network address 182.44.
1)	a proce of dold long the sum
	the IP address of the host is
	25.34.12.56/16. one host, first
	addresses 25.34.0.1 Network address
	25.34.00.
	Last address: 25.34.255.255.
	Limited address 25.34.255.255.
0	m block.
3)	one host, first address 189.44.89
	Network address 189.44.82, o Last
	address: 189. 44. 82. 254 Limited.
	address.
	Ended.
	Thank you.
	THOME YOU.
	•
	The state of the s