

# **Digital Logic & Design(Theory)**

**Instructor: Muhammad Amin**

**Course Codes: CSC-201**

**EDP Codes: 102002077**

**Mid-Term Assignment**

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**Program: BS(CS)**

**Name = M.YASIR**

**ID = 15459**



Q1) Convert each of the following.

@  $45.25_{10} = (?)_2$

Give that

$$45.25_{10}$$

Required:

Convert  $45.25_{10} = (?)_2$  to binary number system

Soln

using repeated division of 45

2	45	
2	22	→ 1
2	11	→ 0
2	5	→ 1
2	2	→ 1
2	1	→ 0

So,

$$(45)_{10} = (101101)_2$$

Now,

$$0.25 \times 2 = 0.5$$

$$.5 \times 2 = 1$$

$$(45)_{10} = (101101.01)_2 \text{ Ans}$$

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$$\textcircled{b} 10000000.1010_2 = (? )_{10}$$

Given that

$$10000000.1010_2$$

Required:

To convert the given binary number into decimal number system.

Soln

Using weight notation.

$$(1 \times 2^7) + (1 \times 2^{-1}) + (1 \times 2^{-3})$$

$$(1 \times 128) + (0.5) + (1 \times 0.125)$$

$$128 + 0.5 + 0.125$$

$$(128.625)_{10}$$

So,

$$(10000000.1010)_2 = (128.625)_{10}$$

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$$c) 4D7F_{16} = (??)_{10}$$

Given that

$$4D7F_{16}$$

Required:

Convert the given Hexa number into decimal number system.

Soln

using weighted notation

$$\Rightarrow (4 \times 16^3) + (D \times 16^2) + (7 \times 16^1) + (F \times 16^0)$$

$$\Rightarrow (4 \times 16^3) + (13 \times 16^2) + (7 \times 16^1) + (15 \times 16^0)$$

$$\Rightarrow (4 \times 4096) + (13 \times 256) + (7 \times 16) + (15 \times 1)$$

$$\Rightarrow 16384 + 3328 + 112 + 15$$

$$\Rightarrow (19839)_{10}$$

So,

$$(4D7F)_{16} = (19839)_{10} \text{ Ans}$$

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$$d) 128_{10} = (?)_{16}$$

Given that:

$$(128)_{10}$$

Required:

Dec ~~to~~ convert to the given  
Hex.  
Soln

using Repeated division by 16

$$\begin{array}{r|l} 16 & 128 \\ \hline & 8 \rightarrow 0 \end{array}$$

Hence

$$(128)_{10} = (80)_{16} \text{ Ans}$$

$$e) 3A6F_{16} = (?)_2$$

Given that

$$(3A6F)_{16}$$

Required:

To convert the hex number into binary -

Soln

$$\begin{array}{c|c|c|c} 3 & A & 6 & F \\ \hline 0011 & 1010 & 0110 & 1111 \end{array}$$

Hence

$$3A6F_{16} = 0011101001101111_2 \text{ Ans}$$

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$$(f) \underline{110000} \underline{111100} \underline{101}_2 = (?)_{16}$$

Given that:

$$110000111100101_2$$

Required:-

Hex equivalent of given binary number.

Soln

~~110000111100101~~

$$\begin{array}{cccc} \underline{0110} & \underline{0001} & \underline{1110} & \underline{0101} \\ 6 & 1 & E & 5 \end{array}$$

So,

$$(110000111100101)_2 = (61E5)_{16}$$

$$\underbrace{\hspace{10em}} \underbrace{\hspace{10em}} \underbrace{\hspace{10em}} \underbrace{\hspace{10em}} //$$

g)  $6173_8 = (?)_{10}$

Given that

$$(6173)_8$$

Required:-

Decimal equivalent of  $(6173)_8$

Soln

$$\Rightarrow (6 \times 8^3) + (1 \times 8^2) + (7 \times 8^1) + (3 \times 8^0)$$

$$\Rightarrow (6 \times 512) + (1 \times 64) + (7 \times 8) + (3 \times 1)$$

$$\Rightarrow 3072 + 64 + 56 + 3$$

$$\Rightarrow (3195)_{10}$$

$$\therefore (6173)_8 = (3195)_{10} \text{ Ans}$$

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$$H) (169)_{10} = (?)_8$$

Given:

$$(169)_{10}$$

Required:

octal equivalent of  $(169)_{10}$

Soln

$$\begin{array}{r|l} 8 & 169 \\ \hline 8 & 21 \rightarrow 1 \\ \hline & 2 \rightarrow 5 \end{array}$$

Hence,  $(169)_{10} = (251)_8$  Ans

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$$I) (2A7D)_{16} = (?)_8$$

Given that

$$(2A7D)_{16}$$

Required:

octal equivalent of  $(2A7D)_{16}$

Soln

$\frac{2}{0010}$	$\frac{A}{1010}$	$\frac{7}{0111}$	$\frac{D}{1101}$	convert Hexa to binary
------------------	------------------	------------------	------------------	------------------------

$\frac{000}{0}$	$\frac{210}{010}$	$\frac{210}{101}$	$\frac{210}{001}$	$\frac{210}{111}$	$\frac{210}{101}$	binary to octal
	$\frac{2}{2}$	$\frac{5}{5}$	$\frac{1}{1}$	$\frac{7}{7}$	$\frac{5}{5}$	

Hence,

$$(2A7D)_{16} = (25175)_8$$

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~~Page (6)~~

$$J) (1111111)_2 = \pm (?)_{10}$$

Given:

$$(1111111)_2$$

Required:

Decimal value of the given single number.

Soln

$$\Rightarrow (1 \times 2^6) + (1 \times 2^5) + (1 \times 2^4) + (1 \times 2^3) + (1 \times 2^2) + (1 \times 2^1) + (1 \times 2^0)$$

$$\Rightarrow 64 + 32 + 16 + 8 + 4 + 2 + 1$$

$$\Rightarrow (127)_{10}$$

Since a single bit

$$(1111111)_2 = (-127)_{10} \quad \text{Ans}$$

$$K) -12_{10} = (?)_2$$

Given:

$$(-12)_{10}$$

Required:

Binary equivalent of  $-12_{10}$

Soln

2	12	
2	6	0
2	3	0
	1	1

$$-12_{10} = (1100)_2 \Rightarrow 00001100_{(2)}$$

Now ~~the~~ taking 2's complement of obtain number.

$$\begin{array}{r} 00001100 \\ 11110011 \\ \hline 1 \end{array}$$

$$\begin{array}{r} 11110100 \end{array}$$

Hence,  $(-12)_{10} = (11110100)_2$  Ans

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$$L) 198 = (?)_{BCD}$$

Given:

$$(198)_{10}$$

Required:

convert the given decimal number to binary coded decimal.

soln

$$\frac{1}{0001} \quad \frac{9}{1001} \quad \frac{8}{1000}$$

Hence,

$$(198)_{10} = (000110011000)_{BCD}$$

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$$M) (100001110000)_{BCD} = (?)_{10}$$

Given:-

$$(100001110000)_{BCD}$$

Req:?

Decimal equivalent of the given BCD number.

soln

using BCD-Decimal table

$$\frac{1000}{8} \quad \frac{0111}{7} \quad \frac{0000}{0}$$

Hence,

$$(100001110000)_{BCD} = (870)_{10}$$

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N)  $(1001010)_2 = (?)_{gray}$

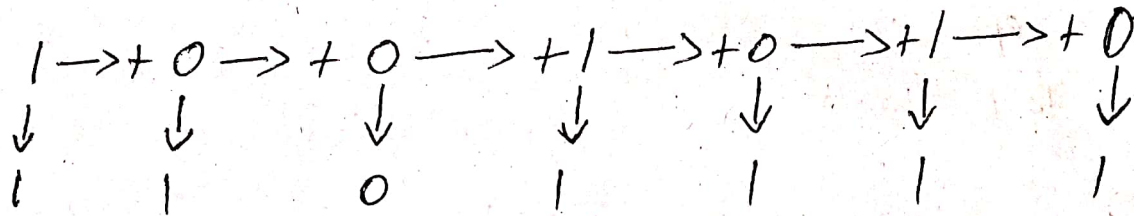
Given:

$(1001010)_2$

Req:

Gray equivalent of  $1001010_2$

Sol:



Hence,

$(1001010)_2 = (1101111)_{gray}$  Ans.

Q)  $(10101111)_{gray} = (?)_2$

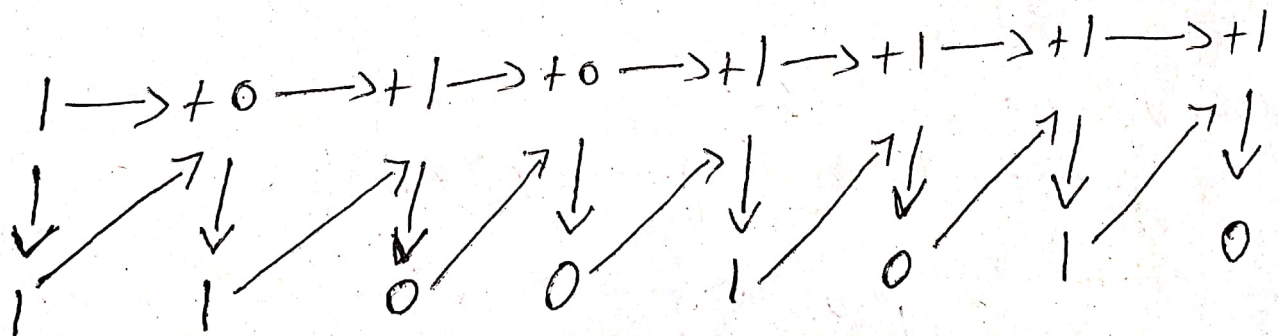
Given:

$(10101111)_{gray}$

Req:

Binary equivalent to gray

Sol:



Hence,

$(10101111)_{gray} = (11001010)_2$



P)  $0100\ 0001 = (?)_{10}$  ASCII

Given:

Req:  $0100\ 0001_{(2)}$

Sol: ASCII Equivalent  $0100\ 0001_{(2)}$   
using ASCII table

$$\Rightarrow (1 \times 2^6) + (1 \times 2^0)$$

$$\Rightarrow (1 \times 64) + (1 \times 1)$$

$$\Rightarrow 64 + 1$$

$$\Rightarrow (65)_{10} \Rightarrow 65_{(10)} = (A) \text{ ASCII character.}$$

Q)  $1110\ 00 = (?)_{10}$  Even Parity

Given:

Req:  $1110\ 00_{(2)}$

Sol: Attach Even Parity bit to  $(1110\ 00)$

Since there has to be an even amount of 1's in an even parity number, we add 1 to the given number:

$$111100 \Rightarrow 111100_{(2)}$$

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Q2) Calculate each of the following numbers:

a)  $0111111_2 - 0000011_2$

soln

Taking 2's complement on

$$\begin{array}{r} 00000111 \\ \hline 11111000 \rightarrow 1's \text{ complement} \\ + \phantom{00000}1 \rightarrow 2's \text{ complement} \\ \hline 11111001 \end{array}$$

Now;

$$\begin{array}{r} 10111111 \\ + 11111001 \\ \hline \rightarrow 101111000 \end{array}$$

Discard  
bit

Hence;

$(01111000)_2$  Ans

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b)  $01101010_{(2)} \times 11110001_{(2)}$

sol  
using 2's complement

$$\begin{array}{r} 11110001 \\ 00001110 \rightarrow 2's \text{ complement} \\ \hline \phantom{0000}1 \rightarrow 2's \text{ complement} \\ \hline 00001111 \end{array}$$

Now;

$$\begin{array}{r} 00001111 \\ 01101010 \\ \hline 00000000 \\ 00001111 \times \\ 00000000 \times \times \\ 00000000 \times \times \times \\ 00000000 \times \times \times \times \\ 00000000 \times \times \times \times \times \\ 00000000 \times \times \times \times \times \times \\ \hline 000011000110110 \end{array}$$

Hence;

$11000110110_{(2)}$  Ans

(C)  $10001000_{(2)} \div 00100010_{(2)}$

Soln

Quotient = 00000000

Now; subtracting divisor from dividend using 2's complement

$$\begin{array}{r} 10001000 \\ + 11011110 \\ \hline \end{array}$$

Discard bit  $\leftarrow 101100110$

Adding "1" to Quotient

$\Rightarrow 00000001$

Subtract divisor from ~~the~~ 1st partial remainder using 2's complement.

$$\begin{array}{r} 01100110 \\ + 11011110 \\ \hline \end{array}$$

Discard bit  $\leftarrow 101000000$

Adding "1" to quotient

$\Rightarrow 00000010$

Again

$$\begin{array}{r} 101000100 \\ + 11011110 \\ \hline \end{array}$$

Discard bit  $\leftarrow 100100010$

Adding "1" to quotient

$\Rightarrow 00000011$

$$\begin{array}{r} 00100010 \\ \hline 11011101 \rightarrow 1's \text{ complement} \\ \hline 11011110 \rightarrow 2's \text{ complement} \\ \hline \end{array}$$

Now again

$$\begin{array}{r} 00100010 \\ + 11011110 \\ \hline \end{array}$$

$\leftarrow 100000000$   
discarded

Adding "1" quotient

Quotient = 00000100

Ans

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" "

---

" "



d)  $6D_{16} - 3F_{16}$

Soln

Hexa converted to binary numbers

$$\begin{array}{r} 6 \\ \hline 0110 \end{array} \quad \begin{array}{r} D \\ \hline 1001 \end{array}$$

$6D_{(16)} = 01101001_{(2)}$

$$\begin{array}{r} 6D \\ + C1 \\ \hline 12E \end{array}$$

$$\begin{array}{r} 3 \\ \hline 0011 \end{array} \quad \begin{array}{r} F \\ \hline 1111 \end{array}$$

2's complement of 3F

$$\begin{array}{r} 00111111 \\ \hline 11000000 \end{array} \rightarrow \text{1's complement}$$

$$\begin{array}{r} 11000000 \\ \hline 11000001 \end{array} \rightarrow \text{2's complement}$$

$$\begin{array}{r} 1100 \\ \hline C \end{array} \quad \begin{array}{r} 0001 \\ \hline 1 \end{array}$$

Hence,  $\boxed{12E}$  Ans

e)  $(00010110)_{BCD} + 00010101_{BCD} = (??)_{10}$

Soln

$$\begin{array}{r} 0001 \quad 0110 \\ + 0001 \quad 0101 \\ \hline \end{array}$$

$0010 \quad 1010 \rightarrow$  ~~invalid~~ invalid due to (79)

Add to invalid code:

$$\begin{array}{r} 0010 \quad 1011 \\ + 0110 \\ \hline 0011 \quad 0001 \end{array}$$

$$\begin{array}{r} 0011 \\ \hline 3 \end{array} \quad \begin{array}{r} 0001 \\ \hline 1 \end{array}$$

Hence,  $\boxed{(31)}$  Ans

Q3) Apply CRC to data bits 11010011<sub>(2)</sub> using generator code 1010<sub>(2)</sub> to produce transmitted CRC code.

Soln  $D = 11010011$ ,  $G = 1010$

Since its four data bits add four to data byte

$$D' = 11010011 \boxed{0000}$$

Now, divided  $D'$  by  $G$ .

$$\begin{array}{r}
 110100110000 \\
 \underline{1010} \\
 1110 \\
 \underline{1010} \\
 1000 \\
 \underline{1010} \\
 1011 \\
 \underline{1000} \\
 1000 \\
 \underline{1010} \\
 100
 \end{array}$$

$R = 0100$  and add more four zeros  $\rightarrow 110100110100$

$$\begin{array}{r}
 110100110100 \\
 \underline{1010} \\
 1110 \\
 \underline{1010} \\
 1000 \\
 \underline{1010} \\
 1011 \\
 \underline{1010} \\
 1010
 \end{array}$$



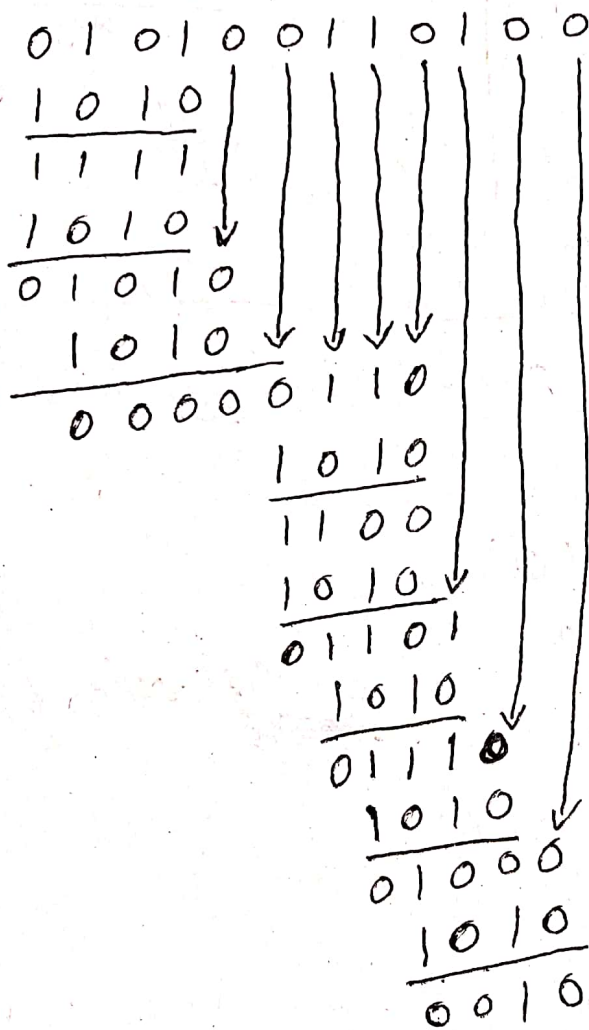
Q4) ASSUME that the code produced in problem Q.3 incurs an error in the most significant bit during transmission. APPLY CRC to detect the error.

Given:

Received Data =  $D' = 010100110100$

$C_1 = 1010$

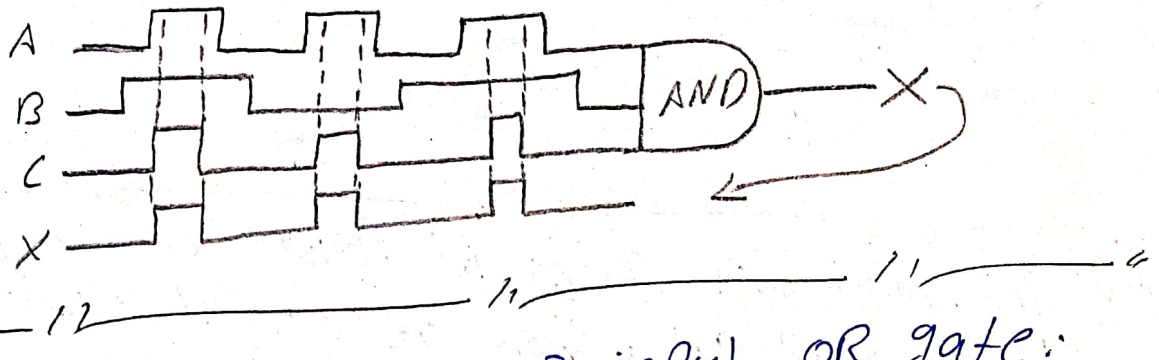
Req:-



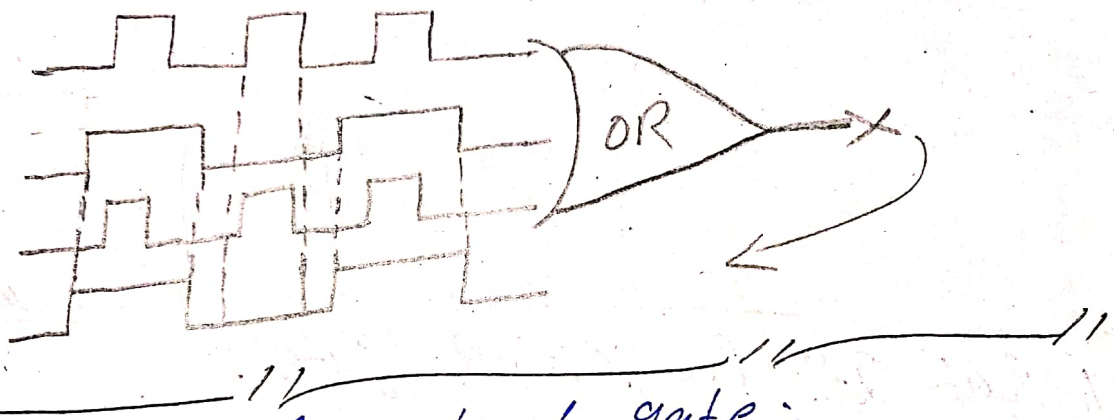
Reminder  $\neq 0$

Hence; Error has been occurred

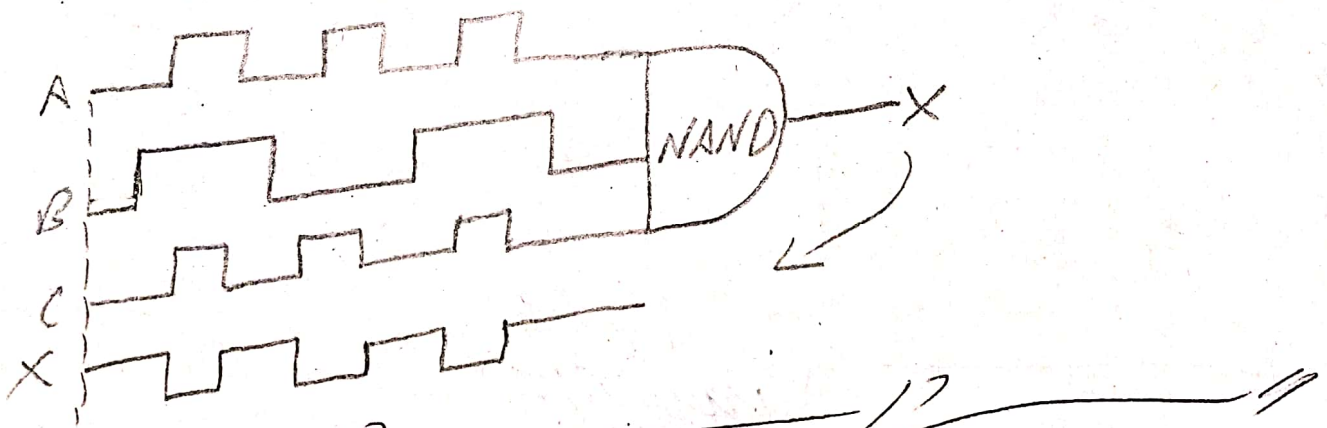
Q5) The input waveform in figure 1 is applied to a 3-input AND gate. Show the output waveform in proper relation to the inputs with a timing diagram.



Q6) Repeat Q.5 for a 3-input OR gate.



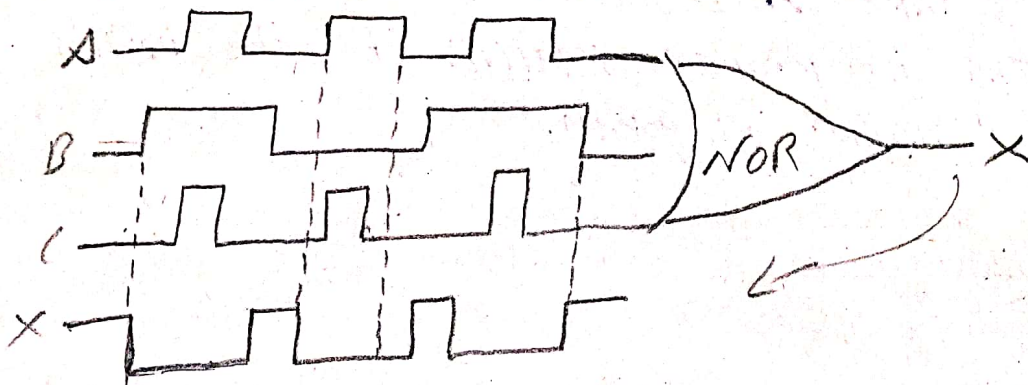
Q7) Repeat Q.5 for NAND gate.



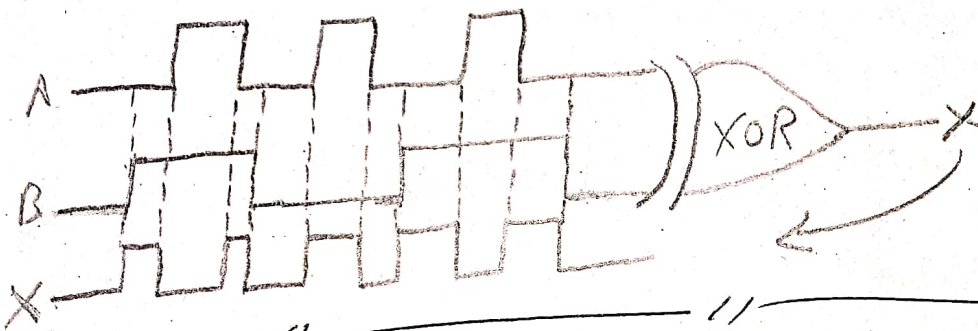
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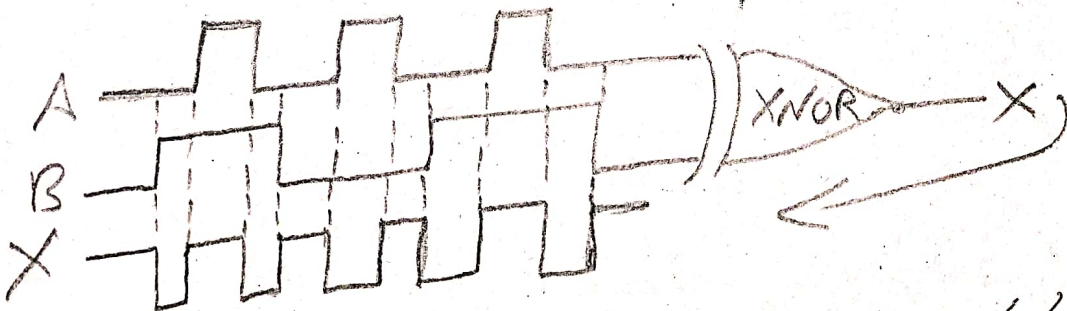
Q 8) Repeat Q 5 for NOR Gates



Q 9) The input waveform in figure are applied to a XOR gate, show the output waveform with timing diagram.



Q 10) Repeat Q. 9 for XNOR gate.



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Q11) using boolean algebra techniques, simplify the following expression as much as possible.

$$\bar{A}\bar{B} + A\bar{B}C + A\bar{B}CD + A\bar{B}CDE$$

Soln

using boolean algebra Rules

$$\Rightarrow \bar{A}\bar{B} + A\bar{B}C + A\bar{B}CD + A\bar{B}CDE$$

$$\Rightarrow \overline{A+AB} = A$$

$$\Rightarrow \bar{A}\bar{B} + A\bar{B}CD + A\bar{B}CDE$$

$$\Rightarrow \overline{A+AB} = A$$

$$\Rightarrow \bar{A}\bar{B} + A\bar{B}CDE$$

$$\Rightarrow \overline{A+AB} = A$$

$$\Rightarrow \bar{A}\bar{B} \text{ Answer}$$

R.W

$$\bar{A}\bar{B} + A\bar{B}C$$

$$\bar{A}\bar{B}(1+C)$$

$$\overline{A+1} = 1$$

$$\bar{A}\bar{B}(1)$$

$$(\bar{A}\bar{B})$$



Q.12) Convert the following expression into standard SOP form.

$$(C+D)(\bar{A}+D)$$

Soln

convert expression to SOP form

$$(C+D)(\bar{A}+D)$$

$$\Rightarrow C\bar{A} + CD + D\bar{A} + DD$$

$$\Rightarrow C\bar{A} + CD + D\bar{A} + DD$$

Domain of this SOP is ABC term CA is missing D.

$$\Rightarrow C\bar{A} = C\bar{A}(C+D) = C\bar{A}D + C\bar{A}\bar{D}$$

Term CD is missing A

$$\Rightarrow CD = CD(A+\bar{A}) = CDA + CD\bar{A}$$

Term D\bar{A} is missing C

$$\Rightarrow D\bar{A} = D\bar{A}(C+\bar{C}) = D\bar{A}C + D\bar{A}\bar{C}$$

Term DD is missing A and C

$$\Rightarrow DD = DD(A+\bar{A}) = DDA + DD\bar{A}$$

Term DDA and DD\bar{A} is missing C

$$\Rightarrow DDA = DDA(C+\bar{C}) = DDAC + DDA\bar{C}$$

$$\Rightarrow DD\bar{A} = DD\bar{A}(C+\bar{C}) = DD\bar{A}C + DD\bar{A}\bar{C}$$

Hence;

$$(C\bar{A}D + C\bar{A}\bar{D} + CDA + CD\bar{A} + D\bar{A}C + D\bar{A}\bar{C} + DDA + DDA\bar{C} + DD\bar{A}C + DD\bar{A}\bar{C})$$

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Date: \_\_\_\_\_

Q.13) Write standard POS expression using standard POS expression from Q.12.

$$\bar{C}\bar{A}D + C\bar{A}\bar{D} + D\bar{A}\bar{C} + ACD$$

Soln

Evaluation of the POS expression is

$$(101) + (100) + (110) + (111)$$

Since there are three variables in the domain of this expression, there are  $2^3 = 8$  possible combinations. Four of which are contained by the expression the rest are,

$$000, 010, 011, 001$$

Hence, the equivalent POS expression

$$\text{is } (A+B+C)(A+\bar{B}+C)(A+B+\bar{C})(A+\bar{B}+\bar{C})$$

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Q14) Draw a sing Truth for both standard Pos and Standard sop expression obtained in Q.12 and Q.13.

A	C	D	X	Pos/SOP
0	0	0	0	$(A+C+D)$
0	0	1	0	$(A+C+\bar{D})$
0	1	0	0	$(A+\bar{C}+D)$
0	1	1	0	$(A+\bar{C}+\bar{D})$
1	0	0	1	$(A\bar{C}\bar{D})$
1	0	1	1	$(A\bar{C}D)$
1	1	0	1	$(AC\bar{D})$
1	1	1	1	$(ACD)$

Pos Expression

$$(A+C+D)(A+\bar{C}+D)(A+\bar{C}+\bar{D})(A+C+D)$$

SOP Expressions

$$(C\bar{A}\bar{D}) + (C\bar{A}D) + (CDA) + (D\bar{A}C)$$

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Date: \_\_\_\_\_

Q15) Use Karnaugh map to simplify the following expression to minimum SOP form  
soln

$$\bar{A}\bar{B}\bar{C} + \bar{A}BC + A\bar{B}\bar{C} + ABC$$
$$000 + 011 + 101 + 110$$

AB	C	0	1
00	1	1	0
01	0	0	1
11	1	0	0
10	0	1	1

→  $(\bar{A}\bar{B}\bar{C})$   
→  $(\bar{A}BC)$   
→  $(A\bar{B}\bar{C})$   
→  $(ABC)$

$(\bar{A}\bar{B}\bar{C}) + (\bar{A}BC) + (A\bar{B}\bar{C}) + (ABC)$  minimum SOP

Q16) Obtain the minimum POS expression from K-m used in Q.15.

soln

AB	C	0	1
00	1	0	1
01	0	1	0
11	1	0	1
10	0	1	0

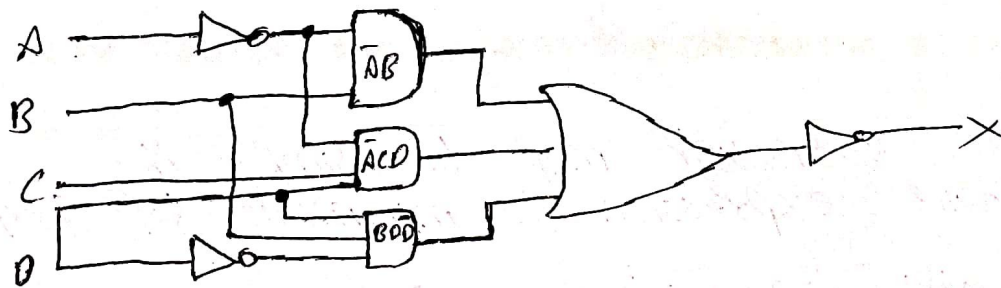
→  $(A+B+\bar{C})$   
→  $(A+\bar{B}+C)$   
→  $(A+B+\bar{C})$   
→  $(\bar{A}+B+C)$

$(A+B+\bar{C})(A+\bar{B}+C)(A+B+\bar{C})(\bar{A}+B+C)$  is the minimum POS expression

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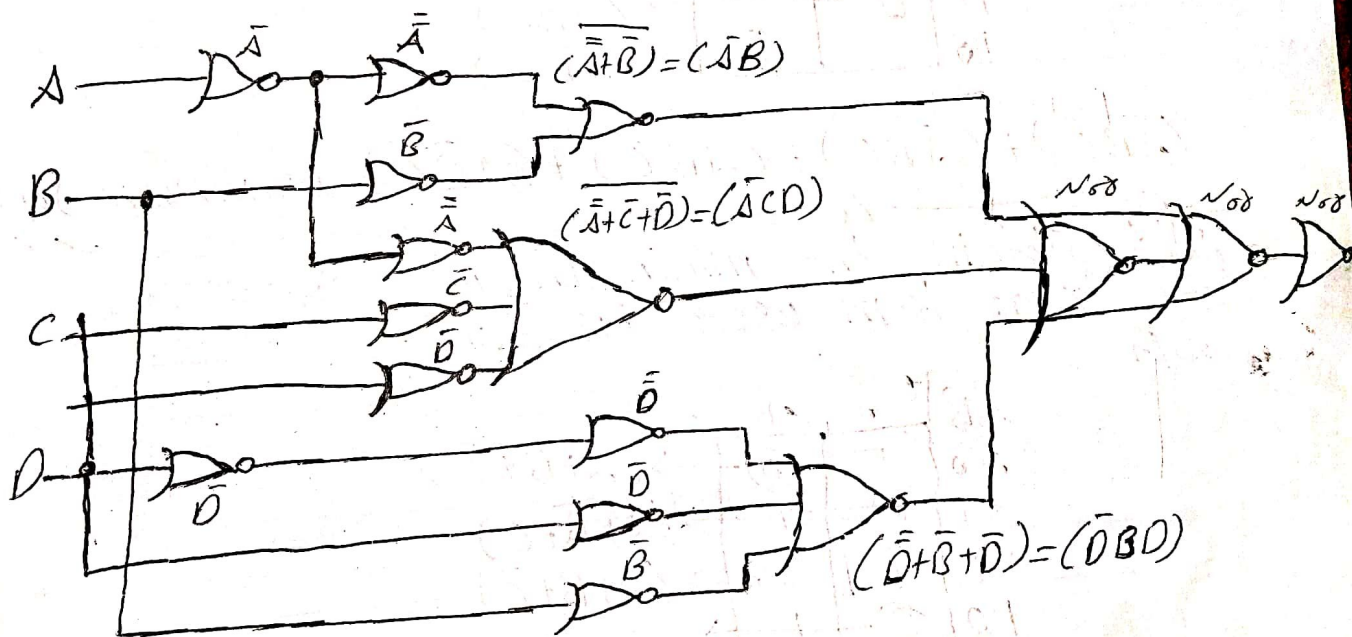


Q17) write the output expression for circuit in figure:



$$X = \overline{(\overline{A}B)} + (\overline{A}CD) + (B\overline{D}\overline{D})$$

Q18) Implement the logic circuit using only NOT gate.



$$X = \overline{\overline{(\overline{\overline{A+B}})(\overline{\overline{A+C+D}})(\overline{\overline{\overline{D}+B+D}})}} = (\overline{A}B) + (\overline{A}C\overline{D}) + (\overline{D}B\overline{D})$$

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Date: \_\_\_\_\_

Q19) Same as Q18:

Q20) Implement a logic circuit for the truth table in table (1).

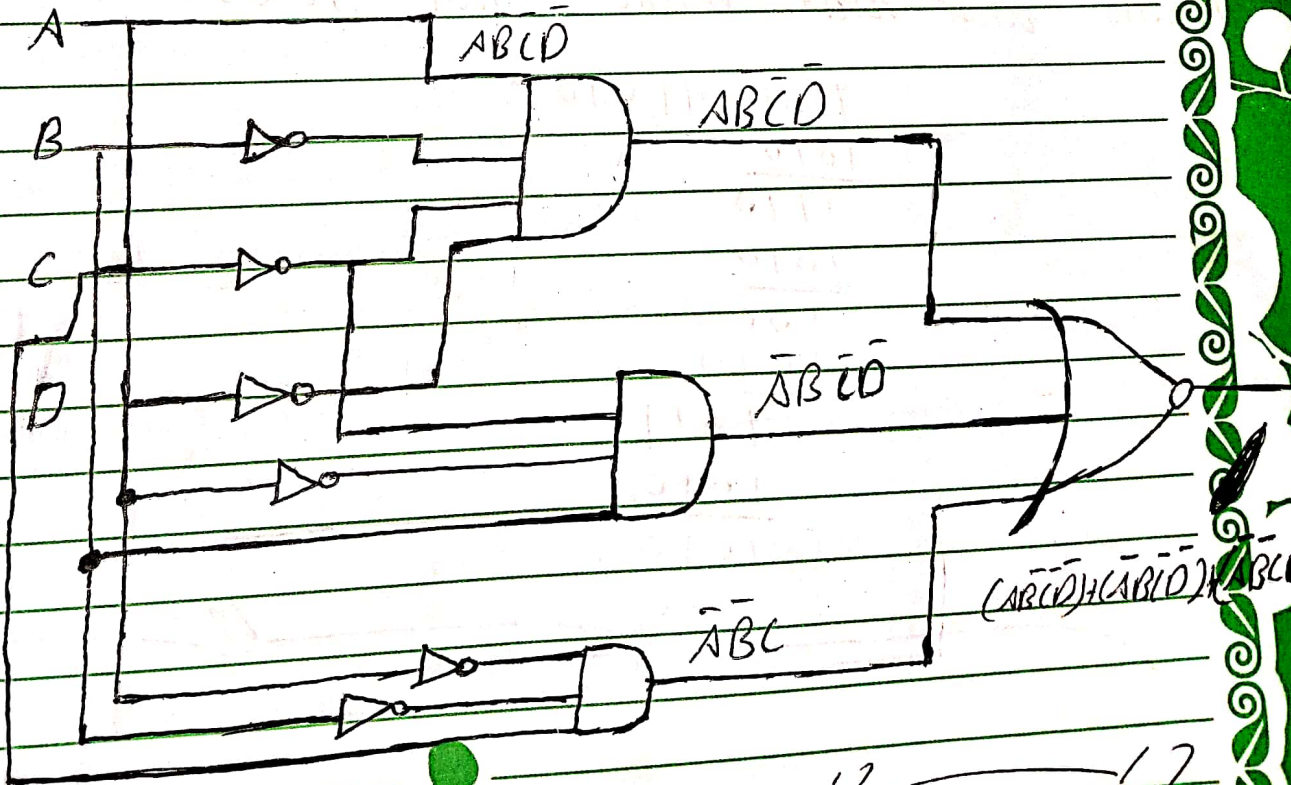
SA

Obtain expression from truth table is

$$(\bar{A}\bar{B}C\bar{D}) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}\bar{D}) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}D)$$

By reducing expression using boolean laws and rules:

$$(\bar{A}\bar{B}C\bar{D}) + (\bar{A}B\bar{C}\bar{D}) + (\bar{A}B\bar{C})$$



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