Department of Electrical Engineering

Sessional Assignment Date: 05/05/2020

Course Details

Course Title:	Signals & Systems	Module:	04
Instructor:		Total Marks:	20

Student Details

Name: Student ID:

Q1.	Evaluate the even and odd components for the given function.	Marks
	5	05
	2 4 3 x[n] 1	CLO 1
	1 2 3 4	
Q2.	Calculate the inverse Laplace transform of the given equation.	Marks
		07
	$Y(s) = \frac{s+4}{s^2 + 4s - 12}$	CLO 3
Q3. i	Discuss the procedure of converting an analog signal into a digital one.	Marks
i	Suppose an analog signal has a highest frequency of 60Hz. Outline the steps that will	02+02
	ensure that no aliasing occurs.	CLO 2
Q4.	Show that:	Marks
	$x[n] * [h_1[n] * h_2[n]] = [x[n] * h_1[n]] * h_2[n]$	04
		CLO 2

Department of Electrical Engineering

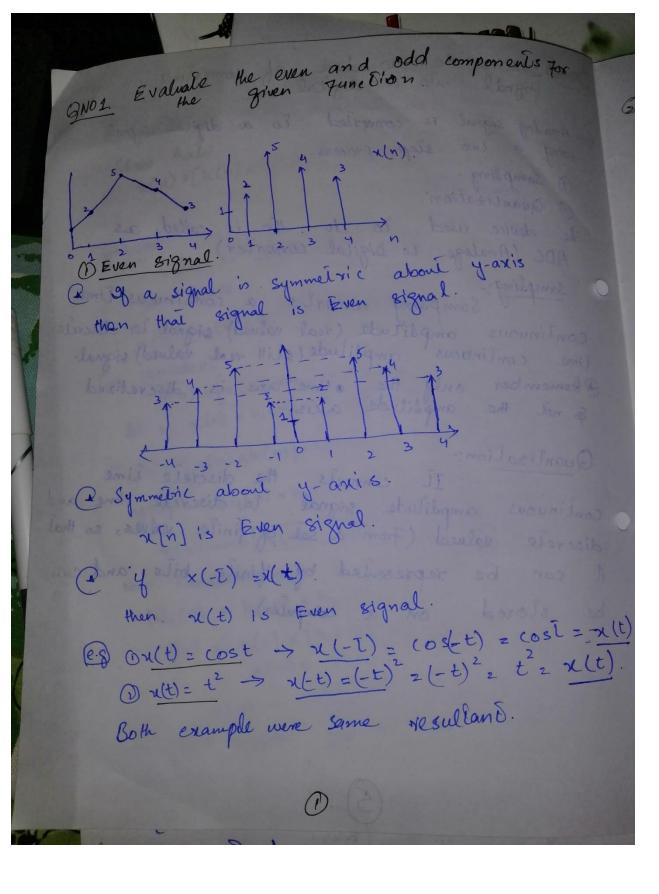
Course Title: Signals & Systems

Module: 4th semester

Student Detail

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Roll No :14563



godd signal:if 2(-E) 2 - x(E). than x(t) = ODD signal. of the orignal, then signal is odd signal. (Dx(t) = sin [-> x(-[) = sin (-[) = - sin [/-x(t)] I of a signal is symmetric about the origin, then OIT is ODD signal. & ODD signal always passes through the origin. Mathimatically $\chi(t)=0 \longrightarrow \text{ for } t=0$.

(For each and every signal). le Here we have I at origin. (2)

x(m). So this is Not odd signal.

Wis is only Even signal. QNO2: Calculate the Inverse Laplace Transform of me given Equation. $V(s) = \frac{S+4}{S^2+4s-12}$ $= \frac{S+4}{S^2+4s-12}$ 2 5 +68-25-12 2 (5+6) (5-2). 2 5+4 (8-2) (5+6) $\frac{S+4}{(S-2)(S+6)} = \frac{A}{S-2} + \frac{13}{S+6}$ S+4 = A(s+6) + B(s-2). S-220 2 'S=2 Put the value. 2+42 A (2+6) + B(2-2). 6 2 8A + B(0). 3 6 2 8A A2 3/4 . A whole Holling S+42 A(S+6) + B(S-2). .: Now S+620 2[5=6]. -6+4= A(-6+6) +B(-6-2). -22 A(0) -8B. 12 2 + 8B = B = 1 So V(s) = 3 (1/5=2) + 4 (5+6). = \frac{3}{4} \frac{1}{5-2} + \frac{1}{4} \frac{1}{5+k}.
= \frac{3}{4} \frac{1}{6} + \frac{1}{4} \frac{1}{6} \frac{1}{5+k}.

QN03@ Discuss the procedure of converting an analog signal into a digital one. Ans: Analog signal is converted to a digital signal using a two step process. 1 Sampling @ Quantization. the device used to do this is called as ADC (Analoge To Digital converter). Sampling: Sampling converts a continuous lime. continuous amplitude (real valued) signal la discrete Eine continuous amplitude (still real valued) signal. Exemember only the otime axis is discretized a not the amplitude axis. Quantization: Il converts the discrete time continuous amplitude signal to discrete time and discrele valued (From a set of finite values, so that it can be represented by finite bits and can be sloved on a computer).

QNO4 Show that. $\chi(n) \times [h_1(n) \times h_2(n)] = [\chi(n) \times h_1(n)] \times h_2(n)$

Solution: This is associative property holds true this case.

 $\chi(n) \times [h_1(n) \times h_1(n)] = [\chi(n) \times h_1(n)] \times h_2(n).$

det consider:

y(n) = [x(n) xh,(n)] xh2(n). let, ~(n) + h, (n) = W, (n).

So $y(n) = [x(n) \times h_1(n)] \times h_2(n) \longrightarrow \textcircled{1}$

y(n) = W1(n) x h2(n).

W1(n) > [h, (n)] $\chi(n) \longrightarrow h_{\underline{1}}(n)$

Now Consider that. L

W2(n) = h,(n) x h2(n).

y(n) = x(n) x [h, (n) x h2(n)]. 2 x(n) x W2(n).

 $\chi(n) \longrightarrow [W_{\lambda}(n)] \longrightarrow \gamma(n)$.

Both block diagrams give the same Response. Hence.

 $[n(n) \times h_1(n)] \times h_2(n) = x(n) \times [h_1(n) \times h_2(n)]$ Proved.