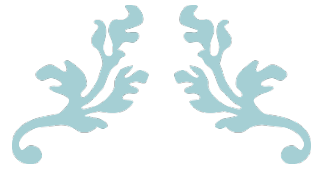


Name: Hassan Mehdi  
ID: 15453  
Teacher: Sir. Mohammad Amin  
Program: BC (CS)  
Subject: Digital Logic Design  
Midterm-Assignment  
Course Code (CS): CSC-201  
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# DIGITAL LOGIC & DESIGN

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Midterm-Assignment



Date: .....

Q.1: Convert each of the following

(a)  $45.25_{10} = (?)_2$

Given:

$$45.25_{10}$$

Required:

Convert  $45.25_{10}$  to binary number system.

Sol

Using Repeated division for 45.

2	45	
2	22	1
2	11	0
2	5	1
2	2	1
2	1	0

$$(45)_{10} = (101101)_2$$

Now for fractional part we use repeated multiplication method.

$$\Rightarrow 0.25 \times 2 = 0.50 \rightarrow 0$$

$$\Rightarrow 0.50 \times 2 = 1.00 \rightarrow 1$$

hence

$$45.25_{(10)} = (101101.01)_2$$

∴ Answer

Date: .....

$$(b) 10000000.1010_2 = (?)_{10}$$

Given:

$$10000000.1010_{(2)}$$

Required:

To convert the given binary number into decimal number system.

Sol

Using weighted notation

$$\begin{aligned} & (1 \times 2^7) + (1 \times 2^{-1}) + (1 \times 2^{-2}) \\ \Rightarrow & (1 \times 128) + (0.5) + (0.25) \end{aligned}$$

$$\Rightarrow 128.625_{(10)} \text{ Answer}$$

$$(c) 4D7F_{(16)} = (?)_{10}$$

Given:  $4D7F_{(16)}$

Required: Convert the given Hex number into decimal number system.

Sol

Using weighted notation.

$$(4 \times 16^3) + (13 \times 16^2) + (7 \times 16^1) + (15 \times 16^0)$$

$$\Rightarrow (4 \times 4096) + (13 \times 256) + (7 \times 16) + (15 \times 1)$$

$$\Rightarrow 16384 + 3328 + 112 + 15$$

$$\Rightarrow (19839)_{10} \text{ Answer}$$

Date: .....

$$(d) 128_{(10)} = (? )_{16}$$

Given:

$$128_{(10)}$$

Required:

Hex equivalent of the given decimal number.

Sol Using repeated division by 16.

16	128	.....
16	8	0

hence

$$128_{(10)} = (80)_{16} \quad \text{Answer}$$

$$(e) 3ABF_{(16)} = (? )_2$$

Given:

$$3ABF_{(16)}$$

Required:

To convert the given hex number into binary number system

Sol

Using Hex-Binary table to convert  
 $3ABF_{(16)}$  to Binary.

<u>3</u>	<u>A</u>	<u>6</u>	<u>F</u>
0011	1010	0110	1111

hence

$$3ABF_{(16)} = 11101001101111_{(2)} \text{ Answer.}$$

$$(f) 110000111100101_{(2)} = (?)_{16}$$

Given

$$110000111100101_{(2)}$$

Required

Hex equivalent of given binary number

Sol

Using Groups of four.

<u>1100</u>	<u>0011</u>	<u>1110</u>	<u>0101</u>
C	3	E	5

hence

$$110000111100101_{(2)} = C3E5_{(16)} \text{ Answer}$$

Date: .....

$$(g) 6173_8 = (?)_{10}$$

Given:

$$6173_8$$

Required:

Decimal Equivalent of  $6173_8$

Sol

Using weighted notation

$$\begin{aligned} & (6 \times 8^3) + (1 \times 8^2) + (7 \times 8^1) + (3 \times 8^0) \\ \Rightarrow & (6 \times 512) + (1 \times 64) + (7 \times 8) + (3 \times 1) \\ \Rightarrow & (3072) + (64) + (56) + (3) \\ \Rightarrow & (3195)_{10} \end{aligned}$$

hence

$$6173_8 = 3195_{10} \quad \text{Answer.}$$

$$(h) 169_{10} = (?)_8$$

Given:

$$169_{10}$$

Required:

Octal Equivalent of  $169_{10}$



Sol

using repeated division by 8

$$\begin{array}{r|l} 8 & 169 \\ 8 & 21 \quad 1 \\ 8 & 2 \quad 5 \end{array}$$

here  $169_{(10)} = (251)_8$  Answer

(i)  $2A7D_{(16)} = (?)_8$

Given:

$2A7D_{(16)}$

Required:

Octal Equivalent of  $2A7D_{(16)}$

Sol

First convert  $2A7D_{(16)}$  to Binary number using groups of four. (Table)

$$\begin{array}{c} \underline{2} \\ 0010 \end{array} \quad \begin{array}{c} \underline{A} \\ 1010 \end{array} \quad \begin{array}{c} \underline{7} \\ 0111 \end{array} \quad \begin{array}{c} \underline{D} \\ 1101 \end{array}$$

Now, convert the obtained binary numbers into octal number using groups of 3.



$$(5) \quad 11111111_2 = \pm (?)_{10}$$

Given:

$$(11111111)_2$$

Required:

Decimal equivalent of given signed Binary no.

Sol

Using 2's Complement

$$\begin{array}{r} 11111111 \\ + 00000000 \quad \text{1's Complement} \\ \hline 00000001 \quad \text{2's Complement} \end{array}$$

$$(1 \times 2^0) = 1_{10} \quad \text{Answer}$$

$$(k) -12_{(10)} = (?)_2$$

Given:

$$-12_{(10)}$$

Required:

Binary equivalent of  $-12_{(10)}$

Sol

First find Binary equivalent of  $12_{(10)}$  using repeated division by 2.

2	12		
2	6	0	
2	3	0	
2	1	1	

$$12_{(10)} = 1100_{(2)} \Rightarrow 00001100_{(2)}$$

Now taking 2's complement of obtained number.

00001100	
11110011	
+	1
11110100	

1's complement  
2's complement.

hence

$$-12_{(10)} = 11110100_{(2)} \text{ Answer.}$$

Date: .....

(L)  $198 = (?)_{BCD}$

Given:

$198_{(10)}$

Required:

Convert the given decimal number to binary coded decimal

sol

Using Dec-BCD table

1	9	8
0001	1001	1000

hence

$198_{(10)} = 00010011000_{BCD}$

(M)  $1000110000_{BCD} = (?)_{10}$

Given:

$1000110000_{BCD}$

Required:

Decimal equivalent of the given BCD number.

Sol

Using BCD-Decimal table

$$\frac{1000}{8} \quad \frac{0111}{7} \quad \frac{0000}{0}$$

hence  $100001110000 \text{ Bcd} = 870 (10) \text{ Answer}$

(7)  $1001010_2 = (?) \text{ gray}$

Given:

$1001010_2$

Required:

Gray equivalent of  $1001010_2$

Sol

$$\begin{array}{cccccccc} 1 & \rightarrow & +0 & \rightarrow & +0 & \rightarrow & +1 & \rightarrow & +0 & \rightarrow & +1 & \rightarrow & +0 \\ \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow \\ 1 & & 1 & & 0 & & 1 & & 0 & & 1 & & 0 \end{array}$$

hence

$$1001010_2 = 110111 \text{ gray}$$

Answer

Date: .....

(Q)  $10101111_{\text{gray}} = (?)_2$

Given:

$10101111_{\text{gray}}$

Required:

Binary Equivalent of  $10101111_{\text{gray}}$

Sol

1	0	1	0	1	1	1	1
↓	↗+	↓	↗+	↓	↗+	↓	↗+
1	1	0	0	1	0	1	0

hence

$10101111_{\text{gray}} = 11001010_{(2)}$

(P)  $01000001 = (?)_{\text{ASCII}}$

Given:

$01000001_{(2)}$

Required:

ASCII Equivalent of  $01000001_{(2)}$

Sol

using ASCII table

$$(1 \times 2^6) + (1 \times 2^0)$$

$$(1 \times 64) + (1 \times 1)$$

$$64 + 1$$

$$\Rightarrow 65_{(10)}$$

65<sub>10</sub> = (A) ASCII character. Answer

(Q) 111000 = (? 111000) Even parity.

Given:

111000<sub>(2)</sub>

Required:

Attach Even parity bit to (111000)

Sol

Since there has to be even amount of 1's in an even parity number, we add 1 to the given number.

1111000  $\Rightarrow$  1111000<sub>(2)</sub> Answer



Date: .....

Q2: Calculate each of the following.

$$(9) 01111111_{(2)} - 00000111_{(2)}$$

Sol

Taking 2's Complement on

$$\begin{array}{r} 00000111 \\ + 11111000 \quad \text{1's Complement} \\ \hline 11111001 \quad \text{2's Complement} \end{array}$$

Now

$$\begin{array}{r} 10111111 \\ + 11111001 \\ \hline \end{array}$$

$$\begin{array}{r} 10111000 \\ \uparrow \end{array}$$

Discarded bit

hence  $01111000_{(2)}$  Answer

(b)  $01101010_{(2)} \times 11110001_{(2)}$

Sol

Using 2's Complement:

11110001	
00001110	1's Complement
00001111	2's Complement

Now

00001111	
01101010	
00000000	

100001111x	
00000000xx	
00001111xxx	
00000000xxxx	
00001111xxxxx	
00001111xxxxx	
00000000xxxxx	

---

000011000110110

hence

$11000110110_{(2)}$  Ans

Date: .....

$$(C) 10001000_2 \div 00100010_2$$

Sol

$$\text{Quotient} = 00000000$$

Now Subtracting divider from dividend using 2's complement.

$$\begin{array}{r} 10001000 \\ + 11011110 \\ \hline \text{discard.} \leftarrow 10110010 \end{array}$$

$$\begin{array}{r} 00100010 \\ 11011101 \text{ 2's Com} \\ \hline 11011110 \text{ 2's Com} \end{array}$$

Adding 1 to quotient.

$$= 00000001$$

Subtracting divider from 1st partial remainder using 2's comp.

$$\begin{array}{r} 01100110 \\ + 11011110 \\ \hline \text{discard.} \leftarrow 101000100 \end{array}$$

Discard  $\leftarrow 101000100$

Adding 1 to quotient.

$$= 00000010$$

Again

$$\begin{array}{r} 01000100 \\ + 11011110 \\ \hline \text{discard.} \leftarrow 100100010 \end{array}$$

Discard  $\leftarrow 100100010$

Adding 1 to quotient

$$= 00000011$$

Now begin

$$\begin{array}{r} + \begin{array}{cccccccc} 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & 1 & 1 & 1 & 0 \end{array} \\ \hline \text{Discard.} \leftarrow 1000000000 \end{array}$$

Adding 1 to Quotient

$$\text{Quotient} = 00000100 \quad \text{Answer}$$

$$(d) \quad 6D_{16} - 3F_{16}$$

Sol Using 2's Complement on  $3F_{16}$

$$\begin{array}{r} 3 \\ \hline 0011 \end{array} \quad \begin{array}{r} F \\ \hline 1111 \end{array}$$

$$3F_{16} = 00111111$$

Taking 2's Complement

$$\begin{array}{r} 1100 \quad 0001 \\ \hline C \quad 1 \end{array}$$

$$\begin{array}{r} 00111111 \\ + 01100000 \quad 1's \text{ Co} \\ \hline 1 \quad 2's \text{ Co} \\ \hline 11000001 \end{array}$$

Now adding  $6D_{16}$  and  $C1$

$$\begin{array}{r} 6D \\ + C1 \\ \hline 12E \end{array} \Rightarrow$$

$$= F = \boxed{12E} \text{ Ans}$$

Date: .....

$$(e) 00010110 \text{ BCD} + 00010101 \text{ BCD} = (?)_{10}$$

Sol

$$\begin{array}{r} 0001 \quad 0110 \\ + 0001 \quad 0101 \\ \hline 0010 \quad 1010 \rightarrow \text{invalid due to } (>9) \end{array}$$

Add 6 to invalid code

$$\begin{array}{r} 0010 \quad 1011 \\ + 0110 \\ \hline \end{array}$$

$$0011 \quad 0001$$

$$\begin{array}{r} 0011 \quad 0001 \\ \hline 3 \quad 1 \end{array} \quad \text{Answer}$$

Date: .....

Q3: Apply CRC to the Data bits  
11010011<sub>2</sub> using generator  
code 1010<sub>2</sub> to produce the transmitted  
CRC code.

Given:

$$D = 11010011_2$$

$$G = 1010$$

$$D' = 110100110000$$

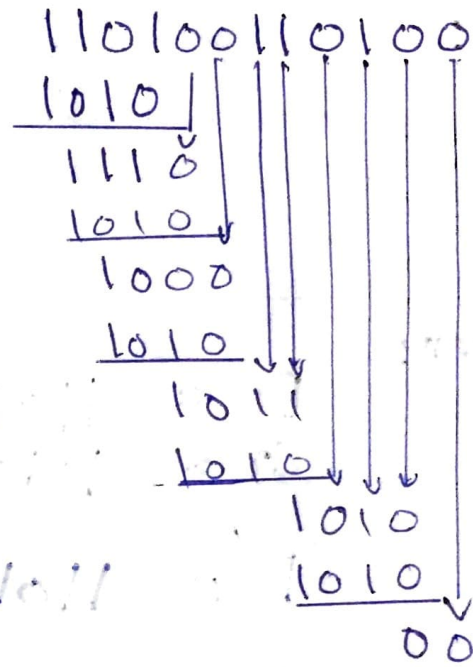
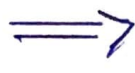
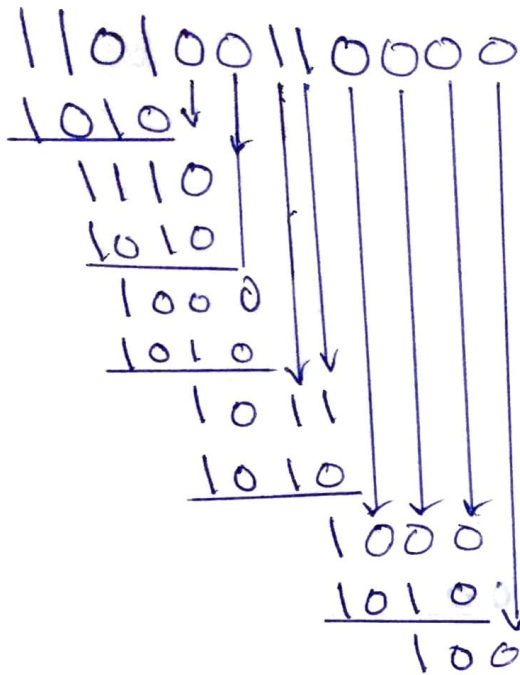
Required:

Transmitted CRC code

Sol

using modulo-2 operation

$$\frac{D'}{G} = \frac{110100110000}{1010}$$



Since Remainder  $\neq 0$ , we append that data bits with remainder end and do modulo-2 operation again.

Remainder = 0

110100110100 is the Answer.



Date: .....

Q4: Assume that code produced in Q3 has error occur in the most significant bit during transmission. Apply CRC to detect the error.

Given:

Received Data =  $D' = 010100110100$

$G = 1010$

Required:

To detect the error in the transmitted CRC code.

010100110100

1010

1111

1010

01010

1010

00000110

1010

1100

1010

01101

1010

01110

1010

01000

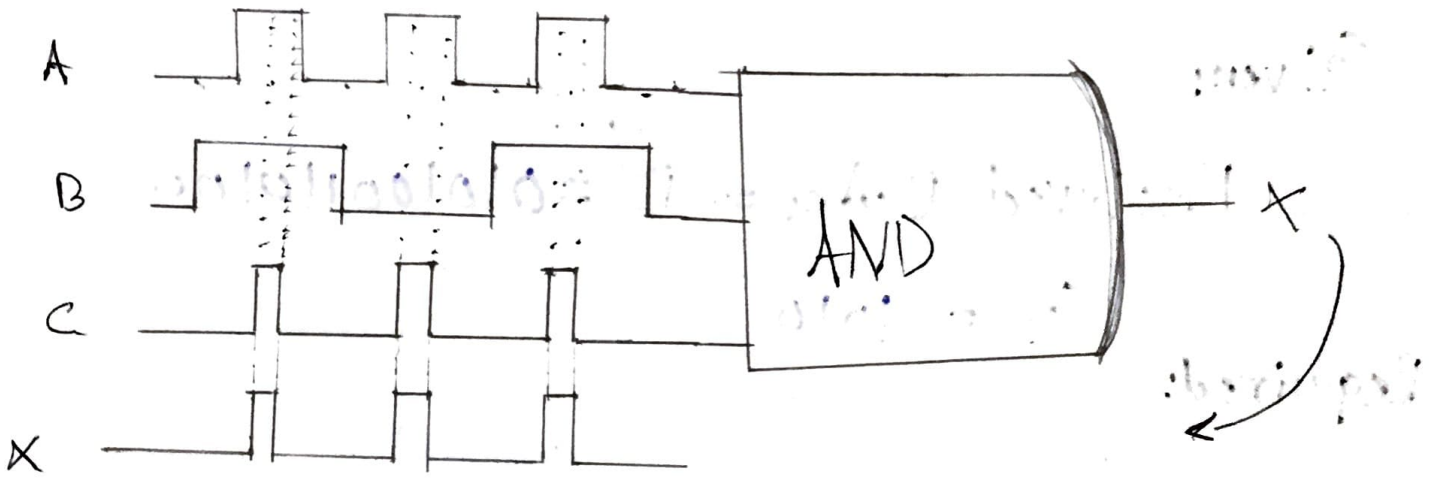
1010

0010

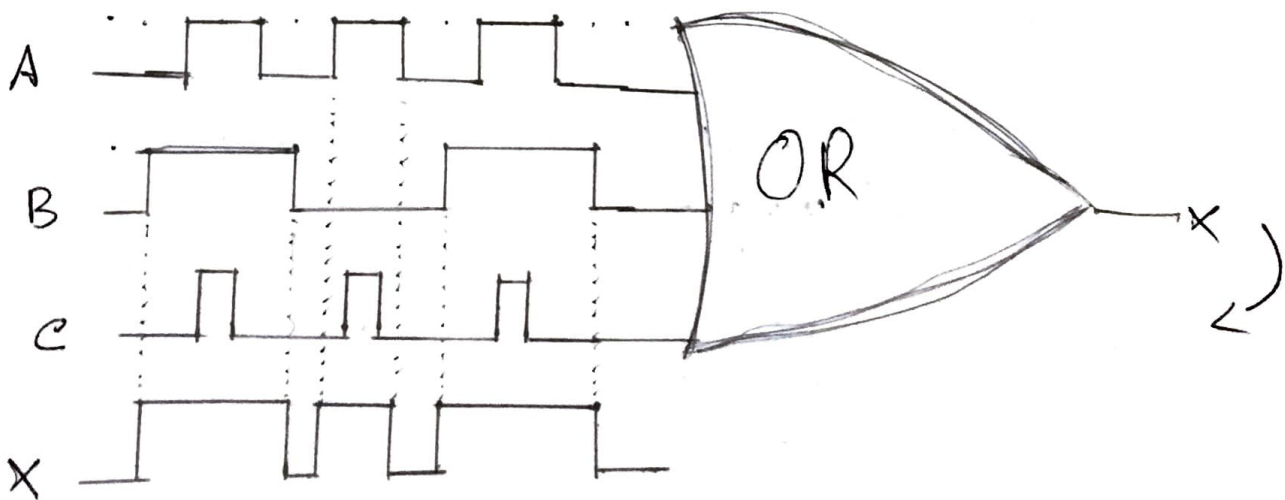
Remainder  $\neq 0$

hence Error has occurred.

**Q5:** The input waveforms in figure are applied to a 3-input AND gate. Show the output waveform with timing diagram.

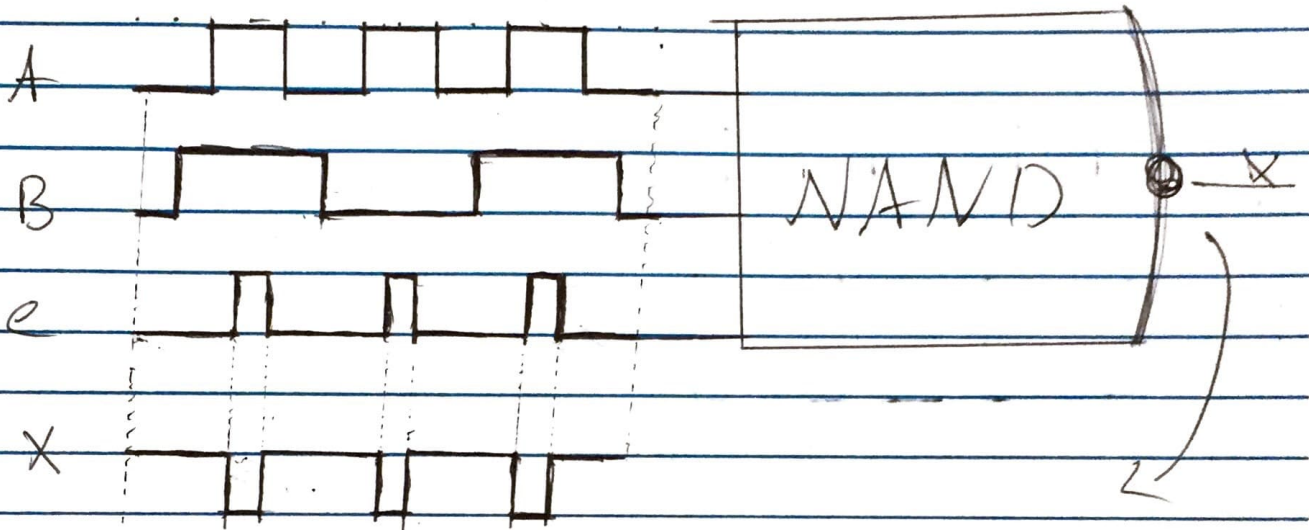


**Q6:** Repeat Q5 for OR gate.

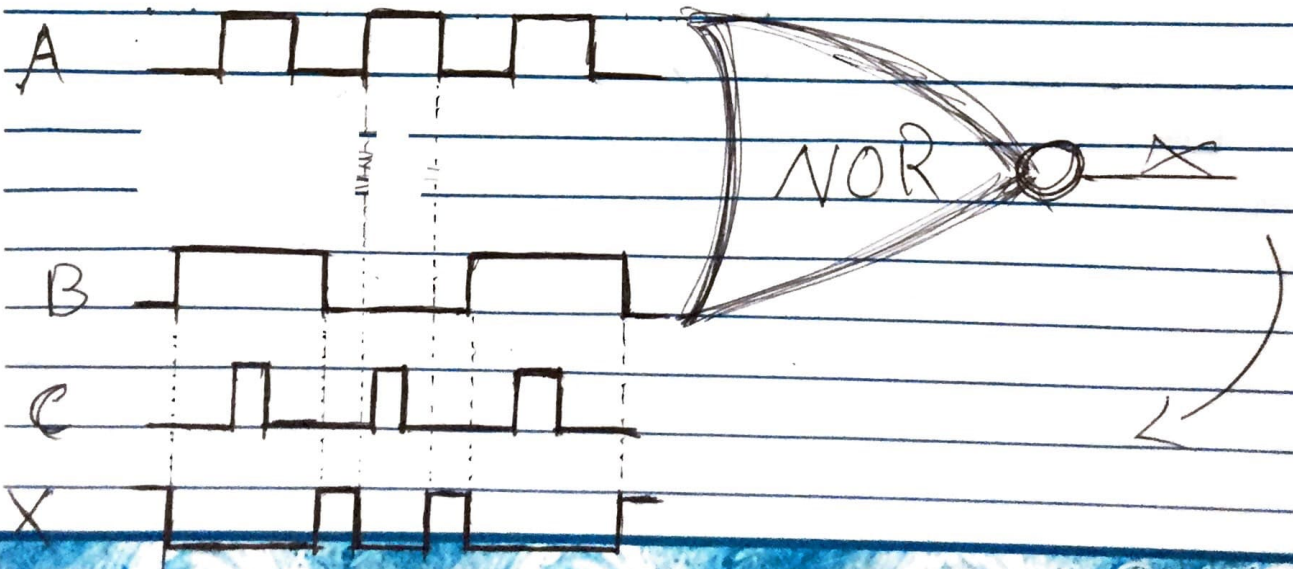


Date: .....

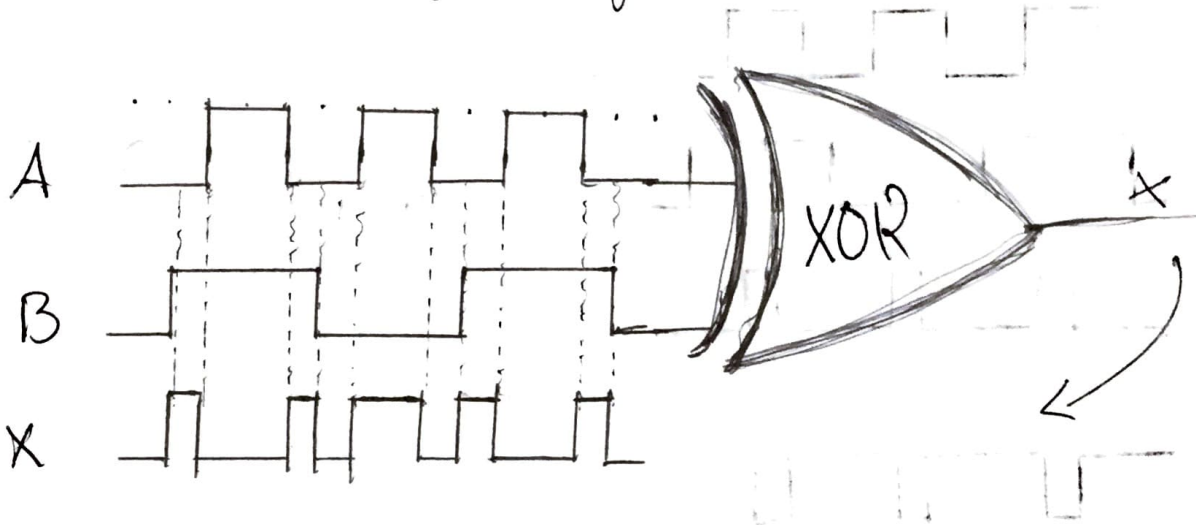
Q7: Repeat Q5, for NAND Gate



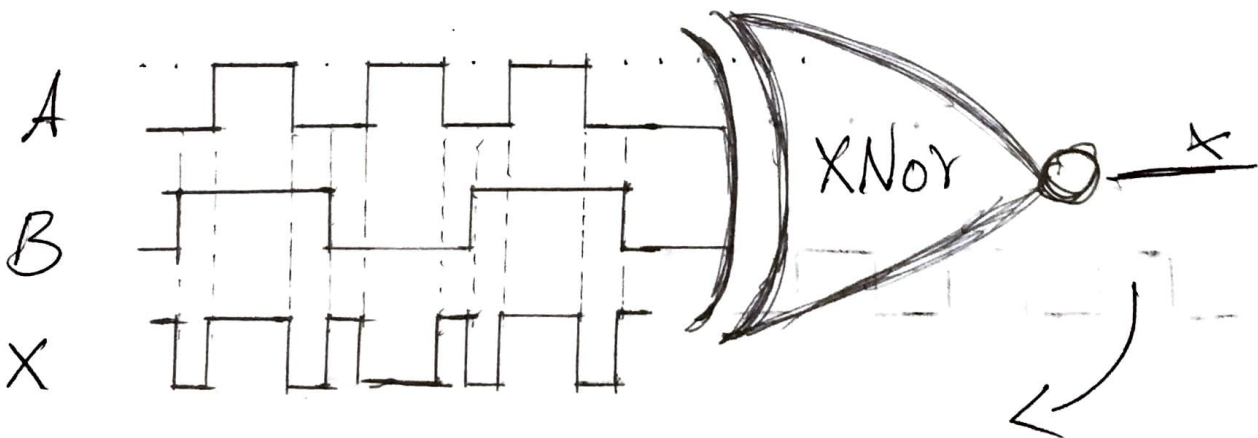
Q8: Repeat Q5 for NOR Gate



Q9: The input waveforms in figure are applied to a XOR gate, Show the output waveform with timing diagram.



Q10: Repeat Q9 for XNOR gate.



Date: .....

Q 11: Using boolean algebra techniques, simplify the following expressions as much as possible.

$$A\bar{B} + A\bar{B}C + A\bar{B}CD + A\bar{B}CDE$$

Sol Using Boolean Algebra Rules

$$\Rightarrow \frac{A\bar{B} + A\bar{B}C + A\bar{B}CD + A\bar{B}CDE}{(A + AB = A)}$$

$$\Rightarrow \frac{A\bar{B} + A\bar{B}CD + A\bar{B}CDE}{A + AB = A}$$

$$\Rightarrow \frac{A\bar{B} + A\bar{B}CDE}{A + AB = A}$$

( $A\bar{B}$ ) Answer

Rough work

$$A\bar{B} + A\bar{B}C$$
$$A\bar{B}(1 + C)$$

$$A\bar{B} = 1$$

$$A\bar{B}(1)$$
$$(A\bar{B})$$

Q12: Convert the following expression to standard SOP form.

Sol  $(C+D)(\bar{A}+D)$   
 $\Rightarrow (C+D)(\bar{A}+D)$

$\Rightarrow C\bar{A} + D$

Domain of this SOP is  $ACD$

$\Rightarrow$  Term  $C\bar{A}$  is missing  $D$ .

$= C\bar{A} = C\bar{A}(D+\bar{D}) = C\bar{A}D + C\bar{A}\bar{D}$

$\Rightarrow$  Term  $D$  is missing  $C$  and  $A$

$= D = D(A+\bar{A}) = DA + D\bar{A}$

$\Rightarrow$  Term  $DA$  and  $D\bar{A}$  is missing  $C$ .

$= DA = DA(C+\bar{C}) = DAC + DA\bar{C}$

$= D\bar{A} = D\bar{A}(C+\bar{C}) = D\bar{A}C + D\bar{A}\bar{C}$

Joining All the terms

$(C\bar{A}D + C\bar{A}\bar{D} + DAC + DA\bar{C} + D\bar{A}C + D\bar{A}\bar{C})$

Answer

Converting to SOP  
 $(C+D)(\bar{A}+D)$   
 $\Rightarrow C\bar{A} + CD + DA + DD$   
 $\frac{DD}{AA=A}$

$= C\bar{A} + CD + \frac{\bar{A}D + D}{A+AB=A}$

$= C\bar{A} + \frac{CD + D}{A+AB=A}$

$\Rightarrow C\bar{A} + D$

Q13: Write standard POS expression  
using the standard SOP expression from

Q12.

Sol

POS Expression

$$(C\bar{A}D) + (C\bar{A}\bar{D}) + (DAC) + (DA\bar{C}) + (D\bar{A}C) + (D\bar{A}\bar{C})$$

There are three variables in the domain  
of this expression.

Total possible combinations are  $2^3 = 8$ .

Combinations contained in this POS expression are

$$\Rightarrow (101) + (100) + (111) + (110) + (101) + (100)$$
$$\Rightarrow (101) + (100) + (111) + (110)$$

Remaining combinations are:

$$000, 010, 011, 001$$

hence the POS expression is

$$(A+B+D)(A+\bar{C}+D)(A+\bar{C}+D)(A+B+\bar{D})$$

Q14: Draw a Sing Truth for both standard POS and standard SOP expressions obtained in Q-12 and Q-13.

	A	C	D	X	POS/SOP
	0	0	0	0	$(A + \bar{C} + \bar{D})$
	0	0	1	0	$(A + \bar{C} + D)$
	0	1	0	0	$(A + \bar{C} + \bar{D})$
	0	1	1	0	$(A + \bar{C} + D)$
	1	0	0	1	$(A\bar{C}\bar{D})$
	1	0	1	1	$(A\bar{C}D)$
	1	1	0	1	$(AC\bar{D})$
	1	1	1	1	$(ACD)$

POS Expression :

SOP Expression :

$$(A + \bar{C} + D)(A + \bar{C} + \bar{D})(A + \bar{C} + D)(A + \bar{C} + D) \cdot$$

$$(C\bar{A}D) + (C\bar{A}\bar{D}) + (CDA) + (D\bar{C})$$



Date: \_\_\_\_\_

Q15: Use Karnaugh map to simplify the following expressions to a minimum SOP form

$$\bar{A}\bar{B}\bar{C} + \bar{A}BC + A\bar{B}\bar{C} + ABC$$
$$000 + 011 + 101 + 110$$

AB \ C	0	1	
00	1		$\rightarrow (\bar{A}\bar{B}\bar{C})$
01		1	$\rightarrow (\bar{A}BC)$
11	1		$\rightarrow (A\bar{B}\bar{C})$
10		1	$\rightarrow (ABC)$

$(\bar{A}\bar{B}\bar{C}) + (\bar{A}BC) + (A\bar{B}\bar{C}) + (ABC)$  is the minimum SOP.

Q16: Obtain the minimum POS expression from Karnaugh map used in Q. 15.

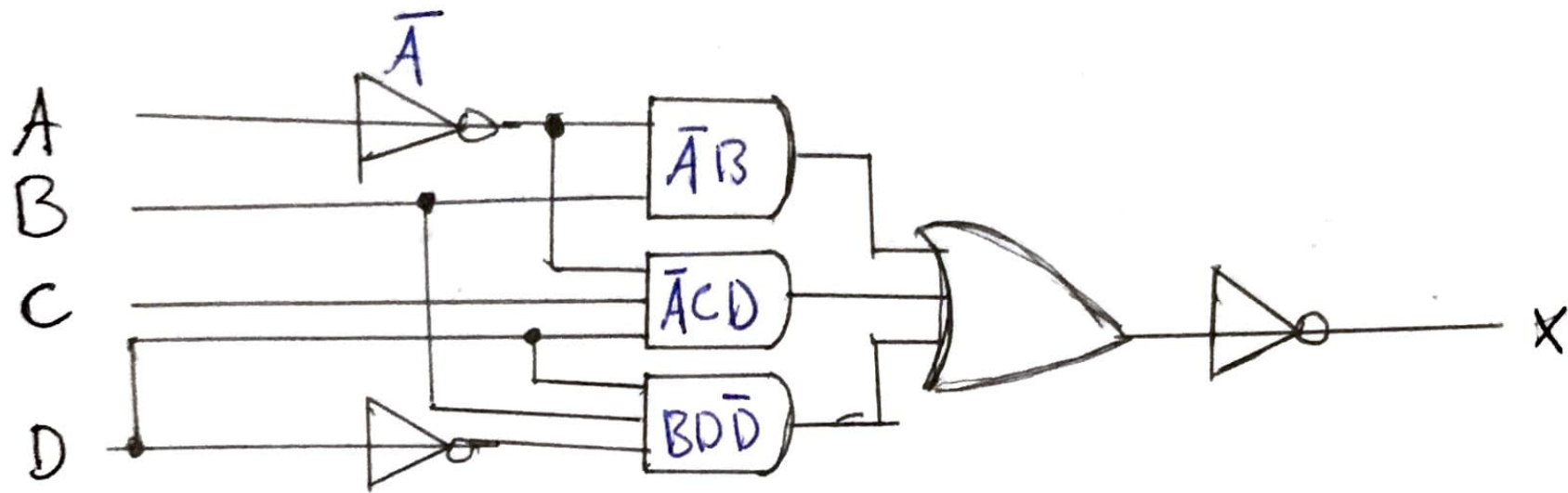
SOP

AB \ C	0	1	
00	1	0	$\rightarrow (A+B+\bar{C})$
01	0	1	$\rightarrow (A+\bar{B}+C)$
11	1	0	$\rightarrow (A+B+\bar{C})$
10	0	1	$\rightarrow (\bar{A}+B+C)$

hence

$(A+B+\bar{C})(A+\bar{B}+C)(A+B+\bar{C})(\bar{A}+B+C)$  is the minimum POS expression.

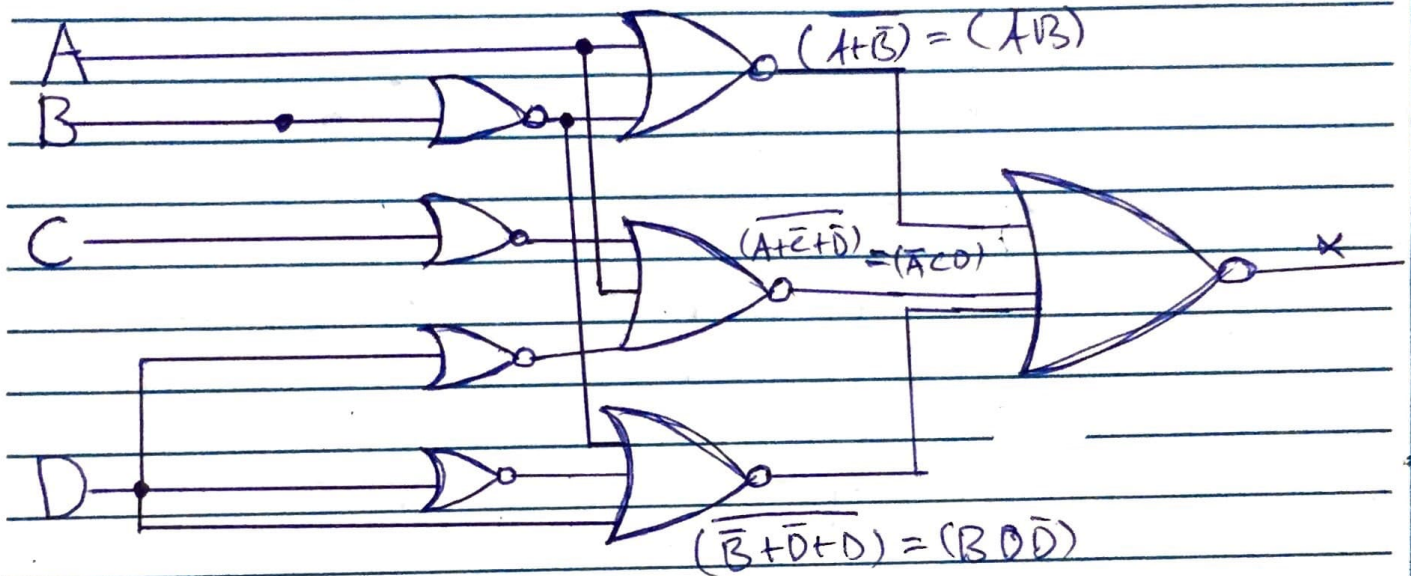
Q17: Write the output expression for the circuit in figure.



$$X = \overline{(\bar{A}B) + (\bar{A}CD) + (BD\bar{D})}$$

Date: .....

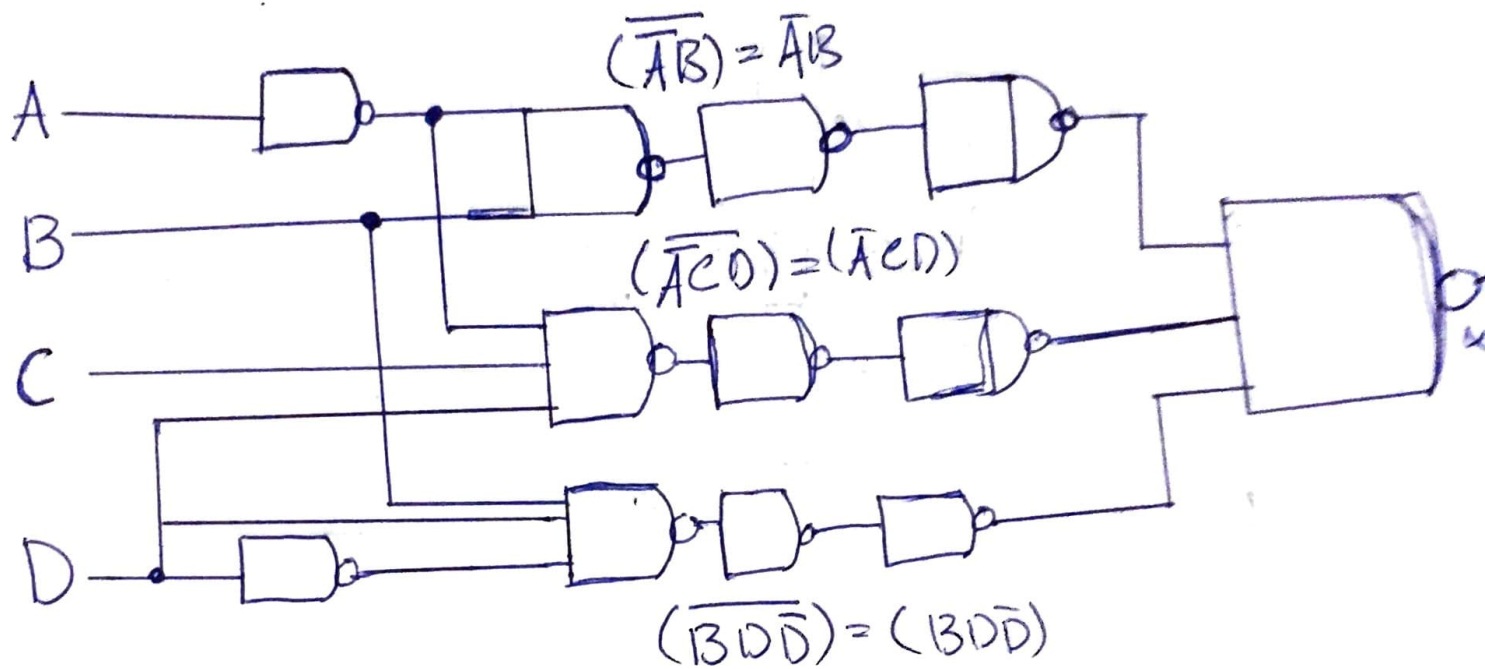
Q18: Implement the logic circuit in Q17 using NOR Gates.



$$X = \overline{(A+B) + (A+B+C) + (A+B+C+D)}$$

$$\Rightarrow \overline{(AB) + (ABC) + (BCD)}$$

Q 19: Implement the logic circuit in Q 17 with only NAND gates.



$$X_2 = \overline{\overline{A \cdot B}} \cdot \overline{\overline{A \cdot C \cdot D}} \cdot \overline{\overline{B \cdot D \cdot \bar{D}}}$$

By De Morgan's theorem

$$X_2 = \overline{(A \cdot B) + (A \cdot C \cdot D) + (B \cdot D \cdot \bar{D})}$$

Date: .....

Q20: Implement a logic circuit for the truth table in table (1)

Sol Obtained expression from truth table is

$$(\bar{A}\bar{B}\bar{C}\bar{D}) + (\bar{A}\bar{B}C\bar{D}) + (\bar{A}B\bar{C}\bar{D}) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}\bar{D}) + (\bar{A}B\bar{C}D) + (\bar{A}B\bar{C}D)$$

By reducing the expression using boolean laws and rules we get

$$(\bar{A}\bar{B}\bar{C}\bar{D}) + (\bar{A}B\bar{C}\bar{D}) + (\bar{A}\bar{B}C)$$

