

Department of Electrical Engineering

Assignment

Date: 14/04/2020

Course Details

Course Title: Power Electronics

Module: _____

Instructor: _____

Total 30

Marks: _____

Student Details

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Student ID: 13223

Note: Plagiarism of more than 20% will result in negative marking.

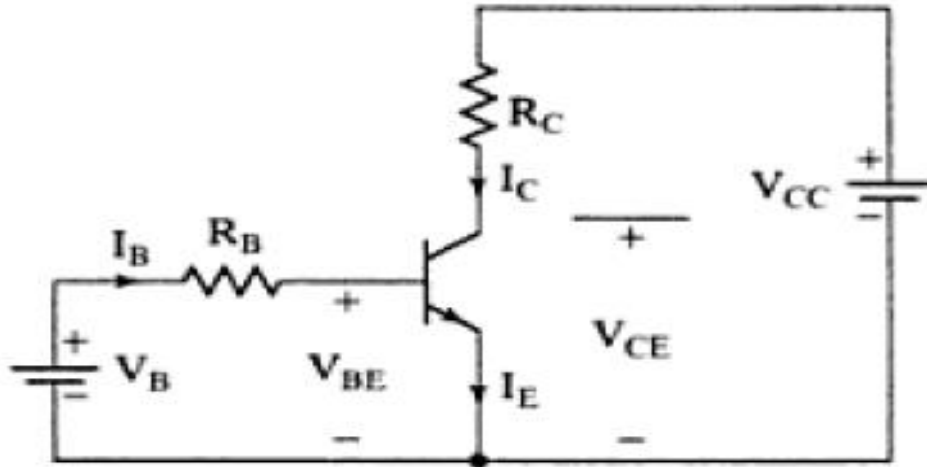
Similar answers of students will result in cancellation of the answer for all parties.

Q1	(a)	An appliance circuit has a R-L connected in series with a diode. After some time, modification is done to the circuit and a free-wheeling diode is added in parallel to the R-L. Will it have any impact on the performance and output of the circuit. Back your answer with before & after data, facts and figures. Does adding a free-wheeling diode in parallel to a R-C circuit have the same effect, different effect or no effect.	Marks 7
			CLO 1
	(b)	A Power Mosfet is connected in a circuit. The Drain to Source voltage, $V_{DS} = (\text{Last 2 digits of your student ID}) V$ and Threshold Voltage, $V_T = (\text{Last 1 digit of your student ID}) V$. What is the minimum Gate to Drain Voltage, V_{GS} required for the P.Mosfet to be in saturation mood.	Marks 3
			CLO 1
Q2	(a)	A Power Electronics appliance of 500W, 220V, 500KHz rating is using a Power Mosfet for switching purpose. If the P.Mosfet is replaced with a Power Bipolar Junction Transistor what effect will it have on the performance, losses and efficiency of the appliance. Will any other changes to the circuit be required? Back your reasons with valid data, facts and figures.	Marks 5
			CLO 1
	(b)	In the above appliance (Q2.a) if the P.Mosfet is replaced with a Silicon Controlled Rectifier what effect will it have on the performance, losses and efficiency of the appliance. Will any other changes to the circuit be required? Back your reasons with valid data, facts and figures.	Marks 5
			CLO 1
Q3	(a)	The bipolar transistor in the Figure below is specified to have β_F in the range of 8 to 40. The load resistance, $R_C = (\text{Last 2 digits of your student ID}) \Omega$.	Marks 10

The dc supply voltage, $V_{CC} = (\text{Last 3 digits of your student ID}) \text{ V}$ and the input voltage to the base circuit, $V_B = 10 \text{ V}$.
If $V_{CE} = (\text{First digits of your student ID}) \text{ V}$ and $V_{BE} = 1.5 \text{ V}$, find

- The mode of operation of the transistor
- the value of R_B that results in saturation with an ODF of 5,
- the β_{forced} ,
- the power loss, P_T in the transistor.

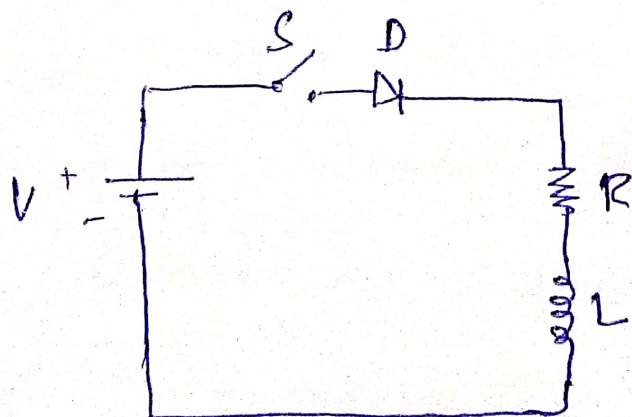
CLO 1



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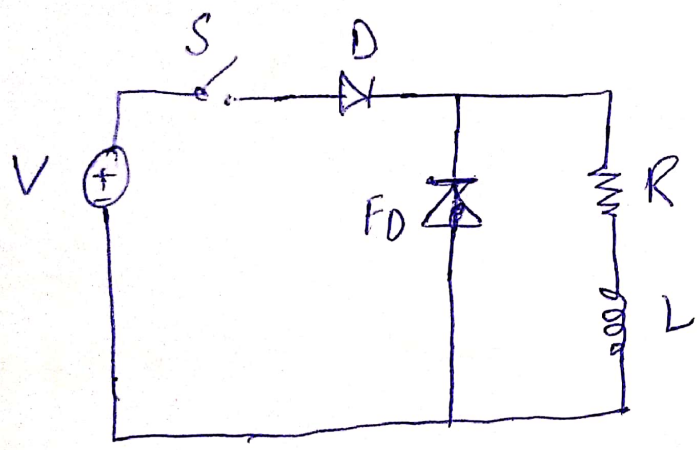
Q#1(a) An Application Circuit has a R-L Connected in Series with diode. After Some time, modification is done to The Circuit and free wheeling diode is added in parallel to RL will it have any impact on the performance and output of the Circuit. Back your answer with before and after data fact and figures Does adding a free wheeling diode in parallel to R-L circuit have the same effect, different effect or no effect.

Ans:



When the Switch is closed the Steady State current flows through the Circuit.

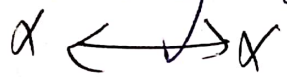
When we open the switch the steady state value will become zero which will result high value of $\frac{di}{dt}$. Since, the current is zero therefore, $\frac{di}{dt}$ will be high and hence the voltage across this high voltage can cause damage to both diode and switch.



When the switch is closed a steady current will flow through the load. When we open the switch the current will start to decay. This decay of current will result in development of voltage across the terminals of inductor due to this voltage the free wheeling diode will be forward bias and it will act like

a Short circuit thus provide a path for the flow of decay current

As we know that the inductor release energy while capacitor stores energy. Therefore, when the switch is open the inductor release energy while capacitor stores energy so, the R-C circuit has different affect. ~~as compare to R-L circuit.~~ Because there is no voltage flowing through the free wheeling diode.



~~Q1~~

Q1(b) A power MOSFET connected in circuit

The Drain to Source voltage, $V_{DS} = (\text{last 2 digit of } I_0) \text{ V}$
 and threshold voltage $V_t = (\text{last 1 digit of } I_0) \text{ V}$

What is the minimum gate to source voltage V_{GS} required for the P-MOSFET to be in saturation mood.

Ans. Solution:

Data - $V_{DS} = 23 \text{ V}$

$V_t = 3$
 for saturation mood.

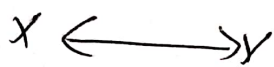
$$V_{DS} \geq V_{GS} - V_T$$

$$V_{DS} = V_{GS} - V_T$$

$$V_{GS} = V_{DS} + V_T$$

$$V_{GS} = 23 + 3$$

$$V_{GS} = 27V$$



Q/2 (a) A power Electronic Appliances of 500 w 220V, 500KHZ rating is using a power MOSFET for switching purpose. If the P.MOSFET is replaced with D.BJT what effect will it have on the performance, losses and efficiency of the appliance will any other changes to the circuit be required? Back your reason with valid data fact and figures.

Ans: A P.MOSFET is a voltage controlled device while P.BJT is a current controlling device. MOSFET have high switching frequency than BJT and appliances switching frequency will be

10000. It will ^{pages} affect the performance of switching.

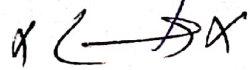
- P-MOSFET have higher losses than BJT. So, it will improve the efficiency of appliances.
- Switching is the biggest problem where BJT in appliances is used.
- ~~Conduction~~ Conduction losses will be decrease because of BJT replacement in appliances.
- In MOSFET driver circuit is simple and easy to design while in BJT it is very complex to design. There will be no changes in the circuit.

X ← → X

Q/2 (b) In the above appliances (Q2.a) if the P-MOSFET is replaced with SCR what effect will it have on the performance, losses and efficiency of the appliances. Will any other changes to circuit be required? Back your reason with valid data, fact and figure.

Ans: SCR has more robust device than P-BJT. It increase the efficiency of the appliances.

- SCR has high current as well as high voltage which increase the efficiency of appliances.
- SCR will have no ability to bear high frequency, so it will come in ~~the~~ ~~losses~~ section. Performance of appliances
- The switching capacity of SCR is not good as P.MOSFET (losses).
- It will no effect in circuitry.
- Losses of using SCR as switching over P.BJT is given below
 - gate trigger losses
 - on state losses
 - off state losses
 - switching losses

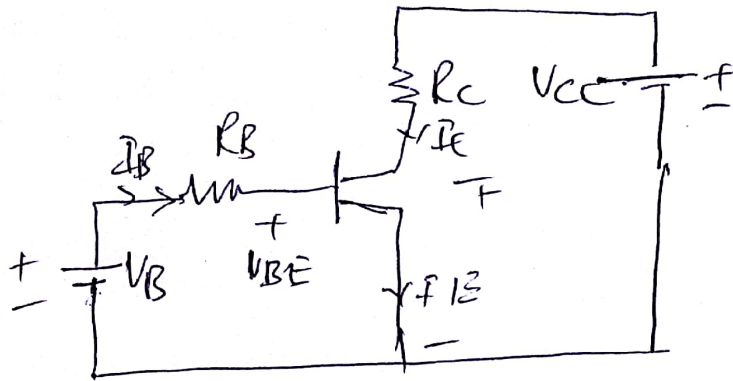


Q/3 (a) The bipolar transistor in the figure below is specified to have β_F in the range of 8 to 40. The load resistance, $R_L = (\text{last 2 digit of ID}) \Omega$. The dc supply voltage, $V_{CC} = (\text{last 3 digit of ID}) V$

and the input voltage to the base circuit $V_{BE} = 1.5V$, find.

- (a) The mode of operation of the transistor
- (b) The value of R_B that result in saturation with an ODF of 5,
- (c) The β_{forced} ,
- (d) The power loss, P_T in the transistor.

Ans



Solution:

Data:

- $R_C = 23\Omega$
- $V_{CC} = 223V$
- $V_B = 10V$
- $V_{CE} = 4V$
- $V_{BE} = 1.5V$
- $\beta_{min} = 8$
- $\beta_{max} = 40$

- (a) Mode of operation of transistor.
- (b) $R_B = ?$
- (c) β_{force}
- (d) power loss P_T in transistor.

(a) Saturated mode.

$$I_{CS} = \frac{V_{CC} - V_{CE(sat)}}{R_C}$$

$$= \frac{22.3 - 1}{23}$$

$$I_{CS} = 0.96 A$$

$$I_{BS} = \frac{I_{CS}}{\beta_{min}}$$

$$= \frac{0.96}{8}$$

$$I_{BS} = 0.12 A$$

$$ODF = \frac{I_B}{I_{BS}}$$

$$I_B = ODF \times I_{BS}$$

$$= 5 \times 0.12$$

$$I_B = 0.6 A$$

$$R_B = \frac{V_B - V_{BE}}{I_B}$$
$$= \frac{10 - 1.5}{6}$$

$$R_B = 1.41 \Omega$$

$$\beta_{force} = \frac{I_C}{I_B}$$
$$= \frac{9.6}{6}$$

$$\beta_{force} = 1.6$$

$$I_C = \frac{V_{CC} - V_{CE}}{R_C}$$
$$= \frac{223 - 1}{23}$$

$$I_C = 9.6 A$$

$$P_T = V_{BE} I_B + V_{CE} I_C$$
$$= 1.5 \times 6 + 1 \times 9.6$$
$$= 9 + 9.6$$

$$P_T = 18.6 W$$