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Subject	Electronic Devices and Circuits
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Doutce sheet. ONO 1 . Form Vail Vin. Reitcheet 11/47474 The from V2= 90 I2= 12.5 & Z=22 IzF ? 0.28. for Ize:" Vait = V2 - AL2 L2 = 20-(Iz- Izk)Z, = 20-(12-25)(22) = 20- (0 9 225)(22) = 20 -0.2695 19-730TV contenting zenes man cussed the manimum purer sosipation is 10 $\frac{1}{2m^2} \frac{P_{D}(mem)}{V_{T}} = \frac{100}{20} = 0.05$ Jam = 80 mm.

Lam -08 Vait = Vz + SIZZ2 = 20x+ (Izm-Iz)Z2 20 v + (80mA- 12.5mA)22 1 = 201+ (37-5mA) 22. = 20 v to. 825 Vait = 20.825 V 1) calculate The value of R for maximum zenes cussent when These no lead as show in figure. 15 R= Vin - Vout Im = 24N-20.825V 53 mA R= 63.52 R= 70 D (Nou oest loogest standard) 20-825 Izm = SomA.

for mominum good Resistance (maximum current) The zener diode currend, (c) Minimum (Izk = 0.25) IT = Vin - wout/R = 24v - 19.73v 704 = 0.061A = 0.061A In= 61 mA IL= IT- IZK = 61 - 0.25 mA. 11 = 60.75 mA $R_{12} = \frac{19.3}{1} = \frac{19.3}{60.75} = \frac{19.3}{60.75} mA$ 19-3 10.0671 = 285.925 1, Re= 23652

QN/0 2 :- Determein IB, Ic, IE, VOE---Ans: VBE = 0.7 V 5-V-0.7V 3.91CD => IB = VBB - VBE = RB. = 1102 JA) => $F_c = B_{0c} \cdot I_B = (153)(1102 ulA) = (155.3 mA)$ $=>I_E = I_C + I_B = 165.3 \text{ mA} + 1102 \text{ uA} = [166.4 \text{ mA}]$ so we for VEE and VEB VeE = Nec - IcAc = 15V- (165.3mH)(180.2) = 15V-29.7V. = - 14.7v (V(B = VE - VBE => -14.71-0.71 = 15.4 V since The collector is cet a lower voltage Them The bases The collector base junction is foward biased

ONO 3: = for using BIT as an amplifes we need to set Them in Active region becase BTT working as an amplifier when use is active region Tounsistor has Three Basis configuration for asing it in Amplifier mode. common Base voltge Grain no cuspent. common Emitter Both Jain common collector cuosort join no coltere. Te = 15uA. E0 SUA 1.1 so This is common emitted configuration of townsister which has both voltge & eworent omplification.

Ic = B. IB = 200 + SUA I. = 200.0005 UA IE = Ic + IB IE = 200-0000 SULA + 15UA [IE = 200.00002]

notical to in it . For a toasistor ta alts as QNO 4 a "switch" Angu ON conditions (i) loasistor fully on/ (II) Maimum of saturation cussent Ic flows-(III) BE Junction is forward bias (iv) Be 11. 11 forword biels. (V) VCE = OV Off conditions i) Transister fully off. (II) Input and base are alow (iii) collector current Ic = 0 (iv) VeF = Vee W) BE junction is reverse bices (vi) BC 11 11 11 bias (VII) BE sunction is less Than o.7.

INDS Arg. JFET is a type of junction field effect transister which is voltage contoutled device as differ from BJT which is wovent controlled Drain crote. Vos sairie. Actually in FET The doain to source current is introlled by The width of. The channel the electric field is The gode to source pooduced by voltaye. rottoye -ID 10 5:01 V65=- 9 405. 11

50 if we see to the griph which The no voltge applied to The gade The cussent flows freely. Drein VPK Sousie wides and prein The channel : are capsent mores focely. If us more. Vas to rigetive ralue The channel width start to decreases and cussent connort mores. N-chen MDS. Gr. on nore negative so no current plows and This effect is called 50 Pinch off region no ensent or lers cuovent flowr.

Quite 6:-

$$n_{c} \neq i \cdot i \cdot i \cdot n_{c}$$

 $v_{in} = \int_{a}^{b} \int_{a}$

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 $\frac{I_{B(mmin)}}{B_{OL}} = \frac{I_{c}(sut)}{12s^{-}} = \frac{10mA}{12s^{-}} = 80 \text{ wA}$ 日本語語のなどのに行