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Subject Microprocessor theory

Assignment (02)

Program B. Tech Elect

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Ans(02) Status Signal

signal ready a busy signal A status signal on

(*) a status signal determines whether the which address for input - output address memory when it is high or low. The address on high bus when it is low low output device address on the at address bus to the memory So, Si, These are status signals.

(*) 8085 μ m has got three status signals So, Si, ~~St~~ and Io and a special signal ALE. It's a read control signal when the signal on this pin is low selected memory input output device is read and information placed on data bus -

Answer # = 02

stored in memory word is transferred by 8088 micro-processor and interfacing.

Example #

