Department of Electrical Engineering

Sessional Assignment Date: 05/05/2020

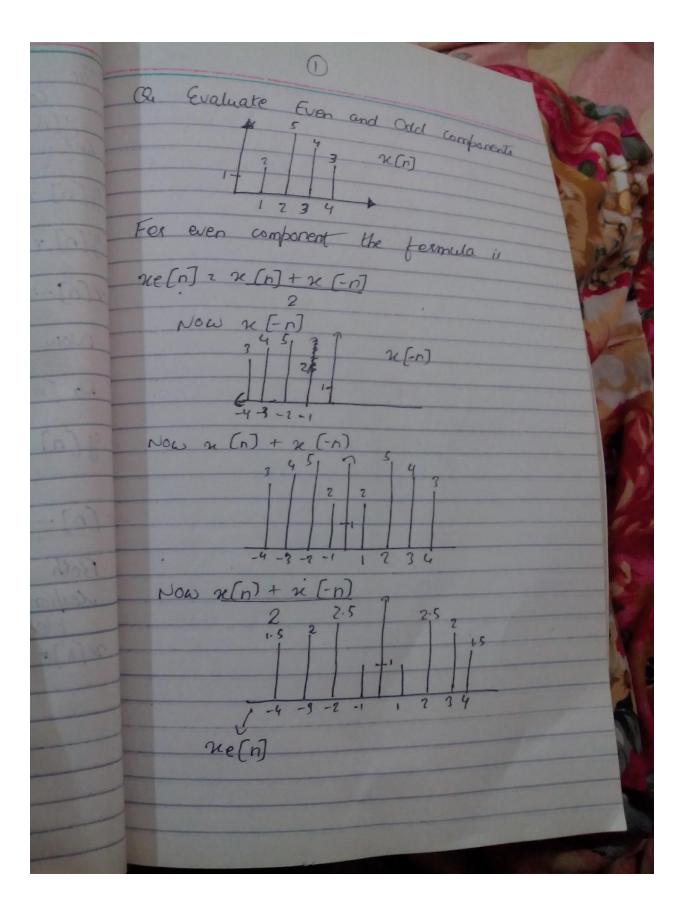
Course Details

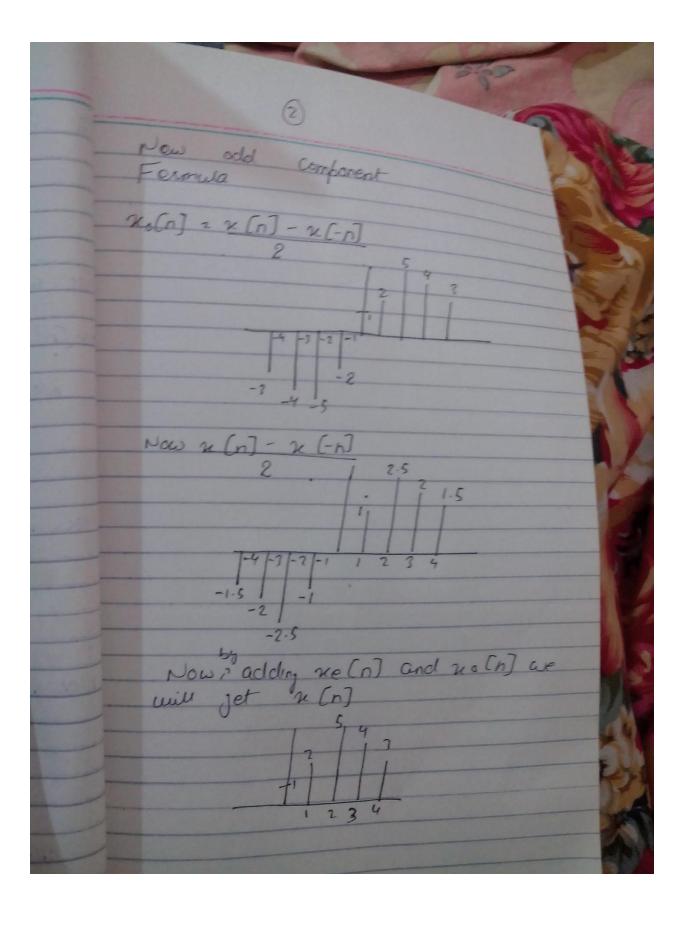
Course Title:Signals & SystemsModule:04Instructor:Engineer Mujtaba EhsanTotal Marks:20

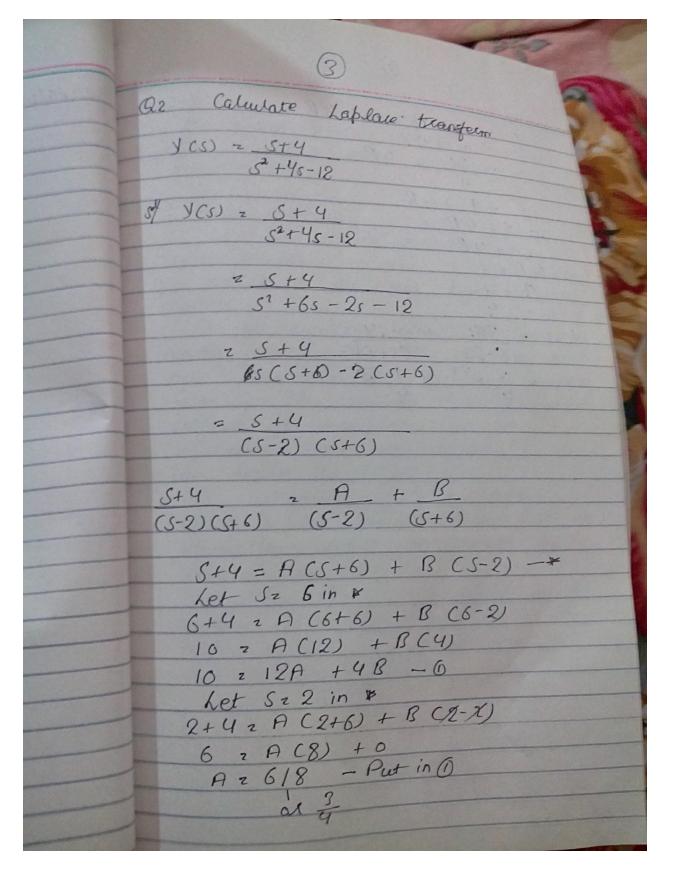
Student Details

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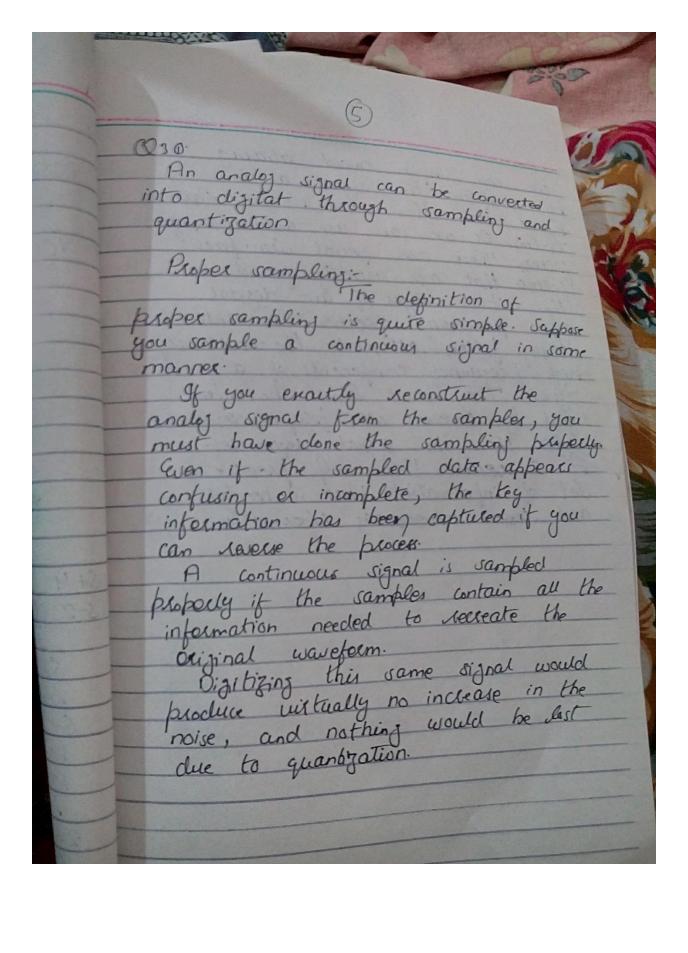
Q1.		Evaluate the even and odd components for the given function.	Marks
		5	05
		1 4 3 x[n] 1 n	CLO 1
		1 2 3 4	
Q2.		Calculate the inverse Laplace transform of the given equation.	Marks
			07
		$Y(s) = \frac{s+4}{s^2 + 4s - 12}$	CLO 3
Q3.	i.	Discuss the procedure of converting an analog signal into a digital one.	Marks
	ii.	Suppose an analog signal has a highest frequency of 60Hz. Outline the steps that will	02+02
		ensure that no aliasing occurs.	CLO 2
Q4.		Show that:	Marks
		$x[n] * [h_1[n] * h_2[n]] = [x[n] * h_1[n]] * h_2[n]$	04
			CLO 2







+48 Ma 413= ou 200 20 (5+6) + + 1 pst Co in co ---



we can simply avoid aliasing by sampling the signal at a by sampling the signal at a higher rate than the Nyquist rate Consider the state than use anti-aliasing Let, not the can use anti-aliasing Let, not the constitution of the state are usually found in Ital 2 (2) the initial stages of any digital the initial stages of any digital the initial stages of any digital stages of any digital signal processing operation. The anti-Ital signal processing operation the unnecessary aliasing filters attenuate the unnecessary aliasing frequencies. Now consistent the infat signal by removing all frequencies. Now consistent than the signal frequencies with [n] 2 higher t and bemon unnecessary information. n[n] = (h

(consider y(n) = n(n) * h, (n) * h, (n) et, n(n) * h, (n) = W, (n) 4[n] 2 [x[n] * h,[n]] * h,[n] -0 4[n] = WI[n] + hin n(n) -> [hi(n)] wi(n) > [hi(n)] too Now consider that Nr (n) 2 h1 (n) 1 h2 (n) y(n) = x(n) * [h,(n] * h,(n]] ~ x(n) * W,(n) 2 (n) -> [N2 (n) -y(n)) Both block diagrams jue the same lesponse ~[h] * [hi[n] * hi[n]] 2 [x[n] * hi[n]] * he(n]