## Department of Electrical Engineering Assignment Date: 14/04/2020

## Course Details

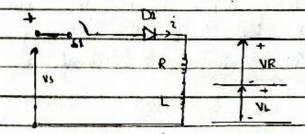
Course Title: Instructor:	Power Electronics ENGR. SHAYAN TARIQ JAN	Module: _ Total _ Marks:	8th 30
Name:	Student Details  Idrees Iqbal	Student ID:	13171
•	sm of more than 20% will result in negative answers of students will result in cancellate	<b>o</b>	ties.

(a)	An appliance circuit has a R-L connected in series with a diode. After some time, modification is done to the circuit and a free-wheeling diode in added in parallel to the R-L. Will it have any impact on the performance and output of the circuit. Back your answer with before & after data, facts and figures.  Does adding a free-wheeling diode in parallel to a R-C circuit have the same effect, different effect or no effect.	Marks 7  CLO 1
(b)	A Power Mosfet is connected in a circuit.  The Drain to Source voltage, $V_{DS} = (Last \ 2 \ digits \ of \ your \ student \ ID) \ V$ and  Threshold Voltage, $V_T = (Last \ 1 \ digits \ of \ your \ student \ ID) \ V$ .  What is the minimum Gate to Drain Voltage, $V_{GS}$ required for the P.Mosfet to be	Marks 3 CLO 1
	in saturation mood.	0201
(a)	A Power Electronics appliance of 500W, 220V, 500KHz rating is using a Power Mosfet for switching purpose. If the P.Mosfet is replaced with a Power Bipolar Junction Transistor what effect will it have on the performance, losses and	Marks 5
	efficiency of the appliance. Will any other changes to the circuit be required? Back your reasons with valid data, facts and figures.	CLO 1
(b)	In the above appliance (Q2.a) if the P.Mosfet is replaced with a Silicon Controlled Rectifier what effect will it have on the performance, losses and efficiency of the appliance. Will any other changes to the circuit be required?	Marks 5
	Back your reasons with valid data, facts and figures.	CLO 1
(a)	The bipolar transistor in the Figure below is specified to have $\beta_F$ in the range of 8 to 40. The load resistance, $R_C$ = (Last 2 digits of your student ID) $\Omega$ .	Marks 10
	(b) (a)	<ul> <li>modification is done to the circuit and a free-wheeling diode in added in parallel to the R-L. Will it have any impact on the performance and output of the circuit. Back your answer with before &amp; after data, facts and figures. Does adding a free-wheeling diode in parallel to a R-C circuit have the same effect, different effect or no effect.</li> <li>(b) A Power Mosfet is connected in a circuit. The Drain to Source voltage, V<sub>DS</sub> = (Last 2 digits of your student ID) V and Threshold Voltage, V<sub>T</sub> = (Last 1 digits of your student ID) V. What is the minimum Gate to Drain Voltage, V<sub>GS</sub> required for the P.Mosfet to be in saturation mood.</li> <li>(a) A Power Electronics appliance of 500W, 220V, 500KHz rating is using a Power Mosfet for switching purpose. If the P.Mosfet is replaced with a Power Bipolar Junction Transistor what effect will it have on the performance, losses and efficiency of the appliance. Will any other changes to the circuit be required? Back your reasons with valid data, facts and figures.</li> <li>(b) In the above appliance (Q2.a) if the P.Mosfet is replaced with a Silicon Controlled Rectifier what effect will it have on the performance, losses and efficiency of the appliance. Will any other changes to the circuit be required? Back your reasons with valid data, facts and figures.</li> <li>(a) The bipolar transistor in the Figure below is specified to have β<sub>F</sub> in the range of 8 to 40.</li> </ul>

The dc supply voltage, $V_{CC}$ = (Last 3 digits of your student ID) V and the input voltage to the base circuit, $V_B$ = 10 V.  If $V_{CE}$ = (First digits of your student ID) V and $V_{BE}$ = 1.5 V, find (a) The mode of operation of the transistor (b) the value of $R_B$ that results in saturation with an ODF of 5, (c) the $\beta_{forced}$ , (d) the power loss, $P_T$ in the transistor.	CLO 1
$\begin{cases} R_{C} \\ I_{C} \\ + \end{cases} V_{CC} \stackrel{+}{=}$	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	

Question	NO	1:
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Part a	Sec.	2
= =		×

-> R-L Connected in Geries With



t When S1 is Closed at t=0; the

Current through the inductor increases

and is expressed as

Vs = VL + VR = Ldi + Ri

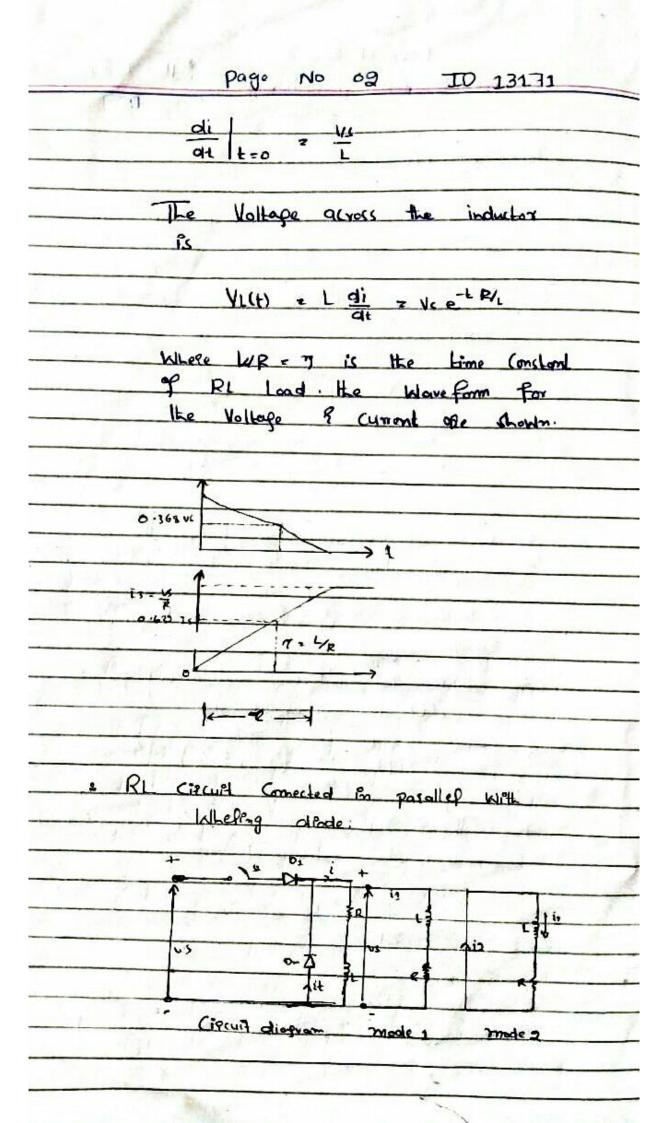
\* With the initial Condition ilt=0)=0

ilt) is expressed as

+ The gate of Change of this Circuit

Can be obtained from

4 The initial sale of rice of the Current (at tea) is oblained:



the inductor has property to store energy  To Ac (unrent alusing positive high Cycle the inductor chees amosgy  AT negative happ cycle the inductor  alc - analyses.  This may (ause severse damage to  (freely to  The awaid energy description free  Interpring diade is used  To megative happ cycle the tree Wheeling  diade becames forward bias so the  Current will Plant trough diade
Cycle the inductor chees energy  AT megative happ cycle the inductor  dicenesses.  This may cause geverse damage to  Cricuit.  to avoid energy description free  Wheeping diode is used  In megative happ cycle the tree Wheeping  diode becomes forward bias so the  Current will plant trough diode
AT megative happ cycle the inductor  dic-eneques.  This may (ause gevesse damage to  (recuit)  to avoid energy description free  Wheeping diade is used  To megative happ cycle the tree Wheeping  diade becomes forward bias so the  Current Will Plant trough diade
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Circuit  * To avoid cresqs description free  Wheeling diode is used  * In megative half cycle the tree Wheeling  diode becomes forward bigs so the  Current Will Plant trough diode
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Current Will Plant trough diade
Current Will Plant trough diade
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Wave Form
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DI (b) Drain to Source Voltage, vos: 72v
Thoughold Vollege, vT= 1v
Min Grak to Drain Voltage
V667?

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Sol :
In Saturation
Where
VOS > VGS - VT
Vos + VT > VGs
VES & YOS + VT
V65 6 71V + 1V
VGS & 72V
ONO 8 POR (9)
A powles electronic appliance of soow
220 V. SOKHZ Rating & using a power
mosfet for Switching purpose The power
mother is replaced With powler Ripular
Junction bransistar its effect on his performance
and based and efficiently on the appliance
The Juiliting frequency will be loved
of appliance - because mosses have high
Juitching frequency the Riopelas Junction
fransistor.
The Losses Will be low because
Losses in BT Ps less than mosfet
have high waithching frequency than
RA LOSS in appliance will be low
another reason lokes - The BIT Compl

## Page No 06 ID 23171 and Will impact on its performance The SCR an honolle more powler Wolfage current which increase the efficiency of the appliance and one

of the advantages efficiency.

The SCR Can protected because of the fuse Which com decrease

Losses used as the performance of the appliance improve

DN03 4

The hipoles hansister in fry Ps.
Specifical to have Bp in the

The Load Resistance

Res Last 2 digit of ID A

The De Supply Vollege Vice 3 digit of IDV

VB = 10 V

VCE = 1 digit of IDV

VBE = 1.5 V find

a) The mode of operation

Dala :

VCC 2 171

VB 2 10V

VCE 2 IV

Rc 2 71

(a) as a switch

NOW

BB

BF = 1.601

(d) PT 2 VBE IB + VCE ICS

PT = 1.5 x 1.495+1 x 2.394

PT = 2.242 + 2.394

PT = 4.636 W