

Dariusz Jankowski

15385

DDD - Assignment #1

Q : 1

Answer

A register is an electronic device consists of a series of flip-flops to store data bits and moving the data bits

The length of the stored binary word

depends on the number of flip-flops that make up the register.

Q: 2

ANSWER.

The storage capacity of a register that can retain one byte of data is 8 bits. This particular register will be an 8 stage shift register.

Q: 3  
ANSWER.

The shift capacity of a register permits to store and make data from one stage to another within.



ind<sub>0</sub>, ex out of register.

Q: 14

Answer:

Since the shift register is initially

cleared

initially	0	0	0	0
cell <sub>1</sub>	1	0	0	0
cell <sub>2</sub>	1	1	0	0
cell <sub>3</sub>	0	1	1	0

Q: 5

Answer.

U<sub>0</sub>



Data



Q<sub>0</sub>



Q<sub>1</sub>

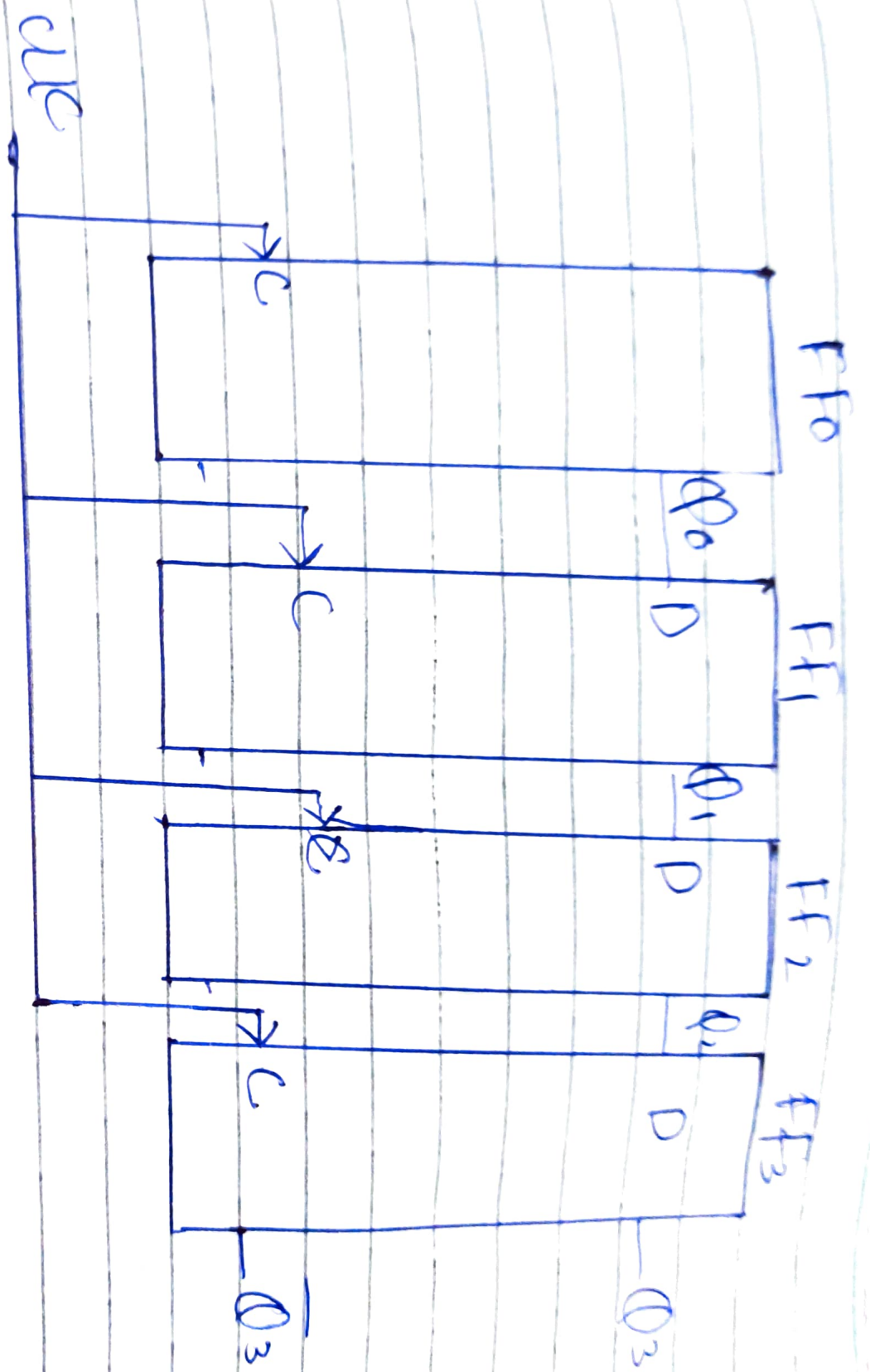


Q<sub>2</sub>



Q<sub>3</sub>



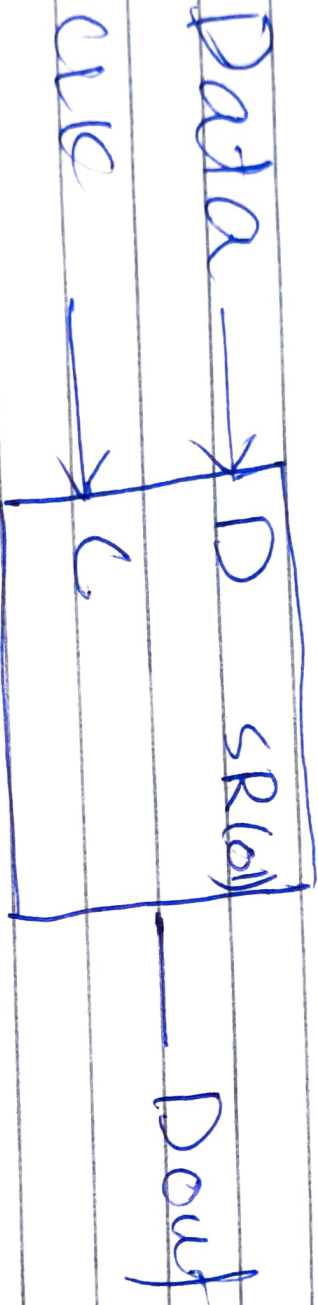


Q: 6

Answer:



data





Initially 1 1 0 0 0 1 1 1 0 0 0 0

cell 1 0 1 1 0 0 0 0 1 1 1 0 0 0

cell 2 0 0 1 1 0 0 0 1 1 1 0 0 0

cell 3 0 0 0 1 0 0 0 1 1 1 0 0 0

cell 4 1 0 0 0 0 1 1 0 0 0 1 1

cell 5 1 1 0 0 0 0 1 0 0 0 0 1

cell 6 1 1 1 0 0 0 0 1 1 0 0 0

cell 7 0 1 1 1 0 0 0 0 1 1 0 0

cell 8 0 0 1 1 1 0 0 0 0 1 1 0

cell 9 0 0 0 1 1 1 0 0 0 0 1 1

cell 10 1 0 0 0 0 1 1 1 0 0 0 0 1

~~cell 4~~

cell 11 1 1 0 0 0 1 1 1 0 0 0 0 0

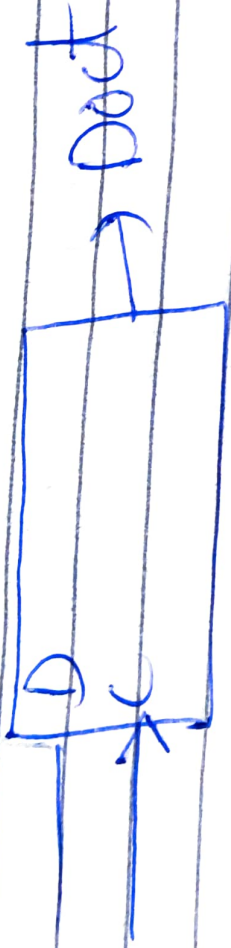
cell 12 0 1 1 0 0 0 1 1 1 0 0 0 0

cell 13



Q. 7

Answer.



Detail.

CG

Detail.

Q: 8

Answer:



The Data bits stored

are

1 1 0 1 1 0 1 0 0

Q: 59

Answer.

Din 0 1 1 0

Q1

Q2

Q3

Q4

Q5

Q3

