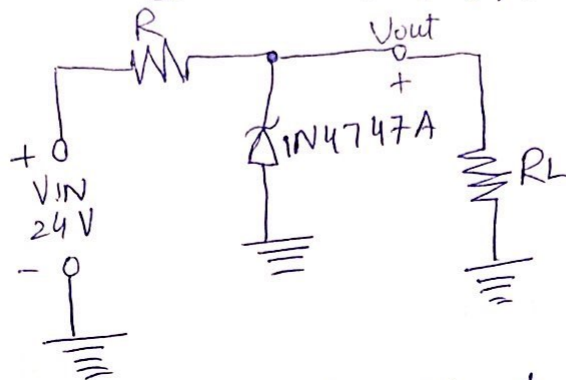


ABDUL MOEZ

Electronics 1

Dr. Shahriyar Qureshi

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Q¹

Sol: From the data sheet we can write
of IN4747A

$$V_Z = 20V$$

$$I_Z = 12.5mA ; Z_Z = 2\Omega$$

$$I_{ZK} = 0.25mA$$

a) $V_{out} = ?$ at I_{ZK} & I_{ZM}

first to find $V_{out(I_{ZK})}$ then I_{ZM} .

$$V_{out} = V_Z - \Delta V_Z \text{ (for } I_{ZK}\text{)}$$

$$= V_Z - (I_Z - I_{ZK}) Z_Z$$

(2)

$$V_{out} = V_Z - (I_Z - I_{ZK}) Z_Z$$

$$V_{out} = 20 - (12.5 - 0.25) 22$$

$$V_{out} = 20 - (12.25) 22$$

$$V_{out} = 20 - 0.2495$$

$$\boxed{V_{out} = 19.7305V} \text{ for } I_{ZK}$$

Now V_{out} for I_{ZM} first to find I_{ZM} :

$$I_{ZM} = \frac{P_D(\text{MAX})}{V_Z}$$

for zener max current the maximum power dissipated is 1W

$$I_{ZM} = \frac{P_D(\text{MAX})}{V_Z} = \frac{1W}{20V} = 0.05A$$

$$\boxed{I_{ZM} = 50mA}$$

Now V_{out} for I_{ZM}

$$V_{out} = V_Z + \Delta V_Z$$

$$V_{out} = V_Z + (I_{ZM} - I_Z) Z_Z$$

$$V_{out} = 20 + (50mA - 12.5mA) 22 \Omega$$

$$V_{out} = 20 + (37.5mA) 22 \Omega$$

$$\boxed{V_{out} = 20.825V} \text{ for } I_{ZM}$$

B) $R = ?$

As we know

$$V_{IN(max)} = I_{ZM}R + V_{out}$$

$$R = \frac{V_{IN} - V_{out}}{I_{ZM}} = \frac{24V - 20.825V}{50mA}$$

$$R = 63.5 \Omega$$

c) Minimum load resistance $R_L = ?$
 To find min load resistance we will consider maximum load current

$$\text{So } I_T = \frac{V_{in} - V_Z}{R} = \frac{24 - 20}{63.5 \Omega} = \frac{4}{63.5} = 62.99$$

$$I_T = 63 mA$$

Now to find $I_L(max)$:

$$\begin{aligned} I_L(max) &= I_T - I_{ZK} \\ &= 63mA - 0.25mA \end{aligned}$$

$$I_L(max) = 62.75mA$$

Page (2B)

So

$$R_L(\min) = \frac{V_Z}{I_L(\max)}$$

$$= \frac{20}{62.75 \text{ mA}}$$

$$R_L(\min) = 318.72 \Omega$$

∴ the required minimum
value of R_L .

Answer

Q⁴

③

For a transistor to act as a switch you need to join each of the following conditions on the left to ON or OFF state

Transistor fully ON (ON)

Transistor fully OFF (OFF)

Input & base are at 0V (OFF)

Collector current $I_c = 0$ (OFF)

$V_{CE} = V_{CC}$ (OFF)

BE junction is reverse bias (OFF)

BC junction is ~~forward~~^{reverse} bias (OFF)

Max of Saturation current I_c flows (ON)

BE junction is forward bias (ON)

BC junction is forward bias (ON)

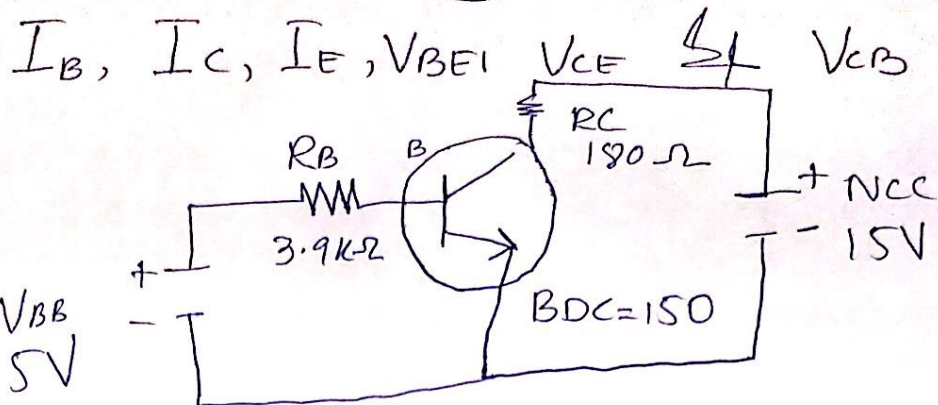
$V_{CE} = 0V$ (ON)

BE junction is less than 0.7V (OFF)

Answered

(4)

Q^a



Solⁿ: From the given figure we can write
 $V_{BB} = 5V$, $R_B = 3.9k\Omega$, $R_C = 180\Omega$, $V_{CC} = 15V$

As I_B is given by $\beta_{DC} = 150$

$$I_B = \frac{V_{BB} - V_{BE}}{R_B} = \frac{5V - 0.7V}{3.9k\Omega} = 1102\mu A$$

we know

$$I_C = \beta_{DC} I_B = (150)(1102\mu A)$$

$$I_C = 165.3mA$$

$$I_E = I_C + I_B = 165.3mA + 1102\mu A = 166.4mA$$

$$I_E = 166.4mA$$

Now to find V_{CE} first

$$V_{CE} = V_{CC} - I_C R_C = 15V - (165.3mA)(180\Omega)$$

$$= 15V - 29.7V = -14.7V$$

Now

$$V_{CB} = V_{CE} - V_{BE} = -14.7V - 0.7V = -15.4V$$

Since collector is at a lower voltage than the base, the collector base junction is forward biased.

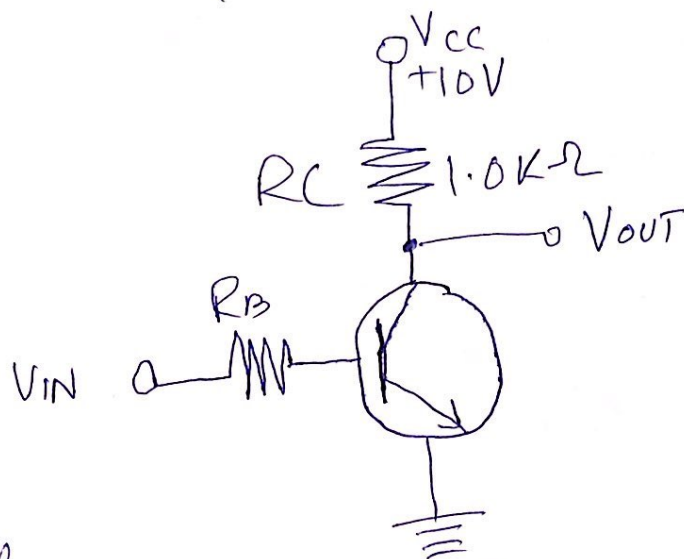
(5)

Q⁶ For the transistor circuit Calculate the following

a) What is V_{CE} when $V_{in} = 0V$?

b) Determine the minimum value of I_B is required to saturate this transistor if

β_{DC} is 125 & $V_{CE(sat)}$ is $0.4V$.



Solⁿ

From figure $V_{CC} = 10V$; $R_C = 1.0K-\Omega$

a) $V_{CE} = ?$ when $V_{in} = 0V$

As we know that for $V_{in} = 0V$ the transistor will be in cut off mode

$\therefore V_{CE}$ will be the same as V_{CC}

$$\Rightarrow V_{CE} = V_{CC} = 10V$$

So

$$\boxed{V_{CE} = 10V}$$

⑥
③ $I_B = ?$ when $\beta_{DC} = 125$ & $V_{CE(sat)} = 0.4V$

we can find $I_C(sat)$ to calculate

$$I_B(\min)$$
$$I_C(sat) = \frac{V_{CC}}{R_C} = \frac{10V}{1.0K\Omega} = 10mA$$

So
$$I_B(\min) = \frac{I_C(sat)}{\beta_{DC}} = \frac{10mA}{125} = 0.08mA$$

$I_B(\min) = 0.08mA$ is the required
min I_B to saturate the transistor.

Answered*

(7)

Q³ Discuss how is BJT used as an amplifier with the help of schematic diagram for a basic BJT amplifier? Which basic configurations are required for it? Consider input AC & DC Values as 5mA & 15mA respectively, assume $\beta = 200$.

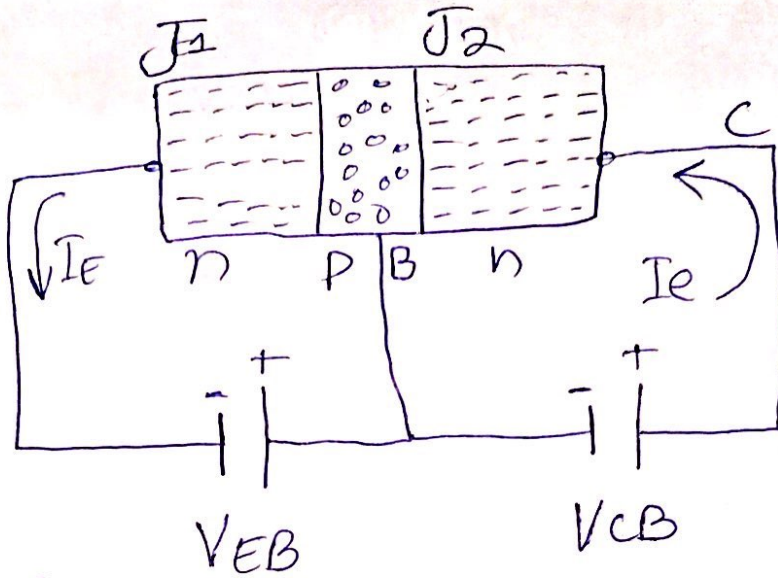
Solⁿ BJT's can be used as amplifiers by using its "Active" Region of operation by achieving forward biased mode of junction 1 (J1) & by reverse biasing the junction 2 (J2)

S NO	J ₁	J ₂	Region of operation	Works as
1	F.B	R.B	Active	Amplifier
2	F.B	F.B	Saturation	ON Switch
3	R.B	R.B	Cutt off	OFF / open circuit.
4	R.B	F.B	Inverted	rarely used

Above are the 4 modes the BJT can be used & mode 1 is the configuration of the BJT to be used as an amplifier.

(8)

Schematic Diagram.



Above Diagram Shows the basic Configuration for Using a BJT in active region thus acting as an amplifier.

After application of V_{BE} the junction J_1 is F.B so the barrier potential will now reduce & new barrier potential will be $V_0 - V_{BE}$

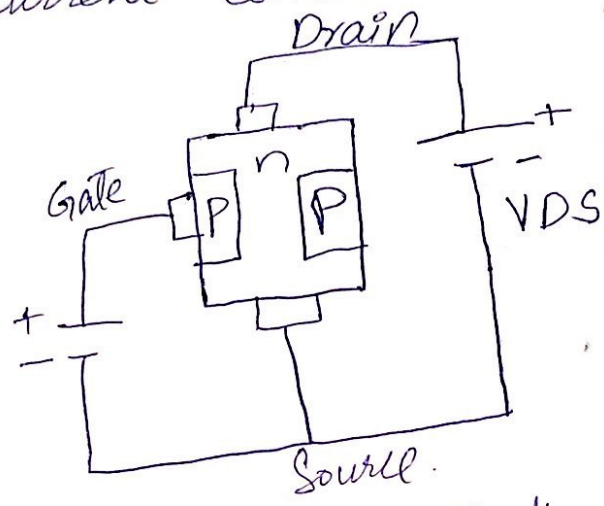
★ After Application of V_{CB} the J_2 in R.B so the B.P will increase & new B.P will be

$V_0 + V_{CB}$.
Because of reduced B.P at J_1 the e will move into p-region & recombine with hole at Base.

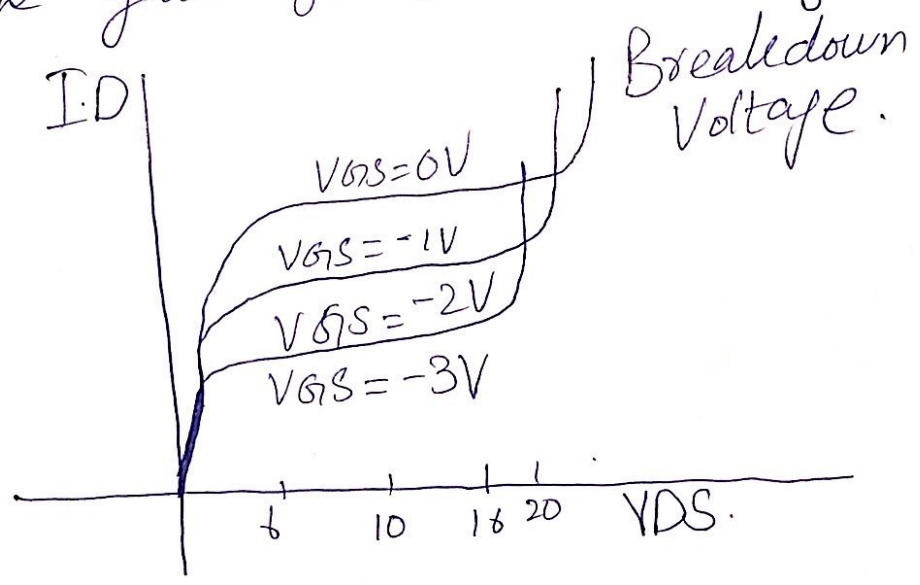
(9)

Q⁵ Discuss the JFET (n-channel) -----

Sol. JFET is a type of junction field effect transistor which is voltage controlled device as differ from BJT which is current controlled

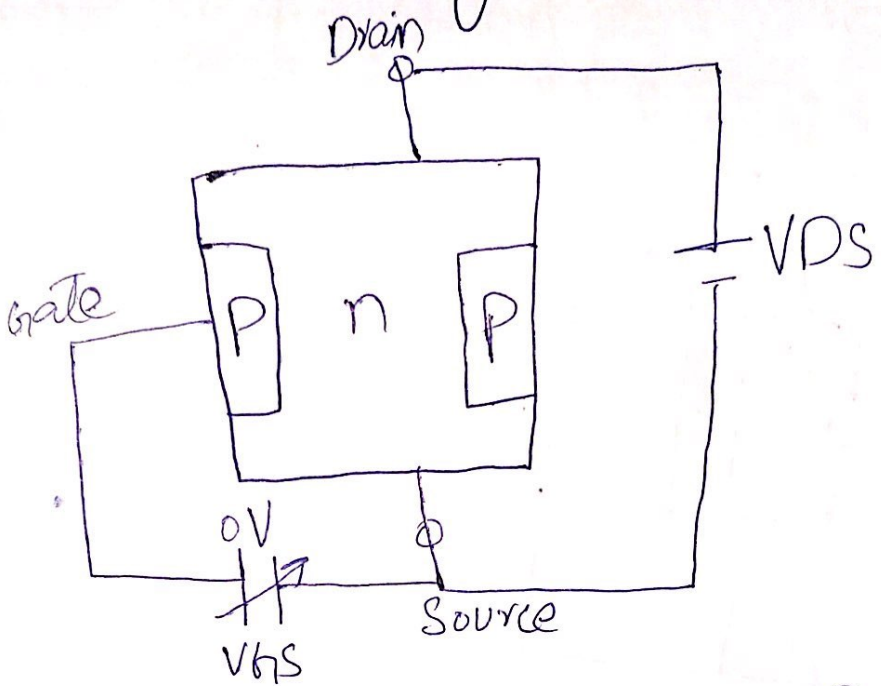


Actually in FET the drain to source current is controlled by the width of the channel the Electric field is Produced by the gate to source Voltage.



(10)

So if we see to the graph which the no voltage applied to the gate the current flows freely



The channel are wider & Drain current moves freely
 if we move V_{GS} to negative value the channel width start to decrease and current cannot moves.