

**Department of Electrical Engineering**  
**Midterm Exam**  
**Date: 25/04/2020**

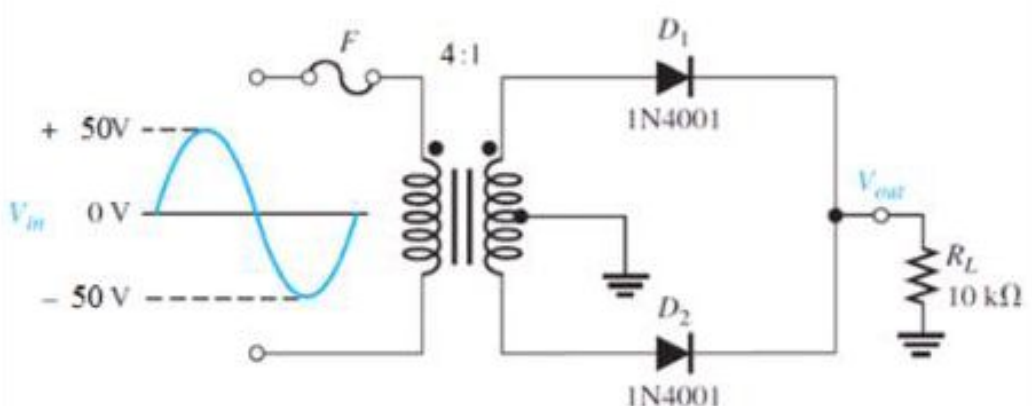
**Course Details**

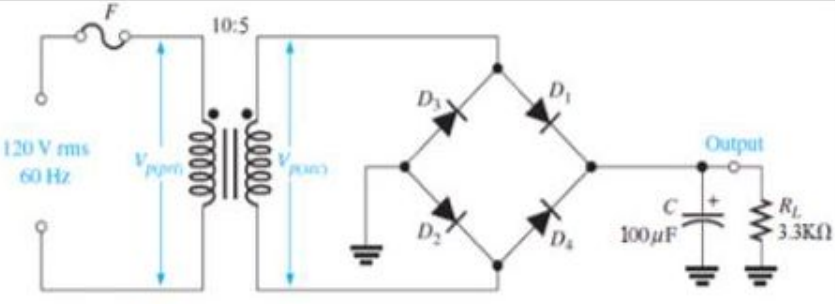
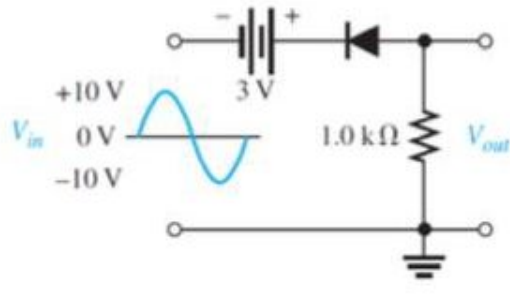
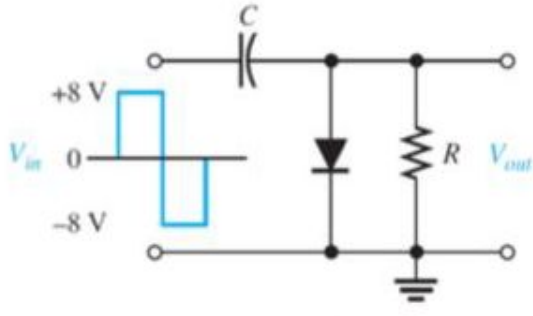
<b>Course Title:</b>	Electronic Devices and Circuits _____	<b>Module:</b>	_____
<b>Instructor:</b>	_____	<b>Total Marks:</b>	30

**Student Details**

**Name:** \_\_\_\_\_ **Student ID:** \_\_\_\_\_

**Student Signature:** \_\_\_\_\_

Q1.	<p><b>For the circuit given in figure 1, answer and solve following problems.</b></p> <ol style="list-style-type: none"> <li>a) What type of circuit is this? (1)</li> <li>b) What is the total peak secondary voltage? (1)</li> <li>c) Find the peak voltage across each half of the secondary. (1)</li> <li>d) What is the peak current through each diode? (2)</li> <li>e) What minimum PIV rating must the diodes have? (2)</li> </ol>	Marks 07 CLO 02
 <p style="text-align: center;"><b>Figure 1</b></p>		
Q2.	Determine the ripple factor for the filtered bridge rectifier with a load as indicated in Figure 2	Marks 05 CLO 02

	 <p style="text-align: center;"><b>Figure 2</b></p>	
Q3.	<p>Determine the output voltage waveform for the circuit given in Figure 3</p>  <p style="text-align: center;"><b>Figure-3</b></p>	Marks 02 CLO 02
Q4.	<p>Determine the output voltage waveform for the circuit given in Figure 4. Assume the RC time constant is much greater than the period of the input.</p>  <p style="text-align: center;"><b>Figure-4</b></p>	Marks 02 CLO 02
Q5.	<p><b>Answer the following questions.</b></p> <ol style="list-style-type: none"> <li>What is a Power Supply Filter? Discuss its operation with help of a circuit diagram. (3)</li> <li>How are n-type and p-type semiconductors formed? (2)</li> <li>What is a diode limiter? What is the difference between a positive limiter and a negative limiter? (3)</li> </ol>	Marks 14 CLO 01

	<ol style="list-style-type: none"> <li>What component in a clamping circuit effectively acts as a battery? (1)</li> <li>When a 60 Hz sinusoidal voltage is applied to the input of a half-wave rectifier, what is the output frequency? (1)</li> <li>If the load resistance connected to a filtered power supply is decreased, what happens to the ripple voltage? (1)</li> <li>Discuss how diode limiters and diode clippers differ in terms of their function. (3)</li> </ol>	
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Instructor: Dr Shaher Yar Sir.

Date: 25/08/2020

Department: Electrical Engineering

Course Title: Electronic devices & circuits.

Q No 1:-

a):-

What types

of circuit is this?

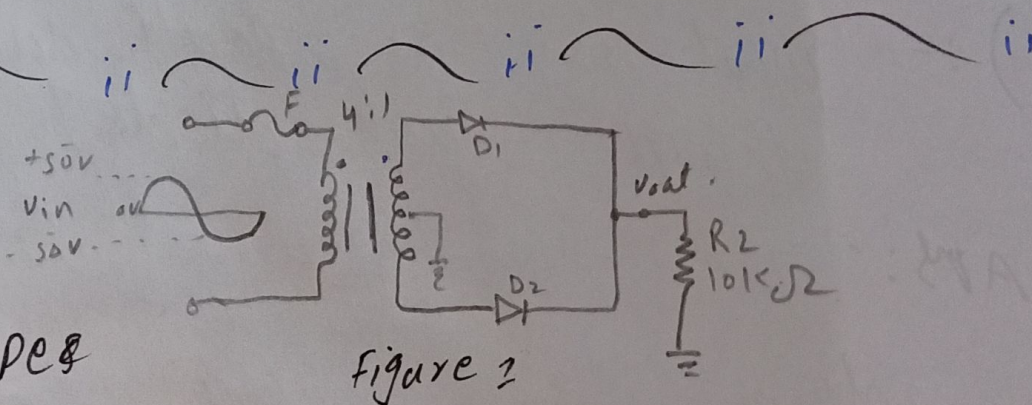


Figure 1

Ans:- The given figure circuit is center tapped Full-wave rectifier transformer circuit.

b):- What is the total peak secondary voltage?

Sol:- The transformer turns ratio,  $n = 0.25$   
 The total peak secondary voltage is.



$$V_{p(sec)} = n V_{p(prim)} = 0.25(50V) = \underline{12.5V}$$

c) :- Find the peak voltage across each half of the secondary?

Ans:- There is 6.25V peak across each half of the secondary with respect to ground.

d) :- What is the peak current through each diode?

Ans:- The peak current through each diode is

$$I_{peak} = \frac{6.25V}{0.7} = \frac{8.9A}{10k\Omega} = 0.00089A = \underline{0.89mA}$$

e) :- What minimum PIV rating must the diode have?

Ans:- Each diode must have a minimum PIV rating of

$$PIV = 2 V_{p(peak)} + 0.7 = 2(5.55V) + 0.7 = \underline{11.8V}$$



Q NO 2 :-

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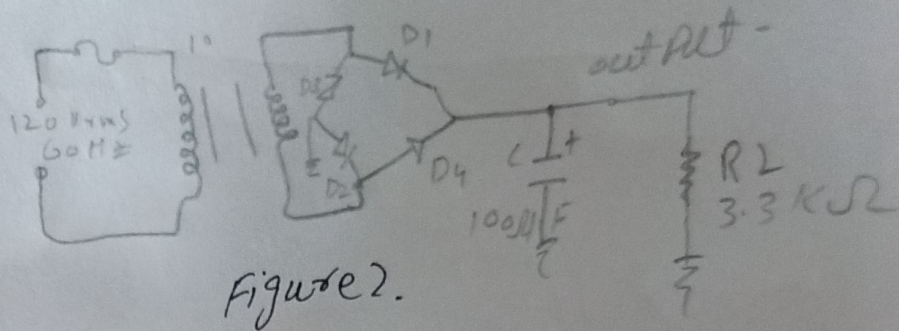


Figure 2.

Sol:- The transformer turns ratio  $n = 0.5$

The peak pri voltage is

$$V_{p(\text{pri})} = 1.414 V_{\text{rms}} = 1.414(120) = 170 \text{ V}$$

The peak secondary voltage is

$$V_{p(\text{sec})} = n V_{p(\text{pri})} = 0.5(170) = 85.0 \text{ V}$$

The unfiltered peak full-wave rectified voltage is

$$V_{p(\text{rect})} = V_{p(\text{sec})} - 1.4 \text{ V} = 85.0 \text{ V} - 1.4 \text{ V} =$$

$$83.6 \text{ V}$$

The frequency of a ~~at~~ Full-wave rectified voltage is  $120 \text{ Hz}$  the approximate peak to peak ripple voltage at the output is

$$V_{r(\text{pp})} \approx \left( \frac{1}{f R L C} \right) V_{p(\text{rect})} = \left( \frac{1}{(120 \text{ Hz})(3.3 \text{ k}\Omega)(100 \mu\text{F})} \right) \times 83.6 \text{ V}$$

$$\times 83.6 \text{ V}$$



$$= \left( \frac{1}{(120 \text{ Hz})(3300 \Omega)(0.0001 \text{ F})} \right) 83.6 \text{ V}$$

$$= 2.11 \text{ V}$$

The approximate dc value of the output voltage is determine as follows.

$$V_{DC} = \left( 1 - \frac{1}{2fRLC} \right) \Phi$$

$$= \left( 1 - \frac{1}{(240 \text{ Hz})(3300 \Omega)(0.0001 \text{ F})} \right) 83.6 \text{ V}$$

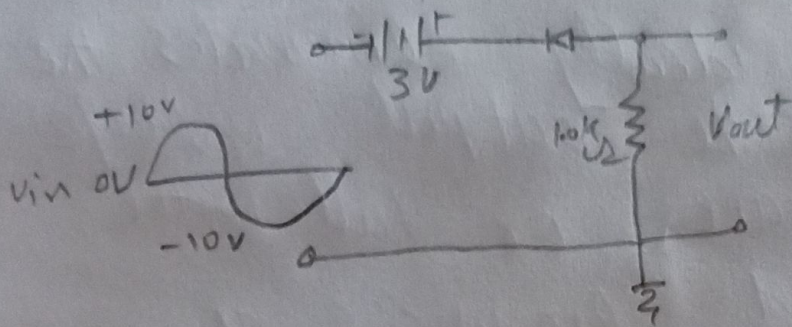
$$= 83.48 \text{ V}$$

The resulting ripple factor is

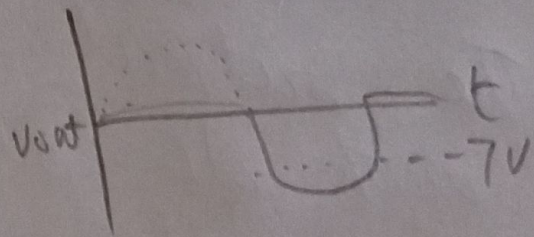
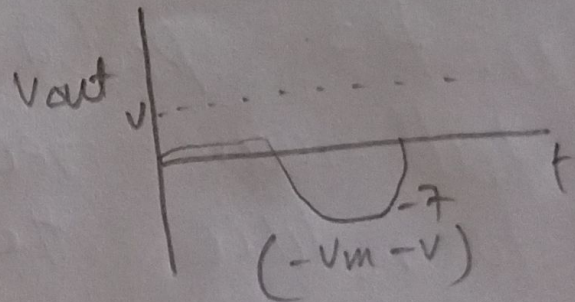
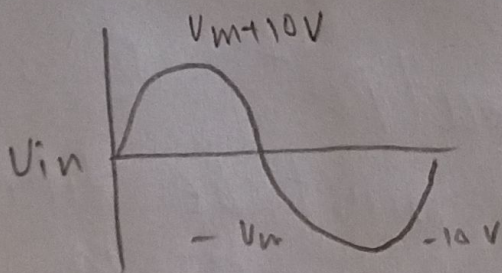
$$\gamma = \frac{V_r(\text{PP})}{V_{DC}} = \frac{2.11 \text{ V}}{83.48 \text{ V}} = 0.025$$



Q No 3:- determine the output voltage waveform for the circuit given in Figure?



ANS:- The output voltage waveform of clipper circuit is



Q No 5:-

a):- what is power supply filter? Discuss its operation with help of a circuit diagram.



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Ans:- Power Supply Filters:- A power

supply filter ideally eliminates the fluctuations in the output voltage of a half-wave or full-wave rectifier and produces a constant-level dc voltage.

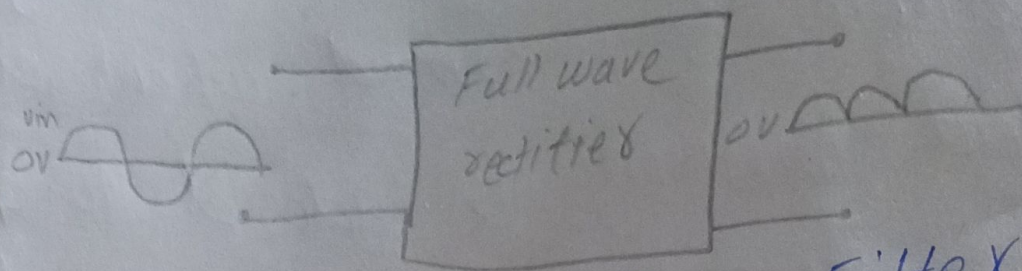
The 50 Hz pulsating dc output of a half-wave rectifier or the 120 Hz pulsating output of a full-wave rectifier must be filtered to reduce the large voltage variations.

The output of a filter is nearly smooth dc output voltage.

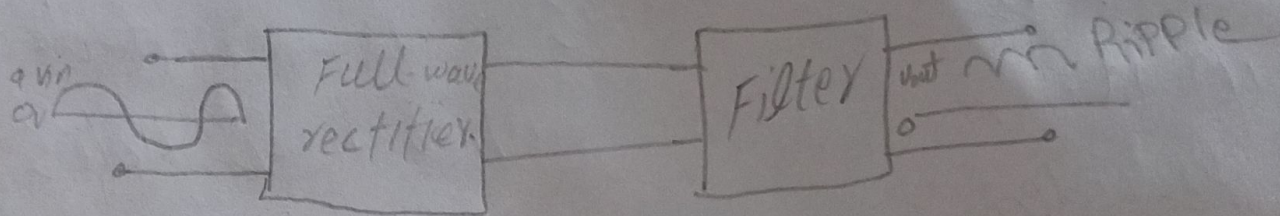
The small amount of fluctuation in the filter output voltage is called ripple.



Diagram :-



Rectifier without a Filter.



Rectifier with a Filter.

b) :- How are n-type & p-type Semiconductors formed?

Ans :- n-type semiconductor :-

To make n-type semiconductor, pentavalent impurities like phosphorus or arsenic are added. Four of the impurities electrons form bonds with the surrounding silicon atoms. Since electrons are negative charge carriers, the resultant material is called an n-type semiconductor.



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P-type semiconductor:-  
the extrinsic P-type semiconductor is formed when a trivalent impurity is added to a pure semiconductor in a small amount and as a result, a large number of holes are created in it. A large number of holes provided in the semiconductor by the addition of trivalent impurities like Gallium and Indium.

c) what is diode limiter? what is the different b/w positive limiter & negative limiter.

Aus:- Diode Limiter:- The diode clipper, also

known as a diode limiter, is a wave shaping circuit that takes an input waveform and clips or cutoff its top half, bottom half or both halves together.



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Different b/w Positive & Negative Limiter:

The series negative limiter limits the negative portion of the output pulse. The difference b/w a series negative limiter and a series positive limiter is that the diode is reversed in the negative limiter and in the parallel positive limiter the positive portion of the input signal is limited when the diode conducts.

d): - What component in a clamping circuit effectively act as a battery?

Ans: - As  $V_{in}$  becomes negative, the capacitor act as a battery of the same voltage of  $V_{in}$ . The voltage source and the capacitor counteract each other, resulting in a net voltage of zero as seen by the load.



e):- when a  $60\text{ Hz}$  sinusoidal voltage is applied to the input of a half-wave rectifier what is the output frequency?

Ans:- The output frequency of half-wave rectifier is equal to as input this means as input complete one cycle at output also complete one cycle.

f):- if the load resistance connected to a filtered power supply is decreased what happens to the ripple voltage?

Ans:- when the load resistance connected to a filtered power supply the ripple voltage remain same.



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Q) Discuss how diode limiter and diode clammers differ in terms of their function.

Ans:- The major difference between clipper and clamper is that the clipper is a limiting circuit which limits the output voltage while clamper is a circuit which shifts the DC level of output voltage. The clipper and clamper circuit are exactly opposite to each other regarding the work principle.

Q No 4:-

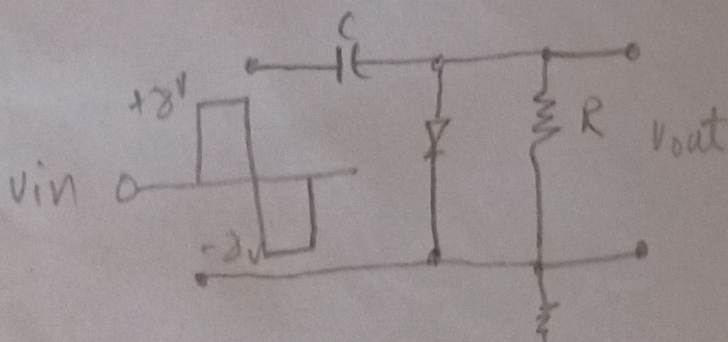


Figure 4.



Ans:- Assume the RC time constant is much greater than period of input

So if we take  $(\tau_{discharge} \geq 5T/2)$  thus

we obtain condition for the clamping operation.

$$\tau_{discharge} \geq 5T$$

So  $V_o = v_i - V + V_p(ow)$   $\therefore T$  is the period of input signal  $v_i$

