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Subject	DLD
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Assignment	7 <sup>th</sup>

Q1)

A register is an electronic device consists of a series of flip-flops to store data bits and moving the data bits.

The length of the stored binary words depends on the number of flip-flops that make up the register.

Q2

The storage capacity of a register that can retain one byte of data is 8 bits.

This particular register will be an 8 stage shift register.

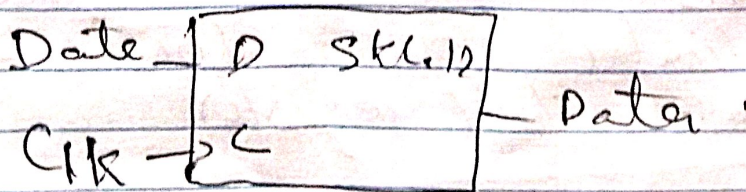
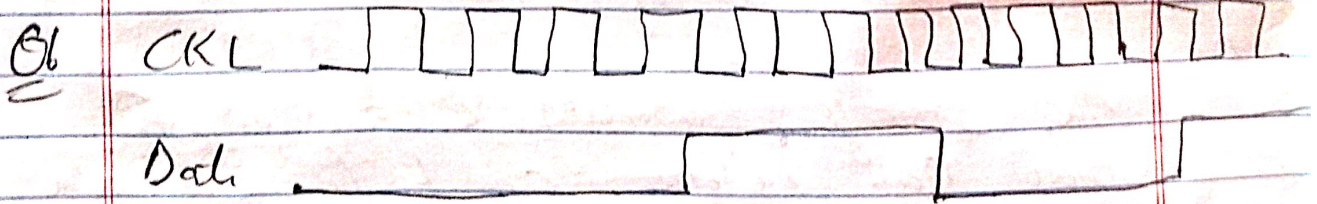
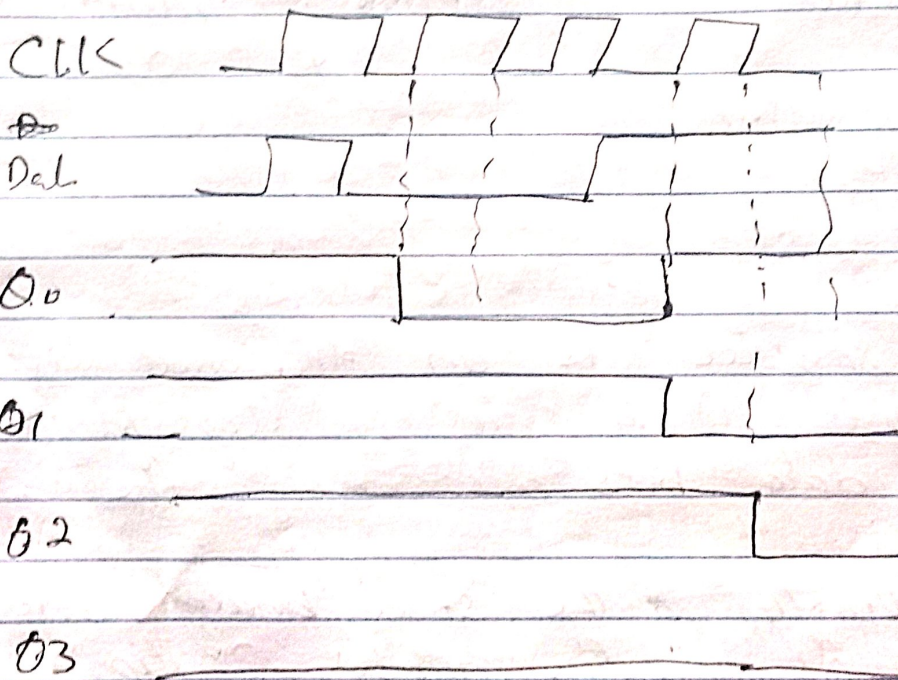
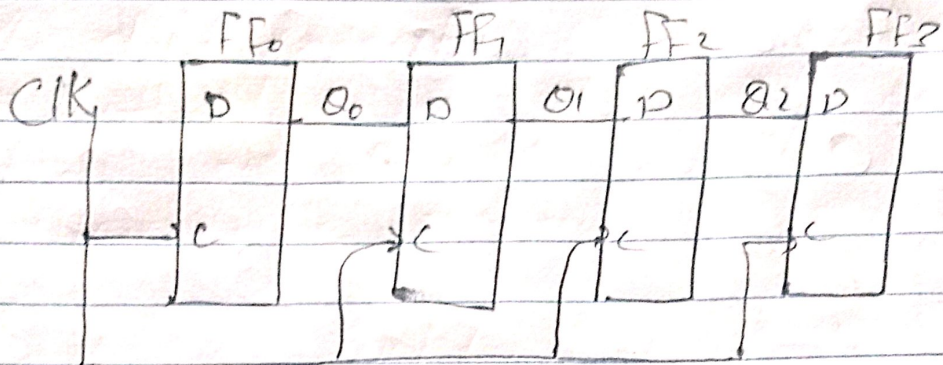
Q3

The shift capacity of a register permits to store and move data from one stage to another within, into or out of register.

Q7) Since the shift register is initially cleared.

initially	0000
clk <sub>1</sub>	1000
clk <sub>2</sub>	1100
clk <sub>3</sub>	0110

Q5/



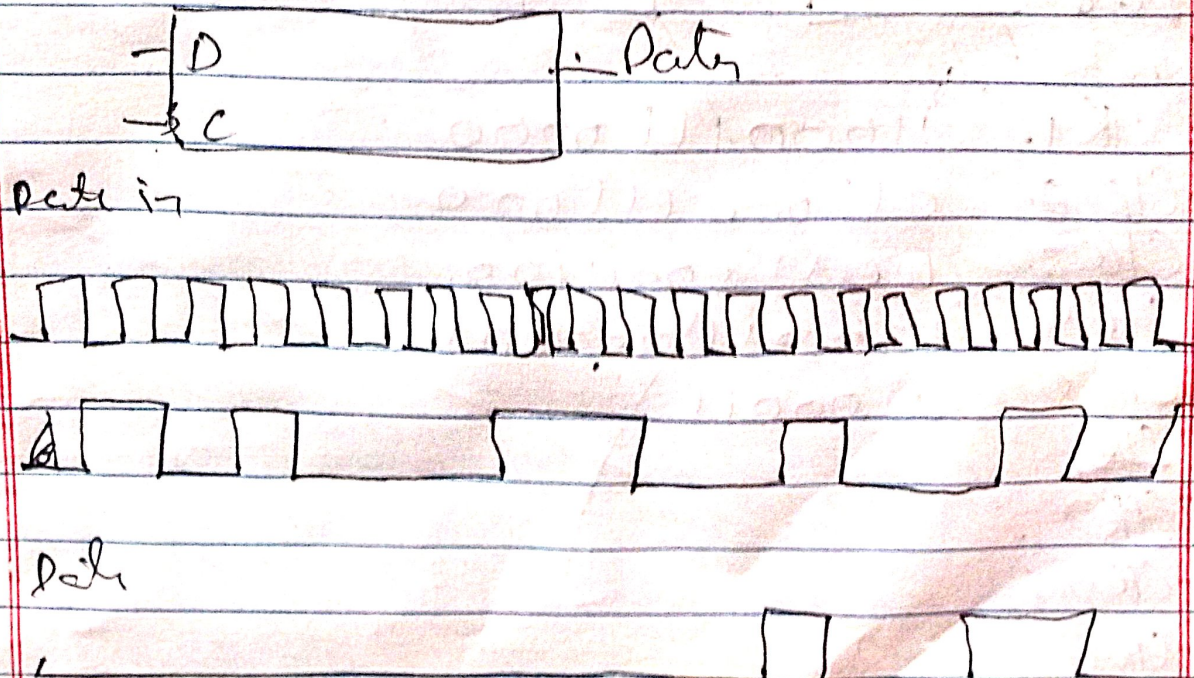
Day: MTWTFPS

(4)

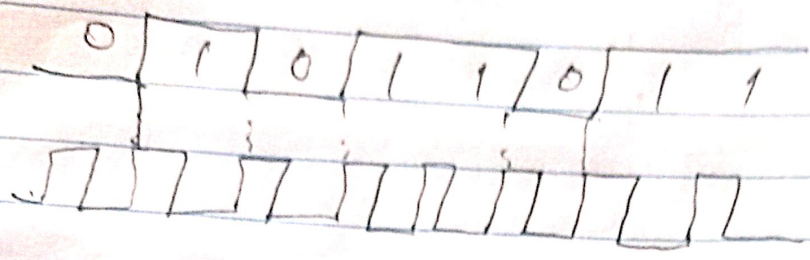
Date: \_\_\_/\_\_\_/\_\_\_

initially	110001110000
CLK 1	011000111000
CLK 2	001100011100
CLK 3	0001100011100
CLK 3	0000110000111
CLK 4	100001100011
CLK 5	110000110001
CLK 6	1110000110000
CLK 7	011100001100
CLK 8	0011100000110
CLK 9	000011000001
CLK 10	100011000001
CLK 11	110001110000
CLK 12	011000111000

Q7/



Q8



The data bit stored all  
11011010

Q9

