

FAWAI) AHMAI)

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ECD

Q. (a)

Discuss the darlington connection for multistage amplifiers

ANS:-

A very popular connection of two bipolar junction transistors for operations as one 'superbeta' transistor is the Darlington connection. The main feature of the Darlington connection is that the composite transistor acts as a single unit with a current gain that is the product of the current gains of the individual transistors. If the connection is made using two separate transistors having current gain of  $\beta_1$  and  $\beta_2$  the Darlington connection provides a current gain

$$\beta_D = \beta_1 \beta_2$$

Q. (b)

The input of a  
in %V.

Soln-

$$i) \text{ Line Reg} = \frac{0.062}{42} \times 100\%$$

$$= 1.37\%$$

$$ii) \text{ Line Regulation in \%V} \Rightarrow$$

$$= \frac{0.062}{40} \times 100\%$$

$$4.5$$

$$= 0.034\%V$$

Q2

Explain Colpitts and Hartley oscillators.

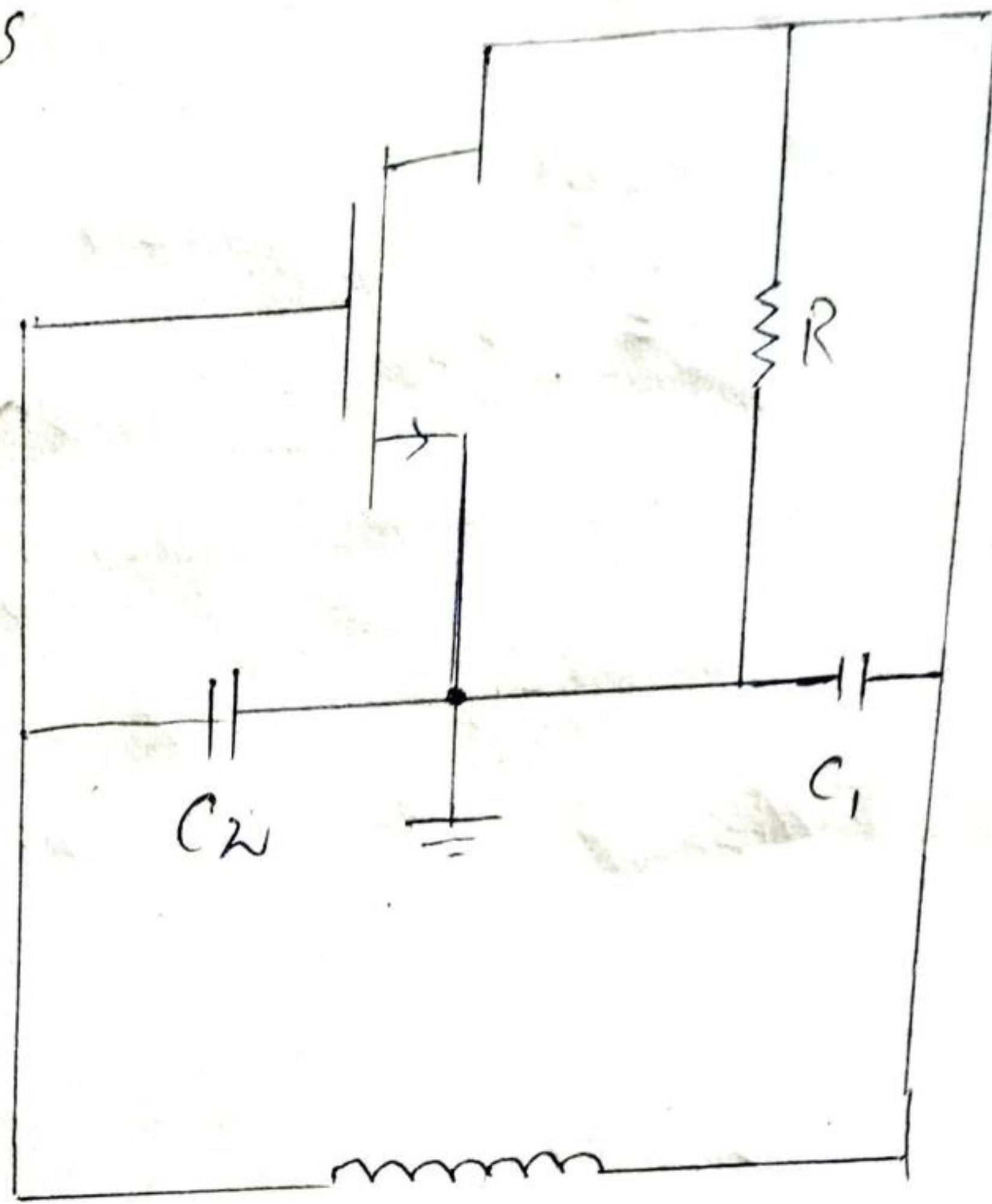
ANS:-

COLPITTS OSCILLATOR:-

The Colpitts oscillator is a type of oscillator that uses an LC circuit in the feed-back loop.

The feedback network is made up of a pair of tapped capacitors ( $C_1$  and  $C_2$ ) and an inductor  $L$  to produce a feedback necessary for oscillations. The ~~feedback~~ output voltage is developed across  $C_1$ . The feedback voltage is developed across  $C_2$ .

COLPITTS

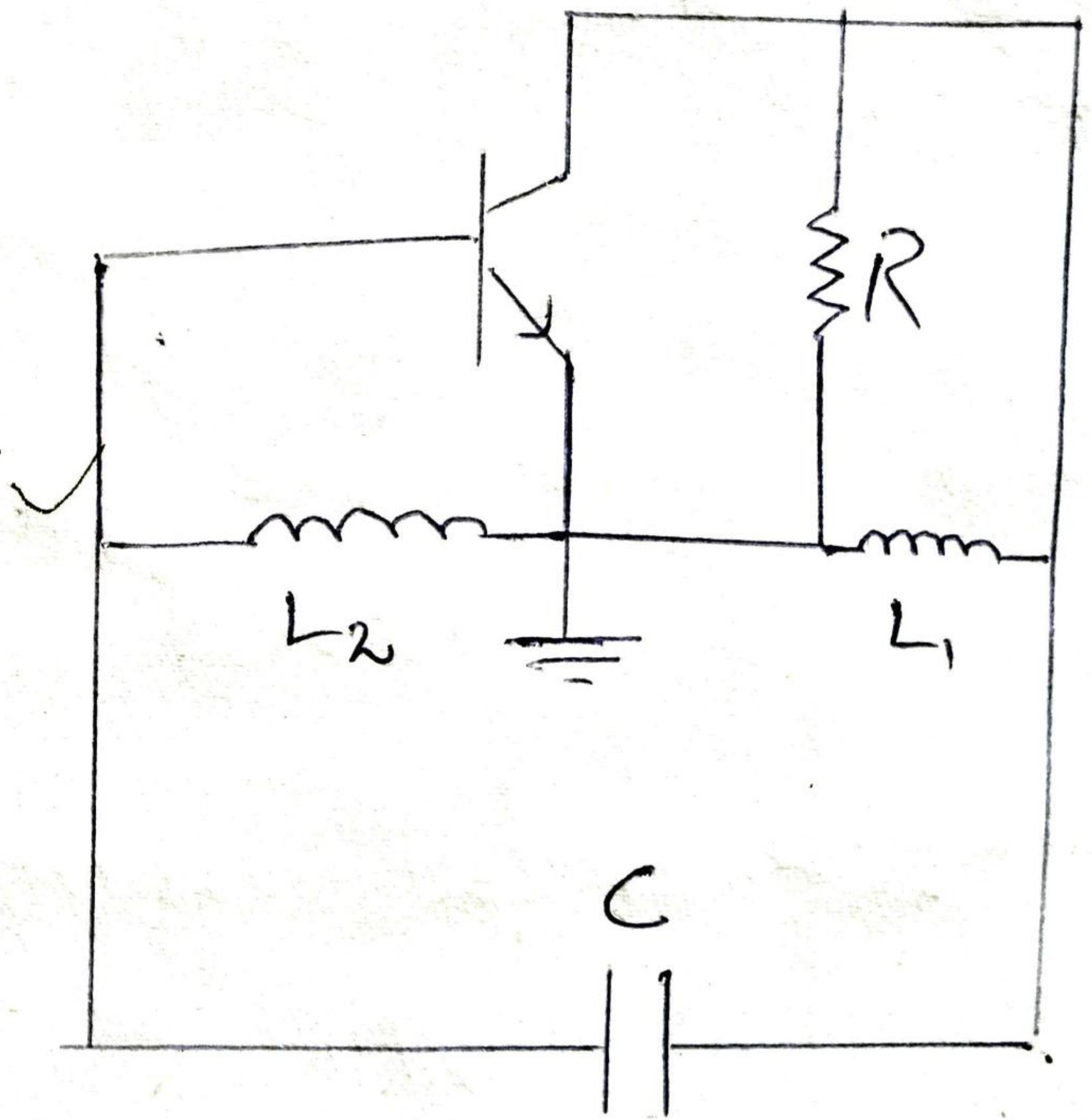


HARTLEY OSCILLATOR:-

The Hartley Oscillator is almost identical to the Colpitts Oscillator. The primary difference is that the feedback network of the Hartley oscillator uses tapped inductors ( $L_1$  and  $L_2$ ) a single capacitor  $C$ . The analysis of Hartley oscillator is identical to that Colpitts oscillator. The frequency of oscillations

$$\omega_0 = \frac{1}{\sqrt{(L_1 + L_2)C}}$$

HARTLEY



Ans. Class B amplifier is a type of power amplifier where the active device conducts only for one half cycle of the input signal. That means the conduction angle is  $180^\circ$  for a Class B amplifier. Since the active device is switched off for half the input cycle, the active device is switched off for half the input cycle, the active device dissipates less power and hence the efficiency is improved.

Theoretical maximum efficiency of Class B power amplifier is 78.5%. The schematic of a single ended Class B amplifier and input, output waveforms.

Q<sub>3</sub><sup>(b)</sup>

Explain the types of voltage regulators and their purpose.

Ans:

There are two types of voltage regulators.

1) SERIES VOLTAGE REGULATORS:-

A series voltage regulator uses a variable element placed in series with the load. By changing the resistance of that series element the voltage dropped across it can be changed.

2) SHUNT VOLTAGE REGULATORS:-

A shunt voltage regulator works by providing a path from the supply voltage to ground through a variable resistance. The current through the shunt regulator has diverted away from the load and flows uselessly to the ground making this form usually less efficient than the series regulator.



## PURPOSE OF VOLTAGE REGULATOR:-

The Regulator is separate set of components and function inside the unit. The regulator function is very simple in most motorcycle regulators. A regulator control circuit monitors battery voltage and compares this measurement to an internal reference voltage. Motorcycle regulators are generally designed to allow a maximum battery voltage of 14.6V at the battery while charging.

Q4

Explain the working of Flash ADC.

ANS:-

A flash ADC is a type of analog-to-digital converter that uses a linear voltage ladder with a comparator to each 'rung' of the ladder to compare the input voltage to successive reference voltage. Often these reference ladders are constructed of many resistors; however modern implementations show that capacitive voltage division is also possible. The output of these comparators is generally feed into a digital encoder, which converts the input into a binary value.

Q5 (a) Differentiate between the following

ANS LOW PASS AND HIGH PASS

### FILTERS:-

The circuit of low pass filter can be built with a resistor as well as a capacitor in series so that the output can be achieved. One the input is given to the circuit of the low pass filter. Then the resistance will give a stable obstacle, however the capacitor position will have an effect on the output signal.

An high pass filter blocks the low-frequency signals and allows just high frequency signals for flowing through it. Even though it provides reduction to high-frequency signal also however the attenuation issue is so little that it can be ignored.

Q5(b) Active and passive filters.

ANS:- ACTIVE FILTER:-

Active filters have the capability of amplifying filter output, while passive filters consume the power of the input signal and cannot amplify the output signal.

PASSIVE FILTERS:-

Passive filters are designed using capacitors, resistors, and inductors while active filters do not use inductors in their design.

This results in a compact design of active filter as compared to passive filters.

Active filters need outside sources for their operation, while passive filters do not need any outside source for their operations.

Q6:-

Sol:-

GIVEN THAT

$$A_{ol} = 400,000$$

$$A_{cm} = 0.6$$

Required : CMRR = ?

$$CMRR = \frac{A_{ol}}{A_{cm}}$$

$$= \frac{400,000}{0.6}$$

$$= 666,666.6$$

Expressed in decibels

$$CMRR = 20 \log (666,666.1)$$

$$= 116.47 \text{ dB}$$

Q7(a)

ANS:-

Using an op-amp with negative feedback is that the actual voltage gain of the op-amp doesn't matter so long as it's very large. If the op-amp's differential gain were 250,000 instead of 200,000 all it would mean is that the output voltage would hold just a little closer to  $V_{in}$ . In the circuit just illustrated, the output voltage would still be equal to the non-inverting input voltage. Op-amps gains therefore do not have to be precisely set by the factory in order for the circuit designer to build an amplifier circuit with precise gain. Negative feedback makes the system self-correcting.