Department of Electrical Engineering Mid – Term Assignment Spring 2020 Date: 13/04/2020

Course Details

Course Title:	Computer Communication Network	Module:	06
Instructor:	Engr Muhammad Waqas	Total Marks:	30

Student Details

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Q1.	(a)	1 topology has unidirectional movement of traffic.	Marks 14	
X	(4)	 Set of rules that govern communication is called 	CLO 1	
		3 of a network is the frequency of failure and network recovery time after a failure is		
		measured.		
		4. ASK, PSK, FSK and QAM are all examples of modulation.		
		5. Data synchronization is a function related with layer.		
		6. The layer changes bits into electromagnetic signals.		
		7. The information to be communicated in a network is called the		
		8 topology requires the maximum number of I/O ports.		
		9. A signal that repeats itself is a signal.		
		10. A 56k modem can download at a rate of Kbps and upload at a rate of		
		Kbps.		
		11. In mesh topology, if there are five nodes then there will be links.		
		12. When data is transmitted from device A to device B using internet model, the header from A's layer		
		4 is read by B's layer.		
		13. A device will convert an analog signal to a digital signal.		
		14 is the collection of all the component frequencies.		
Q2.	(a)	1. How are frames different from packets? Explain with examples.	Marks 10	
		2. A phone line being analog can we send digital data on phone lines? Support your answer with examples.	CLO 1	
		3. Give some details about fault tolerance, which network topologies have fault tolerance capability?		
		4. How is logical addressing different from physical addressing? Support your answer with examples.		
		5. A local telephone company wants to connect the LANs in all its offices throughout a city. For this		
		case which network category would be used?		
Q3.	(a)	Consider the following network, how many hops will it require for data to reach from node A to node J.	Marks 04	
		Intermediate system	CLO 1	
		End system		
		Intermediate C D		
		Link B Intermediate End system		
		A system		
		E F		
		Intermediate End system		
		Link Link		
		Intermediate End system G H		
		system		
	(h)	A Sine wave has a frequency of 135 Hz. What is its period?	Martra 02	
	(b)	A sine wave has a frequency of 155 rfz. what is its period?	Marks 02 CLO 1	
1				

Submitted To . Ergr cir Wagas paper CCN Solution = Pose (1) JD 13171 QNO 1) (0) (1) Ring (2) Protocol Reliabplity (3) (4) Digital - to-Anolog (5) physical layer (6) physical 5 (7) Message (8) Mesh (9) Pegiodic 56.6, 33.6 (10) 10 (11) Transport layer (12) ADL (Convertor) (13) frequency Spectrum (14) Onlog (9) 1. HOW are frames differents from packets? Explain With example. Frame og Ans) An information unit Whose Source and declimation are data Imk layer entities Composed of the data link layer header (trailer) and upper layer · data ·

TP 13171 (02) Page No Frame Data lenk layes data link uppor layer data header lager trailer Packet : A An information unit klhose vource and destination are network layer entities. A Composed of the network layer header (frailer) and upper-layer data. Packet upper layer Network layes Nelwork layer dale header trailer QNO2 (R) A phone line being amplog Can ble cond digital data on phone lines? Support your mereles 12/19th example.

Page NI0 03 ID, 13171 Ver we send analog data over prome Yes we send digital data on phone lines in Two ways Examples Use the phone company's switched network for Dow speed modern traffic We dedicated ensuit that can -junction at high speed or more Cess independently of the notwork switching system

Page No oy ID 13171 The 3 M(3) Give Some detail about fault tolerance , tallich metholak topologies have fault blesance Capability? Ans) Fault toleRonce a foull toleforce PS the property that anables a system to Continue operating properly in the event of the failuse of (or one or more faults Wilkin) Some of its Components. * Mesh topologies have fault tolefame Capability Even if one of the Component fails these is always on alternative Present. So daity transfer gloesnil get affected QN(2) (a)(1) How is logical addressing different from physical addressing ! Support your answer with example.

page No (05) ID 13171 * logical addressing : 2 Logical addressing is generated by cpu While a piogram is sunning the logical address is Virtual address as it does not exist physically. Herefore, if is also known as Wishual address. Thes address is used as a reference to alless the physical memory location by CPY the learn logical address Space is use for the del of all losical addresses generated by a programis perspective For enample: IF you use IP as the Network layer protocol, devices on the metwork are assigned IP addresses Such as 207.100.67.30 K ply sical addressing 2 2 Physical addressing Identifies a physical location of Required data in a memory othe useq nevel directly deal with the

Page No 06 ID. 13171 Physical address but can access by its Corresponding logical address. The User program generates the logical address and think that the program is rumming in this logical address but the program needs physical memory for the execution. For example. Etternet uses a 6-bote (48-bit.) Physical address: 4 \$ Onlos (5) A local telephone Compony Wants to Connect the LANS in all 1ts officies throughout a city for the Case Which metulork Calegory Would be used? Kle can Connect the Larts For all Ffs Ams) offices throughout a city through WAN (INPode Area network) because

Page No of TD 13171 KIAN Connect different Smaller metuliaks. Including local alea networks (LANS) and metro alea network (Manis) This ensure that Computers and Users in one location Can Communificate lalithe Computer and user in other locations. Inland implementation Can be done either With the Lefp of the public transmission System or a private methoriko. QN03 101 Consider the Following network how many hops will it Require For data to reach from mode A to mode I Ans) They have 3 hops will Required for data to Reach from mode A to J.

Page No 08 TO 13171 A Sine place has a frequency Oniol (6) ef 135 HZ - What is its period. Solution . F 2 135 HZ Repuired: T=? EZ 135HZ I2 1/4 2 1 135 0.0074 sec ILE END 12218 1 1 A