

Haroon Rashid

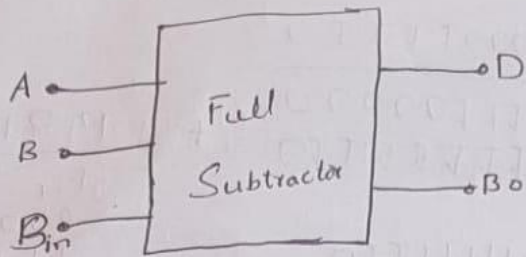
Reg# 16549

Semester 6<sup>th</sup>

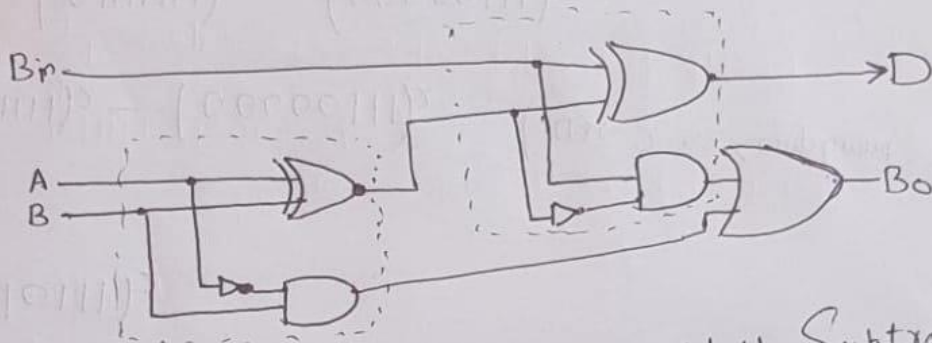
Assignment: DLD Lab

Submitted to: Sir MUHAMMAD AMIN

# Lab 5: Full Subtractor



Full Subtractor



Logic Circuit of Two Half Subtractor

Observation Table

Table 5.1

Inputs			Outputs	
A	B	Bin	D	Bout
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1