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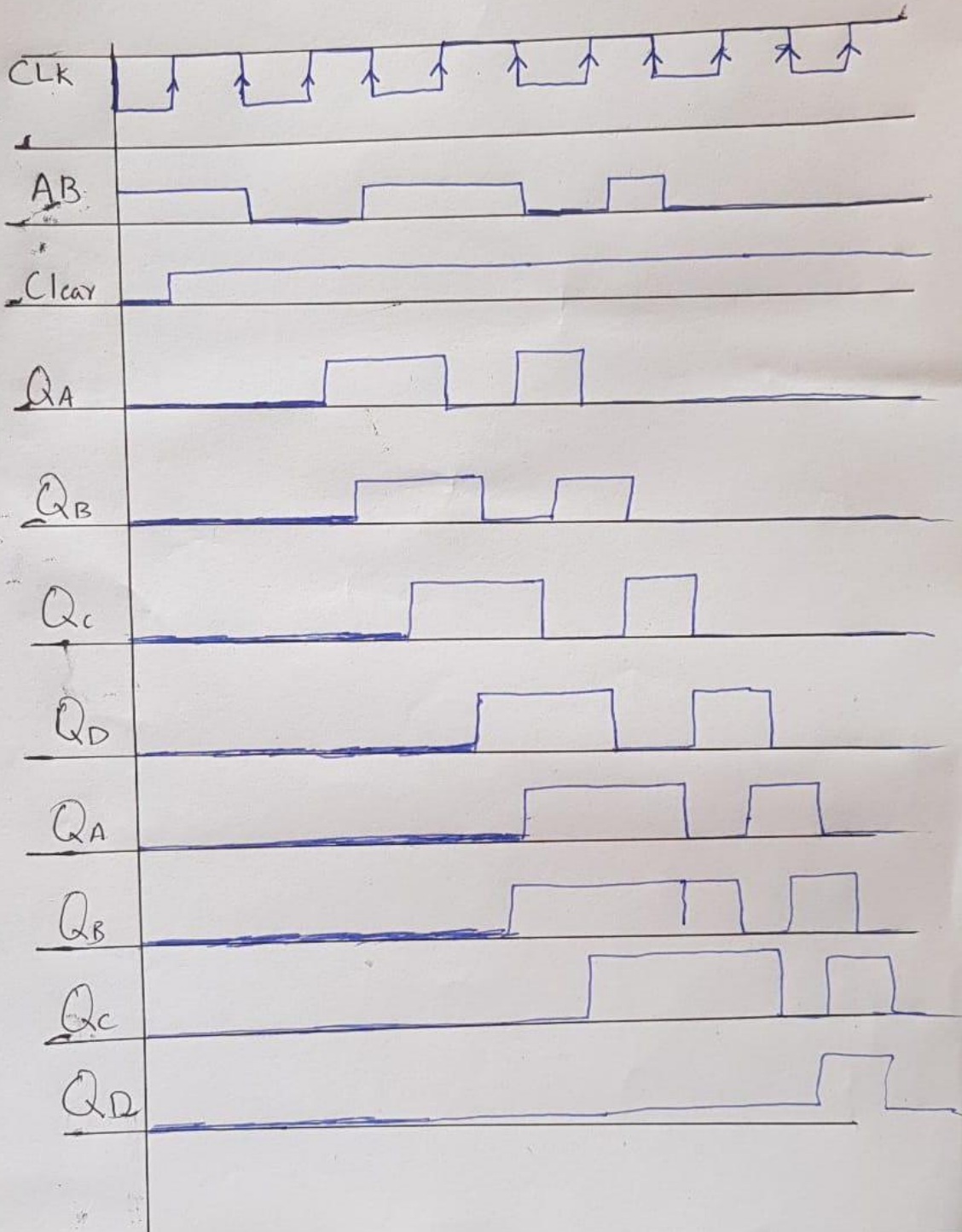
Semester: 6th

Subject: DLD Lab-10

Submitted to: Sir MUHAMMAD AMIN

Lab 12: Shift Register

Shift Time Register Diagram



Lab: 12 Shift Register 74194 IC Timing Diagram

